# SelfE on CPU Scheduling Answers

1. A
2. Completion of process, IO request, System call, Interrupt, Time quantum, Priority, Trap
3. Non-pre-emptive
4. Aging
5. Convoy effect, Time quantum
6. When data arrives to the address space of the process, it may have been swaped
7. STS- Selection of next process, Dispatcher: Doing context switch
8. A. SRT B. RR c. SRT d. FCFS e. FCFS f. Multilevel feedback queue g. RR
9. TT= CT – AT. RT=Commencement time – AT. WT= TT-BT
10. Multi-level feedback scheduling 2, 4, 8

    0-2 P1 2-4 P2 4-6 P3 6-8 P4

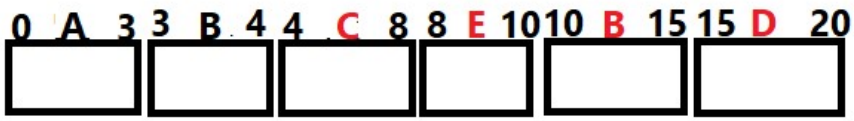
    8-12 P1 12-16 P2 16-17 P3 17-19 P4

    19-25 P1 25-27 P2

    **CT: P1 -25, P2-27, P3-17, P4-19**

| P1 | P2 | P3 | P4 |
|---|---|---|---|
| P1(10) | P2(6) | P3(1) | P4(2) |
| P1(6) | P2(2) | P3(0) | P4(0) |
| P1(0) | P2(0) | | |

11. SRT




12. None
13. RR TQ=2

| Process | P1 | P2 | P3 | P4 |
|---|---|---|---|---|
| CT | 22 | 12 | 26 | 23 |

| | | | |
|---|---|---|---|
| 0-2 P1 | P2(4) | P3(9) | |
| 2-4 P2 | P3(9) | P1(6) | P4(5) |
| 4-6 P3 | P1(6) | P4(5) | P2(2) |
| 6-8 P1 | P4(5) | P2(2) | P3(7) |
| 8-10 P4 | P2(2) | P3(7) | P1(4) |
| 10-12 P2 | P3(7) | P1(4) | P4(3) |
| 12-14 P3 | P1(4) | P4(3) | |
| 14-16 P1 | P4(3) | P3(5) | |
| 16-18 P4 | P3(5) | P1(2) | |
| 18-20 P3 | P1(2) | P4(1) | |
| 20-22 P1 | P4(1) | P3(3) | |
| 22-23 P4 | P3(3) | | |
| 23-26 P3 | P3 | | |

14. CPU and IO burst times

| Process | A | B | C | D | E |
|---|---|---|---|---|---|
| AT | 0 | 2 | 3 | 5 | 7 |
| BT1 | 3 | 2 | 4 | 6 | 2 |
| I/O | 3 | 4 | 3 | 2 | 4 |
| BT2 | 2 | 3 | 1 | 3 | 5 |

| | A | B | C | D | E |
|---|---|---|---|---|---|
| AT | 6 | 9 | 12 | 17 | 23 |
| BT | 2 | 3 | 1 | 3 | 5 |

WT = TT - (BT1+IO+BT2)

| | A | B | C | D | E |
|---|---|---|---|---|---|
| CT | 17 | 22 | 23 | 26 | 31 |
| TT | 17 | | | | |
| WT | 9 | | | | |