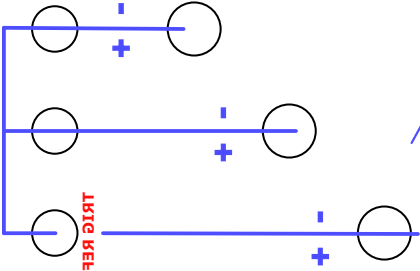


Logic

INPUTS



OUTPUT

