in

VCA



 \oplus CV

β (+)

(+)out

+

aux

gate

vel

+ 1V/oct

VCA

in

(+)

 \oplus CV

β +

(+)out

+

+

VCA

in

+

 \oplus CV

β (+)

(+)out

in

VCA

+

+ CV

β (+)

(+)

out

AD/R

+

trig

+ gate

+ out

+ gate + out

AD/R

VCA

in

+

β

+

out

(+)

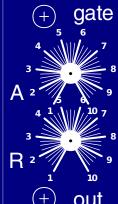
+ trig + trig

> + gate + out

AD/R



+ trig



+ out