Date Tech	Gain (dB)	6/6/2011 JM										
Step Ga 1 2 3 4 5 6	Gain (dR)	JM			,					ļ.	ļ.	1 I
1 2 3 4 5 6	Gain (dR)				<u> </u>							
1 2 3 4 5 6	Gain (dR)				<u> </u>							
2 3 4 5 6	Sairi (GD)	Step (dB)	Std Lvl (dBu)	THD+N %	20Hz (dBu)	20kHz (dBu)	-3dB (Hz)	Noise (dBu)	SNR (dB)	EIN (dBu)	Max In (dBu)	1% Lvl (dBu)
3 4 5 6	14.6	14.6	4.1	0.0007	-0.1	-0.1	123350	-100.5	-132.5	-115.1	17.4	32.0
4 5 6	19.6	5.0	4.1	0.0008	-0.1	-0.1	123350	-99.8	-131.8	-119.4	12.5	32.0
5 6	24.6	5.0	4.1	0.0009	-0.1	-0.1	123350	-98.6	-130.5	-123.1	7.5	32.0
6	29.3	4.8	4.1	0.0012	-0.1	-0.1	123350	-96.6	-128.6	-126.0	2.7	32.0
	34.4	5.1	3.9	0.0018	-0.1	-0.1	123350	-93.7	-125.7	-128.1	-2.4	32.0
7	39.3	4.9	4.1	0.0029	-0.1	-0.1	123350	-90.2	-122.2	-129.5	-7.3	32.0
	44.4	5.1	3.9	0.0052	0.0	-0.1	123350	-85.6	-117.6	-130.1	-12.4	32.0
8	49.4	4.9	4.0	0.0087	0.0	-0.1	123350	-81.0	-113.0	-130.4	-17.3	32.0
9	54.2	4.8	4.0	0.0151	-0.1	-0.1	123350	-76.5	-108.5	-130.7	-22.2	32.0
10	58.7	4.5	4.0	0.0248	0.0	-0.1	117275	-72.1	-104.1	-130.8	-26.7	32.0
11	63.8	5.1	4.1	0.0440	0.0	-0.1	117275	-66.9	-98.9	-130.8	-31.8	32.0
12	68.3	4.5	4.1	0.0751	0.0	-0.1	105975	-62.5	-94.5	-130.8	-36.3	32.0
					<u> </u>							
			On	Off	1							
Phantom Voltage		Pin 2 to Pin 1	46.1vdc	0vdc	<u> </u>			Pin 2 to Pin 1	0vdc			
		Pin 3 to Pin 1	46.1vdc	0vdc	<u> </u>	DC Output		Pin 3 to Pin 1	0vdc			
		Pin 2 to Pin 3	0vdc	0vdc				Pin 2 to Pin 3	0vdc			
					 			_				
					 	B+	24vdc		24vdc			
				1	,	Phase Switch		Difference	0dBu	i i	1	l l