Schematic components and labels:

IC5B 4070D — pins 5, 6, 4

IC5C 4070D — pins 8, 9, 10

IC5D 4070D — pins 12, 13, 11

IC6B 4077D — pins 5, 6, 4

IC6A 4077D — pins 1, 2, 3

IC6D 4077D — pins 12, 13, 11

GND

IC1 4068D — Q1-Q8_OUTPUT
- N$16 (1), N$15 (2), N$17 (3), N$18 (4), (5), (6), (7), (8)
- N$18 2, N$17 3, N$16 4, N$15 5, P$1 1, 13
- 9, 10, 11, 12

IC2 4068D — Q9-Q16-OUTPUT
- N$177 (1), N$178 (2), N$179 (3), N$180 (4), (5), (6), (7), (8)
- N$180 2, N$179 3, N$178 4, N$177 5, P$1 1, 13
- 9, 10, 11, 12

IC3 4068D — Q17-Q24_OUTPUT
- N$19 (1), N$20 (2), N$21 (3), N$22 (4), N$24 (5), N$25 (6), N$26 (7), N$27 (8)
- +12V 2, 3, 4, 5, P$1 1, 13
- N$19 9, N$20 10, N$21 11, N$22 12

IC4 4068D
- +12V
- 2, 3, 4, 5, P$1 1, 13
- N$24 9, N$25 10, N$26 11, N$27 12

IC5A 4070D — pins 1, 2, 3 — DATA_IN (1)

IC6C 4077D — pins 8, 9, 10

JP2 — pins 2, 1

Power supply section:

12V, +12V

IC5P 14 VDD 7 VSS, IC6P 14 VDD 7 VSS, IC1P 14 VDD 7 VSS, IC2P 14 VDD 7 VSS, IC3P 14 VDD 7 VSS, IC4P 14 VDD 7 VSS

GND

C44 0.1uF, C45 0.1uF, C46 0.1uF, C47 0.1uF, C2 0.1uF, C1 0.1uF, + C3

GND

Title block:

Project: Deadlock Protection Adapter Board
Author: Lorenz Schwarz

TITLE:

Document Number: | Version 1 | REV: A

Date: | Sheet: