

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
04	SET PROGRAM MASK	SPM	04	RR	L	7-382
05	BRANCH AND LINK	BALR	05	RR	⌵2,9 T B	7-35
06	BRANCH ON COUNT (32)	BCTR	06	RR	⌵9 B	7-41
07	BRANCH ON CONDITION	BCR	07	RR	⌵9 ⌵1 B	7-40
0A	SUPERVISOR CALL	SVC	0A	I	⌵1 ⌵	7-403
0B	BRANCH AND SET MODE	BSM	0B	RR	⌵3,9 T B	7-38
0C	BRANCH AND SAVE AND SET MODE	BASSM	0C	RR	⌵2,3,9 T B	7-36
0D	BRANCH AND SAVE	BASR	0D	RR	⌵2,9 T B	7-36
0E	MOVE LONG	MVCL	0E	RR	C ⌵9 A SP II ST R1 R2	7-293
0F	COMPARE LOGICAL LONG	CLCL	0F	RR	C ⌵9 A SP II R1 R2	7-159
10	LOAD POSITIVE (32)	LPR	10	RR	C IF	7-289
11	LOAD NEGATIVE (32)	LNR	11	RR	C	7-286
12	LOAD AND TEST (32)	LTR	12	RR	C	7-273
13	LOAD COMPLEMENT (32)	LCR	13	RR	C IF	7-275
14	AND (32)	NR	14	RR	C	7-32
15	COMPARE LOGICAL (32)	CLR	15	RR	C	7-153
16	OR (32)	OR	16	RR	C	7-315
17	EXCLUSIVE OR (32)	XR	17	RR	C	7-257
18	LOAD (32)	LR	18	RR		7-267
19	COMPARE (32)	CR	19	RR	C	7-136
1A	ADD (32)	AR	1A	RR	C IF	7-25
1B	SUBTRACT (32)	SR	1B	RR	C IF	7-399
1C	MULTIPLY (64 <- 32)	MR	1C	RR	SP	7-307
1D	DIVIDE (32 <- 64)	DR	1D	RR	⌵9 SP IK	7-255
1E	ADD LOGICAL (32)	ALR	1E	RR	C	7-29
1F	SUBTRACT LOGICAL (32)	SLR	1F	RR	C	7-401
20	LOAD POSITIVE (long HFP)	LPDR	20	RR	C ⌵7,9 Da	18-16
21	LOAD NEGATIVE (long HFP)	LNDR	21	RR	C ⌵7,9 Da	18-16
22	LOAD AND TEST (long HFP)	LTDR	22	RR	C ⌵7,9 Da	18-13
23	LOAD COMPLEMENT (long HFP)	LCDR	23	RR	C ⌵7,9 Da	18-14
24	HALVE (long HFP)	HDR	24	RR	⌵7,9 Da EU	18-13
25	LOAD ROUNDED (extended to long HFP)	LDXR	25	RR	⌵7,9 SP Da EO	18-17
26	MULTIPLY (extended HFP)	MXR	26	RR	⌵7,9 SP Da EU EO	18-17
27	MULTIPLY (long to extended HFP)	MXDR	27	RR	⌵7,9 SP Da EU EO	18-17
28	LOAD (long)	LDR	28	RR	⌵7,9 Da	9-31
29	COMPARE (long HFP)	CDR	29	RR	C ⌵7,9 Da	18-10
2A	ADD NORMALIZED (long HFP)	ADR	2A	RR	C ⌵7,9 Da EU EO LS	18-8
2B	SUBTRACT NORMALIZED (long HFP)	SDR	2B	RR	C ⌵7,9 Da EU EO LS	18-24
2C	MULTIPLY (long HFP)	MDR	2C	RR	⌵7,9 Da EU EO	18-17
2D	DIVIDE (long HFP)	DDR	2D	RR	⌵7,9 Da EU EO FK	18-12
2E	ADD UNNORMALIZED (long HFP)	AWR	2E	RR	C ⌵7,9 Da EO LS	18-9
2F	SUBTRACT UNNORMALIZED (long HFP)	SWR	2F	RR	C ⌵7,9 Da EO LS	18-25
30	LOAD POSITIVE (short HFP)	LPER	30	RR	C ⌵7,9 Da	18-16
31	LOAD NEGATIVE (short HFP)	LNER	31	RR	C ⌵7,9 Da	18-16
32	LOAD AND TEST (short HFP)	LTER	32	RR	C ⌵7,9 Da	18-13
33	LOAD COMPLEMENT (short HFP)	LCER	33	RR	C ⌵7,9 Da	18-14
34	HALVE (short HFP)	HER	34	RR	⌵7,9 Da EU	18-13
35	LOAD ROUNDED (long to short HFP)	LEDR	35	RR	⌵7,9 Da EO	18-17
36	ADD NORMALIZED (extended HFP)	AXR	36	RR	C ⌵7,9 SP Da EU EO LS	18-8
37	SUBTRACT NORMALIZED (extended HFP)	SXR	37	RR	C ⌵7,9 SP Da EU EO LS	18-24
38	LOAD (short)	LER	38	RR	⌵7,9 Da	9-31
39	COMPARE (short HFP)	CER	39	RR	C ⌵7,9 Da	18-10
3A	ADD NORMALIZED (short HFP)	AER	3A	RR	C ⌵7,9 Da EU EO LS	18-8
3B	SUBTRACT NORMALIZED (short HFP)	SER	3B	RR	C ⌵7,9 Da EU EO LS	18-24

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
3C	MULTIPLY (short to long HFP)	MDER	3C	RR	⌘7,9 Da EU E0	18-17
3D	DIVIDE (short HFP)	DER	3D	RR	⌘7,9 Da EU E0 FK	18-12
3E	ADD UNNORMALIZED (short HFP)	AUR	3E	RR	C ⌘7,9 Da E0 LS	18-9
3F	SUBTRACT UNNORMALIZED (short HFP)	SUR	3F	RR	C ⌘7,9 Da E0 LS	18-25
40	STORE HALFWORD (16)	STH	40	RX-a	A ST B2	7-395
41	LOAD ADDRESS	LA	41	RX-a		7-269
42	STORE CHARACTER	STC	42	RX-a	A ST B2	7-389
43	INSERT CHARACTER	IC	43	RX-a	A B2	7-265
44	EXECUTE	EX	44	RX-a	⌘9 AI SP EX	7-259
45	BRANCH AND LINK	BAL	45	RX-a	⌘9 B	7-35
46	BRANCH ON COUNT (32)	BCT	46	RX-a	⌘9 B	7-41
47	BRANCH ON CONDITION	BC	47	RX-b	⌘9 B	7-40
48	LOAD HALFWORD (32 <- 16)	LH	48	RX-a	A B2	7-278
49	COMPARE HALFWORD (32 <- 16)	CH	49	RX-a	C A B2	7-151
4A	ADD HALFWORD (32 <- 16)	AH	4A	RX-a	C A IF B2	7-28
4B	SUBTRACT HALFWORD (32 <- 16)	SH	4B	RX-a	C A IF B2	7-400
4C	MULTIPLY HALFWORD (32 <- 16)	MH	4C	RX-a	A B2	7-308
4D	BRANCH AND SAVE	BAS	4D	RX-a	⌘9 B	7-36
4E	CONVERT TO DECIMAL (32)	CVD	4E	RX-a	⌘9 A ST B2	7-233
4F	CONVERT TO BINARY (32)	CVB	4F	RX-a	⌘9 A Dg IK B2	7-232
50	STORE (32)	ST	50	RX-a	A ST B2	7-388
51	LOAD ADDRESS EXTENDED	LAE	51	RX-a	⌘6 U1 BP	7-269
54	AND (32)	N	54	RX-a	C A B2	7-33
55	COMPARE LOGICAL (32)	CL	55	RX-a	C A B2	7-153
56	OR (32)	O	56	RX-a	C A B2	7-315
57	EXCLUSIVE OR (32)	X	57	RX-a	C A B2	7-257
58	LOAD (32)	L	58	RX-a	A B2	7-267
59	COMPARE (32)	C	59	RX-a	C A B2	7-136
5A	ADD (32)	A	5A	RX-a	C A IF B2	7-26
5B	SUBTRACT (32)	S	5B	RX-a	C A IF B2	7-399
5C	MULTIPLY (64 <- 32)	M	5C	RX-a	A SP B2	7-307
5D	DIVIDE (32 <- 64)	D	5D	RX-a	⌘9 A SP IK B2	7-255
5E	ADD LOGICAL (32)	AL	5E	RX-a	C A B2	7-29
5F	SUBTRACT LOGICAL (32)	SL	5F	RX-a	C A B2	7-401
60	STORE (long)	STD	60	RX-a	⌘7,9 A Da ST B2	9-48
67	MULTIPLY (long to extended HFP)	MXD	67	RX-a	⌘7,9 A SP Da EU E0 B2	18-18
68	LOAD (long)	LD	68	RX-a	⌘7,9 A Da B2	9-31
69	COMPARE (long HFP)	CD	69	RX-a	C ⌘7,9 A Da B2	18-10
6A	ADD NORMALIZED (long HFP)	AD	6A	RX-a	C ⌘7,9 A Da EU E0 LS B2	18-8
6B	SUBTRACT NORMALIZED (long HFP)	SD	6B	RX-a	C ⌘7,9 A Da EU E0 LS B2	18-24
6C	MULTIPLY (long HFP)	MD	6C	RX-a	⌘7,9 A Da EU E0 B2	18-18
6D	DIVIDE (long HFP)	DD	6D	RX-a	⌘7,9 A Da EU E0 FK B2	18-12
6E	ADD UNNORMALIZED (long HFP)	AW	6E	RX-a	C ⌘7,9 A Da E0 LS B2	18-9
6F	SUBTRACT UNNORMALIZED (long HFP)	SW	6F	RX-a	C ⌘7,9 A Da E0 LS B2	18-25
70	STORE (short)	STE	70	RX-a	⌘7,9 A Da ST B2	9-48
71	MULTIPLY SINGLE (32)	MS	71	RX-a	A B2	7-310
78	LOAD (short)	LE	78	RX-a	⌘7,9 A Da B2	9-31
79	COMPARE (short HFP)	CE	79	RX-a	C ⌘7,9 A Da B2	18-10
7A	ADD NORMALIZED (short HFP)	AE	7A	RX-a	C ⌘7,9 A Da EU E0 LS B2	18-8
7B	SUBTRACT NORMALIZED (short HFP)	SE	7B	RX-a	C ⌘7,9 A Da EU E0 LS B2	18-24
7C	MULTIPLY (short to long HFP)	MDE	7C	RX-a	⌘7,9 A Da EU E0 B2	18-18
7D	DIVIDE (short HFP)	DE	7D	RX-a	⌘7,9 A Da EU E0 FK B2	18-12
7E	ADD UNNORMALIZED (short HFP)	AU	7E	RX-a	C ⌘7,9 A Da E0 LS B2	18-9
7F	SUBTRACT UNNORMALIZED (short HFP)	SU	7F	RX-a	C ⌘7,9 A Da E0 LS B2	18-25

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
80	SET SYSTEM MASK	SSM	80	SI	P A SP SO B2	10-143
82	LOAD PSW	LPSW	82	SI	L P A SP SO $\phi$ B2	10-56
83	DIAGNOSE	---	83	---	DM P DM MD	10-24
84	BRANCH RELATIVE ON INDEX HIGH (32)	BRXH	84	RSI	$\alpha$ 9 B	7-48
85	BRANCH RELATIVE ON INDEX LOW OR EQ. (32)	BRXLE	85	RSI	$\alpha$ 9 B	7-48
86	BRANCH ON INDEX HIGH (32)	BXH	86	RS-a	$\alpha$ 9 B	7-41
87	BRANCH ON INDEX LOW OR EQUAL (32)	BXLE	87	RS-a	$\alpha$ 9 B	7-41
88	SHIFT RIGHT SINGLE LOGICAL (32)	SRL	88	RS-a		7-387
89	SHIFT LEFT SINGLE LOGICAL (32)	SLL	89	RS-a		7-384
8A	SHIFT RIGHT SINGLE (32)	SRA	8A	RS-a	C	7-386
8B	SHIFT LEFT SINGLE (32)	SLA	8B	RS-a	C IF	7-383
8C	SHIFT RIGHT DOUBLE LOGICAL (64)	SRDL	8C	RS-a	SP	7-386
8D	SHIFT LEFT DOUBLE LOGICAL (64)	SLDL	8D	RS-a	SP	7-383
8E	SHIFT RIGHT DOUBLE (64)	SRDA	8E	RS-a	C SP	7-385
8F	SHIFT LEFT DOUBLE (64)	SLDA	8F	RS-a	C SP IF	7-382
90	STORE MULTIPLE (32)	STM	90	RS-a	A ST B2	7-396
91	TEST UNDER MASK	TM	91	SI	C A B1	7-404
92	MOVE (immediate)	MVI	92	SI	A ST B1	7-292
93	TEST AND SET	TS	93	SI	C $\alpha$ 9 A \$ ST B2	7-404
94	AND (immediate)	NI	94	SI	C A £2 ST B1	7-33
95	COMPARE LOGICAL (immediate)	CLI	95	SI	C A B1	7-153
96	OR (immediate)	OI	96	SI	C A ST B1	7-315
97	EXCLUSIVE OR (immediate)	XI	97	SI	C A ST B1	7-258
98	LOAD MULTIPLE (32)	LM	98	RS-a	A B2	7-285
99	TRACE (32)	TRACE	99	RS-a	P A SP T $\phi$ B2	10-184
9A	LOAD ACCESS MULTIPLE	LAM	9A	RS-a	$\alpha$ 6 A SP UB	7-268
9B	STORE ACCESS MULTIPLE	STAM	9B	RS-a	A SP ST UB	7-389
A8	MOVE LONG EXTENDED	MVCLE	A8	RS-a	C $\alpha$ 9 A SP IC ST R1 R3	7-297
A9	COMPARE LOGICAL LONG EXTENDED	CLCLE	A9	RS-a	C $\alpha$ 9 A SP IC R1 R3	7-161
AC	STORE THEN AND SYSTEM MASK	STNSM	AC	SI	P A ST B1	10-175
AD	STORE THEN OR SYSTEM MASK	STOSM	AD	SI	P A SP ST B1	10-175
AE	SIGNAL PROCESSOR	SIGP	AE	RS-a	C P \$	10-143
AF	MONITOR CALL	MC	AF	SI	$\alpha$ 4,8,9 SP ME	7-291
B1	LOAD REAL ADDRESS (32)	LRA	B1	RX-a	C P A1 * SO BP	10-58
B6	STORE CONTROL (32)	STCTL	B6	RS-a	P A SP ST B2	10-146
B7	LOAD CONTROL (32)	LCTL	B7	RS-a	P A SP B2	10-51
BA	COMPARE AND SWAP (32)	CS	BA	RS-a	C $\alpha$ 9 A SP \$ ST B2	7-145
BB	COMPARE DOUBLE AND SWAP (32)	CDS	BB	RS-a	C $\alpha$ 9 A SP \$ ST B2	7-145
BD	COMPARE LOGICAL CHAR. UNDER MASK (low)	CLM	BD	RS-b	C A B2	7-158
BE	STORE CHARACTERS UNDER MASK (low)	STCM	BE	RS-b	A ST B2	7-390
BF	INSERT CHARACTERS UNDER MASK (low)	ICM	BF	RS-b	C A B2	7-265
C5	BRANCH PREDICTION RELATIVE PRELOAD	BPRP	C5	MII	$\alpha$ 9	7-42
C7	BRANCH PREDICTION PRELOAD	BPP	C7	SMI	$\alpha$ 9	7-42
D0	TRANSLATE AND TEST REVERSE	TRTR	D0	SS-a	C $\alpha$ 9 A GM B1 B2	7-419
D1	MOVE NUMERICS	MVN	D1	SS-a	$\alpha$ 9 A ST B1 B2	7-303
D2	MOVE (character)	MVC	D2	SS-a	$\alpha$ 9 A ST B1 B2	7-292
D3	MOVE ZONES	MVZ	D3	SS-a	$\alpha$ 9 A ST B1 B2	7-307
D4	AND (character)	NC	D4	SS-a	C $\alpha$ 9 A ST B1 B2	7-33
D5	COMPARE LOGICAL (character)	CLC	D5	SS-a	C $\alpha$ 9 A B1 B2	7-153
D6	OR (character)	OC	D6	SS-a	C $\alpha$ 9 A ST B1 B2	7-316
D7	EXCLUSIVE OR (character)	XC	D7	SS-a	C $\alpha$ 9 A ST B1 B2	7-258
D9	MOVE WITH KEY	MVCK	D9	SS-d	C Q A SO ST B1 B2	10-70
DA	MOVE TO PRIMARY	MVCP	DA	SS-d	C Q A SO $\phi$ ST	10-67
DB	MOVE TO SECONDARY	MVCS	DB	SS-d	C Q A SO $\phi$ ST	10-67

Opcod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
DC	TRANSLATE	TR	DC	SS-a	9 A ST B1 B2	7-413
DD	TRANSLATE AND TEST	TRT	DD	SS-a	C 9 A GM B1 B2	7-414
DE	EDIT	ED	DE	SS-a	C 9 A Dg ST B1 B2	8-8
DF	EDIT AND MARK	EDMK	DF	SS-a	C 9 A Dg G1 ST B1 B2	8-11
E1	PACK UNICODE	PKU	E1	SS-f	9 A SP ST B1 B2	7-319
E2	UNPACK UNICODE	UNPKU	E2	SS-a	C 9 A SP ST B1 B2	7-429
E8	MOVE INVERSE	MVCIN	E8	SS-a	9 A ST B1 B2	7-292
E9	PACK ASCII	PKA	E9	SS-f	9 A SP ST B1 B2	7-318
EA	UNPACK ASCII	UNPKA	EA	SS-a	C 9 A SP ST B1 B2	7-428
EE	PERFORM LOCKED OPERATION	PLO	EE	SS-e	C 1 A SP \$ GM ST FC	7-340
EF	LOAD MULTIPLE DISJOINT (64 <- 32&32)	LMD	EF	SS-e	9 A B2 B4	7-285
F0	SHIFT AND ROUND DECIMAL	SRP	F0	SS-c	C 9 A Dg DF ST B1 B2	8-12
F1	MOVE WITH OFFSET	MVO	F1	SS-b	9 A ST B1 B2	7-306
F2	PACK	PACK	F2	SS-b	9 A ST B1 B2	7-317
F3	UNPACK	UNPK	F3	SS-b	9 A ST B1 B2	7-427
F8	ZERO AND ADD	ZAP	F8	SS-b	C 9 A Dg DF ST B1 B2	8-14
F9	COMPARE DECIMAL	CP	F9	SS-b	C 9 A Dg B1 B2	8-7
FA	ADD DECIMAL	AP	FA	SS-b	C 9 A Dg DF ST B1 B2	8-6
FB	SUBTRACT DECIMAL	SP	FB	SS-b	C 9 A Dg DF ST B1 B2	8-13
FC	MULTIPLY DECIMAL	MP	FC	SS-b	9 A SP Dg ST B1 B2	8-12
FD	DIVIDE DECIMAL	DP	FD	SS-b	9 A SP Dg DK ST B1 B2	8-7
0101	PROGRAM RETURN	PR	0101	E	L 1 A1 * SP Z4 T 2 B ST	10-110
0102	UPDATE TREE	UPT	0102	E	C 9 A SP II GM I4 ST	7-430
0104	PERFORM TIMING FACILITY FUNCTION	PTFF	0104	E	C Q A SP GM ST	10-87
0107	SET CLOCK PROGRAMMABLE FIELD	SCKPF	0107	E	P SP G0	10-133
010A	PERFORM FLOATING-POINT OPERATION	PFPO	010A	E	7-9 SP Da Xi X0 GM Xu Xx Xq	9-35
010B	TEST ADDRESSING MODE	TAM	010B	E	C 9	7-403
010C	SET ADDRESSING MODE (24)	SAM24	010C	E	3,9 SP T	7-381
010D	SET ADDRESSING MODE (31)	SAM31	010D	E	3,9 SP T	7-381
010E	SET ADDRESSING MODE (64)	SAM64	010E	E	3,9 T	7-381
01FF	TRAP	TRAP2	01FF	E	1 A* SO T B ST	10-186
A500	INSERT IMMEDIATE (high high)	IIHH	A500	RI-a		7-266
A501	INSERT IMMEDIATE (high low)	IIHL	A501	RI-a		7-266
A502	INSERT IMMEDIATE (low high)	IILH	A502	RI-a		7-266
A503	INSERT IMMEDIATE (low low)	IILL	A503	RI-a		7-266
A504	AND IMMEDIATE (high high)	NIHH	A504	RI-a	C	7-34
A505	AND IMMEDIATE (high low)	NIHL	A505	RI-a	C	7-34
A506	AND IMMEDIATE (low high)	NILH	A506	RI-a	C	7-34
A507	AND IMMEDIATE (low low)	NILL	A507	RI-a	C	7-34
A508	OR IMMEDIATE (high high)	OIHH	A508	RI-a	C	7-316
A509	OR IMMEDIATE (high low)	OIHL	A509	RI-a	C	7-316
A50A	OR IMMEDIATE (low high)	OILH	A50A	RI-a	C	7-317
A50B	OR IMMEDIATE (low low)	OILL	A50B	RI-a	C	7-317
A50C	LOAD LOGICAL IMMEDIATE (high high)	LLIHH	A50C	RI-a		7-284
A50D	LOAD LOGICAL IMMEDIATE (high low)	LLIHL	A50D	RI-a		7-284
A50E	LOAD LOGICAL IMMEDIATE (low high)	LLILH	A50E	RI-a		7-284
A50F	LOAD LOGICAL IMMEDIATE (low low)	LLILL	A50F	RI-a		7-284
A700	TEST UNDER MASK (low high)	TMLH	A700	RI-a	C	7-404
A701	TEST UNDER MASK (low low)	TMLL	A701	RI-a	C	7-404
A702	TEST UNDER MASK (high high)	TMHH	A702	RI-a	C	7-404
A703	TEST UNDER MASK (high low)	TMHL	A703	RI-a	C	7-404
A704	BRANCH RELATIVE ON CONDITION	BRC	A704	RI-c	10 B	7-46
A705	BRANCH RELATIVE AND SAVE	BRAS	A705	RI-b	9 B	7-45
A706	BRANCH RELATIVE ON COUNT (32)	BRCT	A706	RI-b	9 B	7-47

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
A707	BRANCH RELATIVE ON COUNT (64)	BRCTG	A707	RI-b	⌘9 B	7-47
A708	LOAD HALFWORD IMMEDIATE (32) <- 16	LHI	A708	RI-a		7-279
A709	LOAD HALFWORD IMMEDIATE (64 <- 16)	LGHI	A709	RI-a		7-279
A70A	ADD HALFWORD IMMEDIATE (32 <- 16)	AHI	A70A	RI-a	C IF	7-28
A70B	ADD HALFWORD IMMEDIATE (64 <- 16)	AGHI	A70B	RI-a	C IF	7-28
A70C	MULTIPLY HALFWORD IMMEDIATE (32 <- 16)	MHI	A70C	RI-a		7-309
A70D	MULTIPLY HALFWORD IMMEDIATE (64 <- 16)	MGHI	A70D	RI-a		7-309
A70E	COMPARE HALFWORD IMMEDIATE (32 <- 16)	CHI	A70E	RI-a	C	7-151
A70F	COMPARE HALFWORD IMMEDIATE (64 <- 16)	CGHI	A70F	RI-a	C	7-151
B200	LOAD BEAR	LBEAR	B200	S	P A SP B2	10-51
B201	STORE BEAR	STBEAR	B201	S	P A SP ST B2	10-145
B202	STORE CPU ID	STIDP	B202	S	P A SP ST B2	10-147
B204	SET CLOCK	SCK	B204	S	C P A SP B2	10-131
B205	STORE CLOCK	STCK	B205	S	C ⌘8,9 A \$ ST B2	7-391
B206	SET CLOCK COMPARATOR	SCKC	B206	S	P A SP B2	10-132
B207	STORE CLOCK COMPARATOR	STCKC	B207	S	P A SP ST B2	10-146
B208	SET CPU TIMER	SPT	B208	S	P A SP B2	10-133
B209	STORE CPU TIMER	STPT	B209	S	P A SP ST B2	10-149
B20A	SET PSW KEY FROM ADDRESS	SPKA	B20A	S	Q S0	10-134
B20B	INSERT PSW KEY	IPK	B20B	S	Q G2	10-30
B20D	PURGE TLB	PTLB	B20D	S	P \$	10-123
B210	SET PREFIX	SPX	B210	S	P A SP \$ B2	10-133
B211	STORE PREFIX	STPX	B211	S	P A SP ST B2	10-149
B212	STORE CPU ADDRESS	STAP	B212	S	P A SP ST B2	10-146
B218	PROGRAM CALL	PC	B218	S	Q A1 * Z1 T ¢ GM B ST	10-97
B219	SET ADDRESS SPACE CONTROL	SAC	B219	S	Q SP SW ¢	10-130
B21A	COMPARE AND FORM CODEWORD	CFC	B21A	S	C ⌘9 A SP II GM I1	7-138
B221	INVALIDATE PAGE TABLE ENTRY	IPTE	B221	RRF-a	P A1 SP II \$	10-38
B222	INSERT PROGRAM MASK	IPM	B222	RRE		7-267
B223	INSERT VIRTUAL STORAGE KEY	IVSK	B223	RRE	Q A1 * S0 R2	10-31
B224	INSERT ADDRESS SPACE CONTROL	IAC	B224	RRE	C Q S0	10-29
B225	SET SECONDARY ASN	SSAR	B225	RRE	⌘1 A1 * Z3 T ¢	10-135
B226	EXTRACT PRIMARY ASN	EPAR	B226	RRE	Q S0	10-24
B227	EXTRACT SECONDARY ASN	ESAR	B227	RRE	Q S0	10-25
B228	PROGRAM TRANSFER	PT	B228	RRE	Q A1 * SP Z2 T ¢ B	10-114
B229	INSERT STORAGE KEY EXTENDED	ISKE	B229	RRE	P A1 * S0	10-31
B22A	RESET REFERENCE BIT EXTENDED	RRBE	B22A	RRE	C P A1 * S0	10-126
B22B	SET STORAGE KEY EXTENDED	SSKE	B22B	RRF-c	C1 P A1 * IS ¢ K	10-140
B22C	TEST BLOCK	TB	B22C	RRE	C P A1 * II \$ G0 K	10-179
B22D	DIVIDE (extended HFP)	DXR	B22D	RRE	⌘7,9 SP Da EU EO FK	18-12
B22E	PAGE IN	PGIN	B22E	RRE	C P A1 ¢	10-76
B22F	PAGE OUT	PGOUT	B22F	RRE	C P A1 ¢	10-77
B230	CLEAR SUBCHANNEL	CSCH	B230	S	C P OP ¢ GS	14-5
B231	HALT SUBCHANNEL	HSCH	B231	S	C P OP ¢ GS	14-6
B232	MODIFY SUBCHANNEL	MSCH	B232	S	C P A SP OP ¢ GS B2	14-7
B233	START SUBCHANNEL	SSCH	B233	S	C P A SP OP ¢ GS B2	14-15
B234	STORE SUBCHANNEL	STSCH	B234	S	C P A SP OP ¢ GS ST B2	14-18
B235	TEST SUBCHANNEL	TSCH	B235	S	C P A SP OP ¢ GS ST B2	14-21
B236	TEST PENDING INTERRUPTION	TPI	B236	S	C P A1 * SP ¢ ST B2	14-19
B237	SET ADDRESS LIMIT	SAL	B237	S	P OP ¢ G1	14-12
B238	RESUME SUBCHANNEL	RSCH	B238	S	C P OP ¢ GS	14-10
B239	STORE CHANNEL REPORT WORD	STCRW	B239	S	C P A SP ¢ ST B2	14-17
B23A	STORE CHANNEL PATH STATUS	STCPS	B23A	S	P A SP ¢ ST B2	14-16
B23B	RESET CHANNEL PATH	RCHP	B23B	S	C P	14-9

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
B23C	SET CHANNEL MONITOR	SCHM	B23C	S	P OP ¢ GM	14-13
B240	BRANCH AND STACK	BAKR	B240	RRE	1 A1 * Z5 T B ST	10-11
B241	CHECKSUM	CKSM	B241	RRE	C 9 A SP IC R2	7-49
B244	SQUARE ROOT (long HFP)	SQDR	B244	RRE	7,9 Da SQ	18-23
B245	SQUARE ROOT (short HFP)	SQER	B245	RRE	7,9 Da SQ	18-23
B246	STORE USING REAL ADDRESS (32)	STURA	B246	RRE	P A1 SP SU	10-176
B247	MODIFY STACKED STATE	MSTA	B247	RRE	1 A1 * SP SE ST	10-63
B248	PURGE ALB	PALB	B248	RRE	P \$	10-123
B249	EXTRACT STACKED REGISTERS (32)	EREG	B249	RRE	1 A1 * SE U1 U2	10-26
B24A	EXTRACT STACKED STATE	ESTA	B24A	RRE	C 1 A1 * SP SE	10-27
B24B	LOAD USING REAL ADDRESS (32)	LURA	B24B	RRE	P A1 SP	10-62
B24C	TEST ACCESS	TAR	B24C	RRE	C 1 A1 * U1	10-176
B24D	COPY ACCESS	CPYA	B24D	RRE	6 U1 U2	7-255
B24E	SET ACCESS	SAR	B24E	RRE	6 U1	7-381
B24F	EXTRACT ACCESS	EAR	B24F	RRE	U2	7-260
B250	COMPARE AND SWAP AND PURGE (32)	CSP	B250	RRE	C P A1 SP \$ ST R2	10-21
B252	MULTIPLY SINGLE (32)	MSR	B252	RRE		7-310
B254	MOVE PAGE	MVPG	B254	RRE	C Q A SP SO OP ¢4 G0 K ST R1 R2	10-64
B255	MOVE STRING	MVST	B255	RRE	C 9 A SP IC G0 ST R1 R2	7-305
B257	COMPARE UNTIL SUBSTRING EQUAL	CUSE	B257	RRE	C 9 A SP II GM R1 R2	7-168
B258	BRANCH IN SUBSPACE GROUP	BSG	B258	RRE	1 A1 * SO T B R2	10-13
B25A	BRANCH AND SET AUTHORITY	BSA	B25A	RRE	Q A1 * SO T B	10-7
B25D	COMPARE LOGICAL STRING	CLST	B25D	RRE	C 9 A SP IC G0 R1 R2	7-167
B25E	SEARCH STRING	SRST	B25E	RRE	C 9 A SP IC G0 R2	7-377
B263	COMPRESSION CALL	CMPSC	B263	RRE	C 5,9 A SP II Dg GM ST R1 R2	7-171
B276	CANCEL SUBCHANNEL	XSCH	B276	S	C P OP ¢ GS	14-3
B277	RESUME PROGRAM	RP	B277	S	L Q A SP WE T B B2	10-127
B278	STORE CLOCK EXTENDED	STCKE	B278	S	C 8,9 A \$ ST B2	7-392
B279	SET ADDRESS SPACE CONTROL FAST	SACF	B279	S	Q SP SW	10-130
B27C	STORE CLOCK FAST	STCKF	B27C	S	C 8,9 A ST B2	7-391
B27D	STORE SYSTEM INFORMATION	STSI	B27D	S	C P A SP GM ST B2	10-151
B280	LOAD PROGRAM PARAMETER	LPP	B280	S	?? ??	1-1
B284	LOAD CPU-COUNTER-SET CONTROLS	LCCTL	B284	S	C ?? ??	2-26
B285	LOAD PERIPHERAL-COUNTER-SET CONTROLS	LPCTL	B285	S	C ?? ??	2-27
B286	QUERY SAMPLING INFORMATION	QSI	B286	S	?? ??	2-31
B287	LOAD SAMPLING CONTROLS	LSCTL	B287	S	C ?? ??	2-28
B28E	QUERY COUNTER INFORMATION	QCTRI	B28E	S	?? ??	2-29
B28F	QUERY PROCESSOR ACTIVITY COUNTER INFORMATION	QPACI	B28F	S	C P A G0 ST B2	10-123
B299	SET BFP ROUNDING MODE (2 bit)	SRNM	B299	S	7,9 Db	9-47
B29C	STORE FPC	STFPC	B29C	S	7,9 A Db ST B2	9-49
B29D	LOAD FPC	LFPC	B29D	S	7,9 A SP Db B2	9-31
B2A5	TRANSLATE EXTENDED	TRE	B2A5	RRE	C 9 A SP IC ST R1 R2	7-420
B2A6	CONVERT UTF-16 TO UTF-8	CU21	B2A6	RRF-c	C 5,9 A SP IC ST R1 R2	7-237
B2A7	CONVERT UTF-8 TO UTF-16	CU12	B2A7	RRF-c	C 5,9 A SP IC ST R1 R2	7-247
B2B0	STORE FACILITY LIST EXTENDED	STFLE	B2B0	S	C 1 A SP G0 ST B2	7-394
B2B1	STORE FACILITY LIST	STFL	B2B1	S	P	10-149
B2B2	LOAD PSW EXTENDED	LPSWE	B2B2	S	L P A SP SO ¢ B2	10-57
B2B8	SET BFP ROUNDING MODE (3 bit)	SRNMB	B2B8	S	7,9 SP Db	9-47
B2B9	SET DFP ROUNDING MODE	SRNMT	B2B9	S	7,9 Dt	9-47
B2BD	LOAD FPC AND SIGNAL	LFAS	B2BD	S	7,9 A SP Dt Xg B2	9-32
B2E0	SET CPU COUNTER	SCCTR	B2E0	RRE	C ?? ??	2-32
B2E1	SET PERIPHERAL COUNTER	SPCTR	B2E1	RRE	C ?? ??	2-32
B2E4	EXTRACT CPU COUNTER	ECCTR	B2E4	RRE	C ?? ??	2-25
B2E5	EXTRACT PERIPHERAL COUNTER	EPCTR	B2E5	RRE	C ?? ??	2-25

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
B2E8	PERFORM PROCESSOR ASSIST	PPA	B2E8	RRF-c	⌘1	7-355
B2EC	EXTRACT TRANSACTION NESTING DEPTH	ETND	B2EC	RRE	⌘9 SO	7-264
B2ED	EXTRACT COPROCESSOR-GROUP ADDRESS	ECPGA	B2ED	RRE	C ?? ??	2-25
B2F8	TRANSACTION END	TEND	B2F8	S	C SO \$ EX	7-412
B2FA	NEXT INSTRUCTION ACCESS INTENT	NIAI	B2FA	IE		7-312
B2FC	TRANSACTION ABORT	TABORT	B2FC	S	⌘9 SP SO \$ EX	7-405
B2FF	TRAP	TRAP4	B2FF	S	⌘1 A* SO T B ST	10-186
B300	LOAD POSITIVE (short BFP)	LPEBR	B300	RRE	C ⌘7,9 Db	19-35
B301	LOAD NEGATIVE (short BFP)	LNEBR	B301	RRE	C ⌘7,9 Db	19-34
B302	LOAD AND TEST (short BFP)	LTEBR	B302	RRE	C ⌘7,9 Db Xi	19-31
B303	LOAD COMPLEMENT (short BFP)	LCEBR	B303	RRE	C ⌘7,9 Db	19-31
B304	LOAD LENGTHENED (short to long BFP)	LDEBR	B304	RRE	⌘7,9 Db Xi	19-33
B305	LOAD LENGTHENED (long to extended BFP)	LXDBR	B305	RRE	⌘7,9 SP Db Xi	19-33
B306	LOAD LENGTHENED (short to extended BFP)	LXEBR	B306	RRE	⌘7,9 SP Db Xi	19-33
B307	MULTIPLY (long to extended BFP)	MXDBR	B307	RRE	⌘7,9 SP Db Xi	19-37
B308	COMPARE AND SIGNAL (short BFP)	KEBR	B308	RRE	C ⌘7,9 Db Xi	19-18
B309	COMPARE (short BFP)	CEBR	B309	RRE	C ⌘7,9 Db Xi	19-17
B30A	ADD (short BFP)	AEBR	B30A	RRE	C ⌘7,9 Db Xi Xo Xu Xx	19-15
B30B	SUBTRACT (short BFP)	SEBR	B30B	RRE	C ⌘7,9 Db Xi Xo Xu Xx	19-40
B30C	MULTIPLY (short to long BFP)	MDEBR	B30C	RRE	⌘7,9 Db Xi	19-37
B30D	DIVIDE (short BFP)	DEBR	B30D	RRE	⌘7,9 Db Xi Xz Xo Xu Xx	19-27
B30E	MULTIPLY AND ADD (short BFP)	MAEBR	B30E	RRD	⌘7,9 Db Xi Xo Xu Xx	19-38
B30F	MULTIPLY AND SUBTRACT (short BFP)	MSEBR	B30F	RRD	⌘7,9 Db Xi Xo Xu Xx	19-38
B310	LOAD POSITIVE (long BFP)	LPDBR	B310	RRE	C ⌘7,9 Db	19-35
B311	LOAD NEGATIVE (long BFP)	LNDBR	B311	RRE	C ⌘7,9 Db	19-34
B312	LOAD AND TEST (long BFP)	LTDBR	B312	RRE	C ⌘7,9 Db Xi	19-31
B313	LOAD COMPLEMENT (long BFP)	LCDBR	B313	RRE	C ⌘7,9 Db	19-31
B314	SQUARE ROOT (short BFP)	SQEBR	B314	RRE	⌘7,9 Db Xi Xx	19-40
B315	SQUARE ROOT (long BFP)	SQDBR	B315	RRE	⌘7,9 Db Xi Xx	19-40
B316	SQUARE ROOT (extended BFP)	SQXBR	B316	RRE	⌘7,9 SP Db Xi Xx	19-40
B317	MULTIPLY (short BFP)	MEEBR	B317	RRE	⌘7,9 Db Xi Xo Xu Xx	19-37
B318	COMPARE AND SIGNAL (long BFP)	KDBR	B318	RRE	C ⌘7,9 Db Xi	19-18
B319	COMPARE (long BFP)	CDBR	B319	RRE	C ⌘7,9 Db Xi	19-17
B31A	ADD (long BFP)	ADBR	B31A	RRE	C ⌘7,9 Db Xi Xo Xu Xx	19-15
B31B	SUBTRACT (long BFP)	SDBR	B31B	RRE	C ⌘7,9 Db Xi Xo Xu Xx	19-40
B31C	MULTIPLY (long BFP)	MDBR	B31C	RRE	⌘7,9 Db Xi Xo Xu Xx	19-37
B31D	DIVIDE (long BFP)	DDBR	B31D	RRE	⌘7,9 Db Xi Xz Xo Xu Xx	19-27
B31E	MULTIPLY AND ADD (long BFP)	MADBR	B31E	RRD	⌘7,9 Db Xi Xo Xu Xx	19-38
B31F	MULTIPLY AND SUBTRACT (long BFP)	MSDBR	B31F	RRD	⌘7,9 Db Xi Xo Xu Xx	19-38
B324	LOAD LENGTHENED (short to long HFP)	LDER	B324	RRE	⌘7,9 Da	18-15
B325	LOAD LENGTHENED (long to extended HFP)	LXDR	B325	RRE	⌘7,9 SP Da	18-15
B326	LOAD LENGTHENED (short to extended HFP)	LXER	B326	RRE	⌘7,9 SP Da	18-15
B32E	MULTIPLY AND ADD (short HFP)	MAER	B32E	RRD	⌘7,9 Da EU E0	18-19
B32F	MULTIPLY AND SUBTRACT (short HFP)	MSER	B32F	RRD	⌘7,9 Da EU E0	18-19
B336	SQUARE ROOT (extended HFP)	SQXR	B336	RRE	⌘7,9 SP Da SQ	18-23
B337	MULTIPLY (short HFP)	MEER	B337	RRE	⌘7,9 Da EU E0	18-17
B338	MULTIPLY AND ADD UNNRM. (long to ext. low HFP)	MAYLR	B338	RRD	⌘7,9 Da	18-20
B339	MULTIPLY UNNORM. (long to ext. low HFP)	MYLR	B339	RRD	⌘7,9 Da	18-22
B33A	MULTIPLY & ADD UNNORMALIZED (long to ext. HFP)	MAYR	B33A	RRD	⌘7,9 Da	18-20
B33B	MULTIPLY UNNORMALIZED (long to ext. HFP)	MYR	B33B	RRD	⌘7,9 SP Da	18-22
B33C	MULTIPLY AND ADD UNNRM. (long to ext. high HFP)	MAYHR	B33C	RRD	⌘7,9 Da	18-20
B33D	MULTIPLY UNNORM. (long to ext. high HFP)	MYHR	B33D	RRD	⌘7,9 Da	18-22
B33E	MULTIPLY AND ADD (long HFP)	MADR	B33E	RRD	⌘7,9 Da EU E0	18-19
B33F	MULTIPLY AND SUBTRACT (long HFP)	MSDR	B33F	RRD	⌘7,9 Da EU E0	18-19

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
B340	LOAD POSITIVE (extended BFP)	LPXBR	B340	RRE	C 7,9 SP Db	19-35
B341	LOAD NEGATIVE (extended BFP)	LNXR	B341	RRE	C 7,9 SP Db	19-34
B342	LOAD AND TEST (extended BFP)	LTXBR	B342	RRE	C 7,9 SP Db Xi	19-31
B343	LOAD COMPLEMENT (extended BFP)	LCXBR	B343	RRE	C 7,9 SP Db	19-31
B344	LOAD ROUNDED (long to short BFP)	LEDBRA	B344	RRF-e	7,9 SP Db Xi Xo Xu Xx	19-35
B345	LOAD ROUNDED (extended to long BFP)	LDXBRA	B345	RRF-e	7,9 SP Db Xi Xo Xu Xx	19-35
B346	LOAD ROUNDED (extended to short BFP)	LEXBRA	B346	RRF-e	7,9 SP Db Xi Xo Xu Xx	19-35
B347	LOAD FP INTEGER (extended BFP)	FIXBRA	B347	RRF-e	7,9 SP Db Xi Xx	19-32
B348	COMPARE AND SIGNAL (extended BFP)	KXBR	B348	RRE	C 7,9 SP Db Xi	19-18
B349	COMPARE (extended BFP)	CXBR	B349	RRE	C 7,9 SP Db Xi	19-17
B34A	ADD (extended BFP)	AXBR	B34A	RRE	C 7,9 SP Db Xi Xo Xu Xx	19-15
B34B	SUBTRACT (extended BFP)	SXBR	B34B	RRE	C 7,9 SP Db Xi Xo Xu Xx	19-40
B34C	MULTIPLY (extended BFP)	MXBR	B34C	RRE	7,9 SP Db Xi Xo Xu Xx	19-37
B34D	DIVIDE (extended BFP)	DXBR	B34D	RRE	7,9 SP Db Xi Xz Xo Xu Xx	19-27
B350	CONVERT HFP TO BFP (long to short)	TBEDR	B350	RRF-e	C 7,9 SP Da	9-29
B351	CONVERT HFP TO BFP (long)	TBDR	B351	RRF-e	C 7,9 SP Da	9-29
B353	DIVIDE TO INTEGER (short BFP)	DIEBR	B353	RRF-b	C 7,9 SP Db Xi Xu Xx	19-28
B357	LOAD FP INTEGER (short BFP)	FIEBRA	B357	RRF-e	7,9 SP Db Xi Xx	19-32
B358	CONVERT BFP TO HFP (short to long)	THDER	B358	RRE	C 7,9 Da	9-27
B359	CONVERT BFP TO HFP (long)	THDR	B359	RRE	C 7,9 Da	9-27
B35B	DIVIDE TO INTEGER (long BFP)	DIDBR	B35B	RRF-b	C 7,9 SP Db Xi Xu Xx	19-28
B35F	LOAD FP INTEGER (long BFP)	FIDBRA	B35F	RRF-e	7,9 SP Db Xi Xx	19-32
B360	LOAD POSITIVE (extended HFP)	LPXR	B360	RRE	C 7,9 SP Da	18-16
B361	LOAD NEGATIVE (extended HFP)	LNXR	B361	RRE	C 7,9 SP Da	18-16
B362	LOAD AND TEST (extended HFP)	LTXR	B362	RRE	C 7,9 SP Da	18-14
B363	LOAD COMPLEMENT (extended HFP)	LCXR	B363	RRE	C 7,9 SP Da	18-14
B365	LOAD (extended)	LXR	B365	RRE	7,9 SP Da	9-31
B366	LOAD ROUNDED (extended to short HFP)	LEXR	B366	RRE	7,9 SP Da E0	18-17
B367	LOAD FP INTEGER (extended HFP)	FIXR	B367	RRE	7,9 SP Da	18-15
B369	COMPARE (extended HFP)	CXR	B369	RRE	C 7,9 SP Da	18-10
B370	LOAD POSITIVE (long)	LPDFR	B370	RRE	7,9 Da	9-34
B371	LOAD NEGATIVE (long)	LNDFR	B371	RRE	7,9 Da	9-34
B372	COPY SIGN (long)	CPSDR	B372	RRF-b	7,9 Da	9-30
B373	LOAD COMPLEMENT (long)	LCDFR	B373	RRE	7,9 Da	9-31
B374	LOAD ZERO (short)	LZER	B374	RRE	7,9 Da	9-35
B375	LOAD ZERO (long)	LZDR	B375	RRE	7,9 Da	9-35
B376	LOAD ZERO (extended)	LZXR	B376	RRE	7,9 SP Da	9-35
B377	LOAD FP INTEGER (short HFP)	FIER	B377	RRE	7,9 Da	18-15
B37F	LOAD FP INTEGER (long HFP)	FIDR	B37F	RRE	7,9 Da	18-15
B384	SET FPC	SFPC	B384	RRE	7,9 SP Db	9-47
B385	SET FPC AND SIGNAL	SFASR	B385	RRE	7,9 SP Dt Xg	9-48
B38C	EXTRACT FPC	EFPC	B38C	RRE	7,9 Db	9-30
B390	CONVERT FROM LOGICAL (32 to short BFP)	CELFBR	B390	RRF-e	7,9 SP Db Xx	19-21
B391	CONVERT FROM LOGICAL (32 to long BFP)	CDLFBR	B391	RRF-e	7,9 SP Db	19-21
B392	CONVERT FROM LOGICAL (32 to extended BFP)	CXLFBR	B392	RRF-e	7,9 SP Db	19-21
B394	CONVERT FROM FIXED (32 to short BFP)	CEFBRA	B394	RRF-e	7,9 SP Db Xx	19-19
B395	CONVERT FROM FIXED (32 to long BFP)	CDFBRA	B395	RRF-e	7,9 SP Db	19-19
B396	CONVERT FROM FIXED (32 to extended BFP)	CXFBRA	B396	RRF-e	7,9 SP Db	19-19
B398	CONVERT TO FIXED (short BFP to 32)	CFEBRA	B398	RRF-e	C 7,9 SP Db Xi Xx	19-22
B399	CONVERT TO FIXED (long BFP to 32)	CFDBRA	B399	RRF-e	C 7,9 SP Db Xi Xx	19-22
B39A	CONVERT TO FIXED (extended BFP to 32)	CFXBRA	B39A	RRF-e	C 7,9 SP Db Xi Xx	19-22
B39C	CONVERT TO LOGICAL (short BFP to 32)	CLFEBR	B39C	RRF-e	C 7,9 SP Db Xi Xx	19-25
B39D	CONVERT TO LOGICAL (long BFP to 32)	CLFDBR	B39D	RRF-e	C 7,9 SP Db Xi Xx	19-25
B39E	CONVERT TO LOGICAL (extended BFP to 32)	CLFXBR	B39E	RRF-e	C 7,9 SP Db Xi Xx	19-25

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
B3A0	CONVERT FROM LOGICAL (64 to short BFP)	CELGBR	B3A0	RRF-e	⌵7,9 SP Db Xx	19-21
B3A1	CONVERT FROM LOGICAL (64 to long BFP)	CDLGBR	B3A1	RRF-e	⌵7,9 SP Db Xx	19-21
B3A2	CONVERT FROM LOGICAL (64 to extended BFP)	CXLGBR	B3A2	RRF-e	⌵7,9 SP Db	19-21
B3A4	CONVERT FROM FIXED (64 to short BFP)	CEGBRA	B3A4	RRF-e	⌵7,9 SP Db Xx	19-19
B3A5	CONVERT FROM FIXED (64 to long BFP)	CDGBRA	B3A5	RRF-e	⌵7,9 SP Db Xx	19-19
B3A6	CONVERT FROM FIXED (64 to extended BFP)	CXGBRA	B3A6	RRF-e	⌵7,9 SP Db	19-19
B3A8	CONVERT TO FIXED (short BFP to 64)	CGEBRA	B3A8	RRF-e	C ⌵7,9 SP Db Xi Xx	19-22
B3A9	CONVERT TO FIXED (long BFP to 64)	CGDBRA	B3A9	RRF-e	C ⌵7,9 SP Db Xi Xx	19-22
B3AA	CONVERT TO FIXED (extended BFP to 64)	CGXBRA	B3AA	RRF-e	C ⌵7,9 SP Db Xi Xx	19-22
B3AC	CONVERT TO LOGICAL (short BFP to 64)	CLGEBR	B3AC	RRF-e	C ⌵7,9 SP Db Xi Xx	19-25
B3AD	CONVERT TO LOGICAL (long BFP to 64)	CLGDBR	B3AD	RRF-e	C ⌵7,9 SP Db Xi Xx	19-25
B3AE	CONVERT TO LOGICAL (extended BFP to 64)	CLGXBR	B3AE	RRF-e	C ⌵7,9 SP Db Xi Xx	19-25
B3B4	CONVERT FROM FIXED (32 to short HFP)	CEFR	B3B4	RRE	⌵7,9 Da	18-11
B3B5	CONVERT FROM FIXED (32 to long HFP)	CDFR	B3B5	RRE	⌵7,9 Da	18-11
B3B6	CONVERT FROM FIXED (32 to extended HFP)	CXFR	B3B6	RRE	⌵7,9 SP Da	18-11
B3B8	CONVERT TO FIXED (short HFP to 32)	CFER	B3B8	RRF-e	C ⌵7,9 SP Da	18-11
B3B9	CONVERT TO FIXED (long HFP to 32)	CFDR	B3B9	RRF-e	C ⌵7,9 SP Da	18-11
B3BA	CONVERT TO FIXED (extended HFP to 32)	CFXR	B3BA	RRF-e	C ⌵7,9 SP Da	18-11
B3C1	LOAD FPR FROM GR (64 to long)	LDGR	B3C1	RRE	⌵7,9 Da	9-34
B3C4	CONVERT FROM FIXED (64 to short HFP)	CEGR	B3C4	RRE	⌵7,9 Da	18-11
B3C5	CONVERT FROM FIXED (64 to long HFP)	CDGR	B3C5	RRE	⌵7,9 Da	18-11
B3C6	CONVERT FROM FIXED (64 to extended HFP)	CXGR	B3C6	RRE	⌵7,9 SP Da	18-11
B3C8	CONVERT TO FIXED (short HFP to 64)	CGER	B3C8	RRF-e	C ⌵7,9 SP Da	18-11
B3C9	CONVERT TO FIXED (long HFP to 64)	CGDR	B3C9	RRF-e	C ⌵7,9 SP Da	18-11
B3CA	CONVERT TO FIXED (extended HFP to 64)	CGXR	B3CA	RRF-e	C ⌵7,9 SP Da	18-11
B3CD	LOAD GR FROM FPR (long to 64)	LGDR	B3CD	RRE	⌵7,9 Da	9-34
B3D0	MULTIPLY (long DFP)	MDTRA	B3D0	RRF-a	⌵7,9 Dt Xi Xo Xu Xx Xq	20-48
B3D1	DIVIDE (long DFP)	DDTRA	B3D1	RRF-a	⌵7,9 Dt Xi Xz Xo Xu Xx Xq	20-37
B3D2	ADD (long DFP)	ADTRA	B3D2	RRF-a	C ⌵7,9 Dt Xi Xo Xu Xx Xq	20-19
B3D3	SUBTRACT (long DFP)	SDTRA	B3D3	RRF-a	C ⌵7,9 Dt Xi Xo Xu Xx Xq	20-55
B3D4	LOAD LENGTHENED (short to long DFP)	LDETR	B3D4	RRF-d	⌵7,9 Dt Xi	20-45
B3D5	LOAD ROUNDED (long to short DFP)	LEDTR	B3D5	RRF-e	⌵7,9 Dt Xi Xo Xu Xx Xq	20-46
B3D6	LOAD AND TEST (long DFP)	LTDR	B3D6	RRE	C ⌵7,9 Dt Xi	20-41
B3D7	LOAD FP INTEGER (long DFP)	FIDTR	B3D7	RRF-e	⌵7,9 Dt Xi Xx Xq	20-42
B3D8	MULTIPLY (extended DFP)	MXTRA	B3D8	RRF-a	⌵7,9 SP Dt Xi Xo Xu Xx Xq	20-48
B3D9	DIVIDE (extended DFP)	DXTRA	B3D9	RRF-a	⌵7,9 SP Dt Xi Xz Xo Xu Xx Xq	20-37
B3DA	ADD (extended DFP)	AXTRA	B3DA	RRF-a	C ⌵7,9 SP Dt Xi Xo Xu Xx Xq	20-19
B3DB	SUBTRACT (extended DFP)	SXTRA	B3DB	RRF-a	C ⌵7,9 SP Dt Xi Xo Xu Xx Xq	20-55
B3DC	LOAD LENGTHENED (long to extended DFP)	LXDTR	B3DC	RRF-d	⌵7,9 SP Dt Xi	20-45
B3DD	LOAD ROUNDED (extended to long DFP)	LDXTR	B3DD	RRF-e	⌵7,9 SP Dt Xi Xo Xu Xx Xq	20-46
B3DE	LOAD AND TEST (extended DFP)	LTXTR	B3DE	RRE	C ⌵7,9 SP Dt Xi	20-41
B3DF	LOAD FP INTEGER (extended DFP)	FIXTR	B3DF	RRF-e	⌵7,9 SP Dt Xi Xx Xq	20-42
B3E0	COMPARE AND SIGNAL (long DFP)	KDTR	B3E0	RRE	C ⌵7,9 Dt Xi	20-23
B3E1	CONVERT TO FIXED (long DFP to 64)	CGDTRA	B3E1	RRF-e	C ⌵7,9 Dt Xi Xx	20-30
B3E2	CONVERT TO UNSIGNED PACKED (long DFP to 64)	CUDTR	B3E2	RRE	⌵7,9 Dt	20-35
B3E3	CONVERT TO SIGNED PACKED (long DFP to 64)	CSDTR	B3E3	RRF-d	⌵7,9 Dt	20-35
B3E4	COMPARE (long DFP)	CDTR	B3E4	RRE	C ⌵7,9 Dt Xi	20-22
B3E5	EXTRACT BIASED EXPONENT (long DFP to 64)	EEDTR	B3E5	RRE	⌵7,9 Dt	20-39
B3E7	EXTRACT SIGNIFICANCE (long DFP to 64)	ESDTR	B3E7	RRE	⌵7,9 Dt	20-39
B3E8	COMPARE AND SIGNAL (extended DFP)	KXTR	B3E8	RRE	C ⌵7,9 SP Dt Xi	20-23
B3E9	CONVERT TO FIXED (extended DFP to 64)	CGXTRA	B3E9	RRF-e	C ⌵7,9 SP Dt Xi Xx	20-30
B3EA	CONVERT TO UNSIGNED PACKED (extended DFP to 128)	CUXTR	B3EA	RRE	⌵7,9 SP Dt	20-35
B3EB	CONVERT TO SIGNED PACKED (extended DFP to 128)	CSXTR	B3EB	RRF-d	⌵7,9 SP Dt	20-35
B3EC	COMPARE (extended DFP)	CXTR	B3EC	RRE	C ⌵7,9 SP Dt Xi	20-22

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
B3ED	EXTRACT BIASED EXPONENT (extended DFP to64)	EEXTR	B3ED	RRE	⌵7,9 SP Dt	20-39
B3EF	EXTRACT SIGNIFICANCE (extended DFP to 64)	ESXTR	B3EF	RRE	⌵7,9 SP Dt	20-39
B3F1	CONVERT FROM FIXED (64 to long DFP)	CDGTRA	B3F1	RRF-e	⌵7,9 Dt Xx Xq	20-24
B3F2	CONVERT FROM UNSIGNED PACKED (64 to long DFP)	CDUTR	B3F2	RRE	⌵7,9 Dt Dg	20-28
B3F3	CONVERT FROM SIGNED PACKED (64 to long DFP)	CDSTR	B3F3	RRE	⌵7,9 Dt Dg	20-28
B3F4	COMPARE BIASED EXPONENT (long DFP)	CEDTR	B3F4	RRE	C ⌵7,9 Dt	20-23
B3F5	QUANTIZE (long DFP)	QADTR	B3F5	RRF-b	⌵7,9 Dt Xi Xx Xq	20-49
B3F6	INSERT BIASED EXPONENT (64 to long DFP)	IEDTR	B3F6	RRF-b	⌵7,9 Dt	20-40
B3F7	REROUND (long DFP)	RRDTR	B3F7	RRF-b	⌵7,9 Dt Xi Xx Xq	20-52
B3F9	CONVERT FROM FIXED (64 to extended DFP)	CXGTRA	B3F9	RRF-e	⌵7,9 SP Dt	20-24
B3FA	CONVERT FROM UNSIGNED PACKED (128 to ext. DFP)	CXUTR	B3FA	RRE	⌵7,9 SP Dt Dg	20-28
B3FB	CONVERT FROM SIGNED PACKED (128 to extended DFP)	CXSTR	B3FB	RRE	⌵7,9 SP Dt Dg	20-28
B3FC	COMPARE BIASED EXPONENT (extended DFP)	CEXTR	B3FC	RRE	C ⌵7,9 SP Dt	20-23
B3FD	QUANTIZE (extended DFP)	QAXTR	B3FD	RRF-b	⌵7,9 SP Dt Xi Xx Xq	20-49
B3FE	INSERT BIASED EXPONENT (64 to extended DFP)	IEXTR	B3FE	RRF-b	⌵7,9 SP Dt	20-40
B3FF	REROUND (extended DFP)	RRXTR	B3FF	RRF-b	⌵7,9 SP Dt Xi Xx Xq	20-52
B900	LOAD POSITIVE (64)	LPGR	B900	RRE	C IF	7-289
B901	LOAD NEGATIVE (64)	LNGR	B901	RRE	C	7-286
B902	LOAD AND TEST (64)	LTGR	B902	RRE	C	7-273
B903	LOAD COMPLEMENT (64)	LCGR	B903	RRE	C IF	7-275
B904	LOAD (64)	LGR	B904	RRE		7-267
B905	LOAD USING REAL ADDRESS (64)	LURAG	B905	RRE	P A1 SP	10-63
B906	LOAD BYTE (64 <- 8)	LGBR	B906	RRE		7-275
B907	LOAD HALFWORD (64 <- 16)	LGHR	B907	RRE		7-278
B908	ADD (64)	AGR	B908	RRE	C IF	7-26
B909	SUBTRACT (64)	SGR	B909	RRE	C IF	7-399
B90A	ADD LOGICAL (64)	ALGR	B90A	RRE	C	7-29
B90B	SUBTRACT LOGICAL (64)	SLGR	B90B	RRE	C	7-401
B90C	MULTIPLY SINGLE (64)	MSGR	B90C	RRE		7-310
B90D	DIVIDE SINGLE (64)	DSGR	B90D	RRE	⌵9 SP IK	7-257
B90E	EXTRACT STACKED REGISTERS (64)	EREGG	B90E	RRE	⌵1 A1 * SE U1 U2	10-26
B90F	LOAD REVERSED (64)	LRVGR	B90F	RRE		7-290
B910	LOAD POSITIVE (64 <- 32)	LPGFR	B910	RRE	C	7-289
B911	LOAD NEGATIVE (64 <- 32)	LNGFR	B911	RRE	C	7-286
B912	LOAD AND TEST (64 <- 32)	LTGFR	B912	RRE	C	7-273
B913	LOAD COMPLEMENT (64 <- 32)	LCGFR	B913	RRE	C	7-275
B914	LOAD (64 <- 32)	LGFR	B914	RRE		7-267
B916	LOAD LOGICAL (64 <- 32)	LLGFR	B916	RRE		7-281
B917	LOAD LOGICAL THIRTY ONE BITS (64 <- 31)	LLGTR	B917	RRE		7-284
B918	ADD (64 <- 32)	AGFR	B918	RRE	C IF	7-26
B919	SUBTRACT (64 <- 32)	SGFR	B919	RRE	C IF	7-399
B91A	ADD LOGICAL (64 <- 32)	ALGFR	B91A	RRE	C	7-29
B91B	SUBTRACT LOGICAL (64 <- 32)	SLGFR	B91B	RRE	C	7-401
B91C	MULTIPLY SINGLE (64 <- 32)	MSGFR	B91C	RRE		7-310
B91D	DIVIDE SINGLE (64 <- 32)	DSGFR	B91D	RRE	⌵9 SP IK	7-257
B91E	COMPUTE MESSAGE AUTHENTICATION CODE	KMAC	B91E	RRE	C ⌵5,9 A SP IC GM I1 ST R2	7-221
B91F	LOAD REVERSED (32)	LRVR	B91F	RRE		7-290
B920	COMPARE (64)	CGR	B920	RRE	C	7-136
B921	COMPARE LOGICAL (64)	CLGR	B921	RRE	C	7-153
B925	STORE USING REAL ADDRESS (64)	STURG	B925	RRE	P A1 SP SU	10-176
B926	LOAD BYTE (32 <- 8)	LBR	B926	RRE		7-275
B927	LOAD HALFWORD (32 <- 16)	LHR	B927	RRE		7-278
B928	PERFORM CRYPTOGRAPHIC KEY MGMT. OPERATIONS	PCKMO	B928	RRE	P A SP GM ST	10-78
B929	CIPHER MESSAGE WITH AUTHENTICATION	KMA	B929	RRF-b	C ⌵5,9 A SP IC GM I1 ST R1 R2 R3	7-78

OpCod	Name...	Assm	OpCd	IFmt	Attributes...										Ref	
B92A	CIPHER MESSAGE WITH CIPHER FEEDBACK	KMF	B92A	RRE	C	5,9	A	SP	IC	GM	I1	ST	R1	R2	7-92	
B92B	CIPHER MESSAGE WITH OUTPUT FEEDBACK	KMO	B92B	RRE	C	5,9	A	SP	IC	GM	I1	ST	R1	R2	7-121	
B92C	PERFORM CRYPTOGRAPHIC COMPUTATION	PCC	B92C	RRE	C	5,9	A	SP	IC	GM	I1	ST			7-320	
B92D	CIPHER MESSAGE WITH COUNTER	KMCTR	B92D	RRF-b	C	5,9	A	SP	IC	GM	I1	ST	R1	,R2	,R3	7-108
B92E	CIPHER MESSAGE	KM	B92E	RRE	C	5,9	A	SP	IC	GM	I1	ST	R1	R2	7-53	
B92F	CIPHER MESSAGE WITH CHAINING	KMC	B92F	RRE	C	5,9	A	SP	IC	GM	I1	ST	R1	R2	7-53	
B930	COMPARE (64 <- 32)	CGFR	B930	RRE	C										7-136	
B931	COMPARE LOGICAL (64 <- 32)	CLGFR	B931	RRE	C										7-153	
B938	SORT LISTS	SORTL	B938	RRE	C	5,9	A	SP	IC	Dg	GM	I1	ST	R1	R2	26-96
B939	DEFLATE CONVERSION CALL	DFLTCC	B939	RRF-a	C	5,9	A	SP	IC	GM	I1	ST	R1	R2	R3	26-17
B93A	COMPUTE DIGITAL SIGNATURE AUTHENTICATION	KDSA	B93A	RRE	C	5,9	A	SP	IC	GM	I1	ST	R2			26-2
B93B	NEURAL NETWORK PROCESSING ASSIST	NNPA	B93B	RRE	C	5,9	A	SP	IC	Dg	GM	I1	ST			26-61
B93C	PERFORM RANDOM NUMBER OPERATION	PRNO	B93C	RRE	C	5,9	A	SP	IC	Dg	GM	I1	ST	R1	R2	7-355
B93E	COMPUTE INTERMEDIATE MESSAGE DIGEST	KIMD	B93E	RRE	C	5,9	A	SP	IC	GM	I1	ST	R2			7-189
B93F	COMPUTE LAST MESSAGE DIGEST	KLMD	B93F	RRE	C	5,9	A	SP	IC	GM	I1	ST	R2			7-202
B941	CONVERT TO FIXED (long DFP to 32)	CFDTR	B941	RRF-e	C	7,9	Dt		Xi	Xx					20-30	
B942	CONVERT TO LOGICAL (long DFP to 64)	CLGDTR	B942	RRF-e	C	7,9	Dt		Xi	Xx					20-32	
B943	CONVERT TO LOGICAL (long DFP to 32)	CLFDTR	B943	RRF-e	C	7,9	Dt		Xi	Xx					20-32	
B946	BRANCH ON COUNT (64)	BCTGR	B946	RRE		9	B								7-41	
B949	CONVERT TO FIXED (extended DFP to 32)	CFXTR	B949	RRF-e	C	7,9	SP	Dt	Xi	Xx					20-30	
B94A	CONVERT TO LOGICAL (extended DFP to 64)	CLGXTR	B94A	RRF-e	C	7,9	SP	Dt	Xi	Xx					20-32	
B94B	CONVERT TO LOGICAL (extended DFP to 32)	CLFXTR	B94B	RRF-e	C	7,9	SP	Dt	Xi	Xx					20-32	
B951	CONVERT FROM FIXED (32 to long DFP)	CDFTR	B951	RRF-e		7,9	Dt								20-24	
B952	CONVERT FROM LOGICAL (64 to long DFP)	CDLGTR	B952	RRF-e		7,9	Dt		Xx	Xq					20-25	
B953	CONVERT FROM LOGICAL (32 to long DFP)	CDLFTR	B953	RRF-e		7,9	Dt								20-25	
B959	CONVERT FROM FIXED (32 to extended DFP)	CXFTR	B959	RRF-e		7,9	SP	Dt							20-24	
B95A	CONVERT FROM LOGICAL (64 to extended DFP)	CXLGTR	B95A	RRF-e		7,9	SP	Dt							20-25	
B95B	CONVERT FROM LOGICAL (32 to extended DFP)	CXLFTR	B95B	RRF-e		7,9	SP	Dt							20-25	
B960	COMPARE AND TRAP (64)	CGRT	B960	RRF-c			Dc								7-150	
B961	COMPARE LOGICAL AND TRAP (64)	CLGRT	B961	RRF-c			Dc								7-156	
B964	NAND (64)	NNGRK	B964	RRF-a	C										7-312	
B965	OR WITH COMPLEMENT (64)	OCGRK	B965	RRF-a	C										7-317	
B966	NOR (64)	NOGRK	B966	RRF-a	C										7-314	
B967	NOT EXCLUSIVE OR (64)	NXGRK	B967	RRF-a	C										7-315	
B972	COMPARE AND TRAP (32)	CRT	B972	RRF-c			Dc								7-150	
B973	COMPARE LOGICAL AND TRAP (32)	CLRT	B973	RRF-c			Dc								7-156	
B974	NAND (32)	NNRK	B974	RRF-a	C										7-312	
B975	OR WITH COMPLEMENT (32)	OCRK	B975	RRF-a	C										7-317	
B976	NOR (32)	NORK	B976	RRF-a	C										7-314	
B977	NOT EXCLUSIVE OR (32)	NXRK	B977	RRF-a	C										7-315	
B980	AND (64)	NGR	B980	RRE	C										7-33	
B981	OR (64)	OGR	B981	RRE	C										7-315	
B982	EXCLUSIVE OR (64)	XGR	B982	RRE	C										7-257	
B983	FIND LEFTMOST ONE	FLOGR	B983	RRE	C		SP								7-265	
B984	LOAD LOGICAL CHARACTER (64 <- 8)	LLGCR	B984	RRE											7-282	
B985	LOAD LOGICAL HALFWORD (64 <- 16)	LLGHR	B985	RRE											7-283	
B986	MULTIPLY LOGICAL (128 <- 64)	MLGR	B986	RRE			SP								7-309	
B987	DIVIDE LOGICAL (64 <- 128)	DLGR	B987	RRE		9	SP	IK							7-256	
B988	ADD LOGICAL WITH CARRY (64)	ALCGR	B988	RRE	C										7-30	
B989	SUBTRACT LOGICAL WITH BORROW (64)	SLBGR	B989	RRE	C										7-402	
B98A	COMPARE AND SWAP AND PURGE (64)	CSPG	B98A	RRE	C	P	A1	SP	\$	ST	R2				10-21	
B98B	RESET DAT PROTECTION	RDP	B98B	RRF-b		P	A1	\$							10-124	
B98D	EXTRACT PSW	EPSW	B98D	RRE		8,9									7-264	
B98E	INVALIDATE DAT TABLE ENTRY	IDTE	B98E	RRF-b		U	P	A1	SP	\$					10-33	

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
B98F	COMPARE AND REPLACE DAT TABLE ENTRY	CRDTE	B98F	RRF-b	P A1 SP \$	10-18
B990	TRANSLATE TWO TO TWO	TRTT	B990	RRF-c	C x9 A SP IC GM ST RM R2	7-423
B991	TRANSLATE TWO TO ONE	TRTO	B991	RRF-c	C x9 A SP IC GM ST RM R2	7-422
B992	TRANSLATE ONE TO TWO	TROT	B992	RRF-c	C x9 A SP IC GM ST RM R2	7-422
B993	TRANSLATE ONE TO ONE	TROO	B993	RRF-c	C x9 A SP IC GM ST RM R2	7-422
B994	LOAD LOGICAL CHARACTER (32 <- 8)	LLCR	B994	RRE		7-282
B995	LOAD LOGICAL HALFWORD (32 <- 16)	LLHR	B995	RRE		7-283
B996	MULTIPLY LOGICAL (64 <- 32)	MLR	B996	RRE	SP	7-309
B997	DIVIDE LOGICAL (32 <- 64)	DLR	B997	RRE	x9 SP IK	7-256
B998	ADD LOGICAL WITH CARRY (32)	ALCR	B998	RRE	C	7-30
B999	SUBTRACT LOGICAL WITH BORROW (32)	SLBR	B999	RRE	C	7-402
B99A	EXTRACT PRIMARY ASN AND INSTANCE	EPAIR	B99A	RRE	Q S0	10-24
B99B	EXTRACT SECONDARY ASN AND INSTANCE	ESAIR	B99B	RRE	Q S0	10-25
B99D	EXTRACT AND SET EXTENDED AUTHORITY	ESEA	B99D	RRE	P	10-24
B99E	PROGRAM TRANSFER WITH INSTANCE	PTI	B99E	RRE	Q A1 * SP Z6 T ¢ B	10-114
B99F	SET SECONDARY ASN WITH INSTANCE	SSAIR	B99F	RRE	x1 A1 * Z7 T ¢	10-135
B9A1	TEST PENDING EXTERNAL INTERRUPTION	TPEI	B9A1	RRE	C P	10-181
B9A2	PERFORM TOPOLOGY FUNCTION	PTF	B9A2	RRE	C P SP	10-96
B9AA	LOAD PAGE TABLE ENTRY ADDRESS	LPTEA	B9AA	RRF-b	C P A1 * SP S0 R2	10-52
B9AC	INSERT REFERENCE BITS MULTIPLE	IRBM	B9AC	RRE	P A1 *	10-30
B9AE	RESET REFERENCE BITS MULTIPLE	RRBM	B9AE	RRE	P A1 *	10-127
B9AF	PERFORM FRAME MANAGEMENT FUNCTION	PFMF	B9AF	RRE	P A1 SP IS ¢3 K	10-84
B9B0	CONVERT UTF-8 TO UTF-32	CU14	B9B0	RRF-c	C x5,9 A SP IC ST R1 R2	7-251
B9B1	CONVERT UTF-16 TO UTF-32	CU24	B9B1	RRF-c	C x5,9 A SP IC ST R1 R2	7-234
B9B2	CONVERT UTF-32 TO UTF-8	CU41	B9B2	RRE	C x5,9 A SP IC ST R1 R2	7-244
B9B3	CONVERT UTF-32 TO UTF-16	CU42	B9B3	RRE	C x5,9 A SP IC ST R1 R2	7-241
B9BD	TRANSLATE AND TEST REVERSE EXTENDED	TRTRE	B9BD	RRF-c	C x9 A SP IC ST RM	7-415
B9BE	SEARCH STRING UNICODE	SRSTU	B9BE	RRE	C x9 A SP IC G0 R1 R2	7-378
B9BF	TRANSLATE AND TEST EXTENDED	TRTE	B9BF	RRF-c	C x9 A SP IC ST RM	7-415
B9C0	SELECT HIGH (32)	SELFHR	B9C0	RRF-a		7-380
B9C8	ADD HIGH (32)	AHHHR	B9C8	RRF-a	C IF	7-28
B9C9	SUBTRACT HIGH (32)	SHHHR	B9C9	RRF-a	C IF	7-400
B9CA	ADD LOGICAL HIGH (32)	ALHHHR	B9CA	RRF-a	C	7-30
B9CB	SUBTRACT LOGICAL HIGH (32)	SLHHHR	B9CB	RRF-a	C	7-402
B9CD	COMPARE HIGH (32)	CHHR	B9CD	RRE	C	7-152
B9CF	COMPARE LOGICAL HIGH (32)	CLHHR	B9CF	RRE	C	7-158
B9D8	ADD HIGH (32)	AHHLR	B9D8	RRF-a	C IF	7-28
B9D9	SUBTRACT HIGH (32)	SHHLR	B9D9	RRF-a	C IF	7-400
B9DA	ADD LOGICAL HIGH (32)	ALHHLR	B9DA	RRF-a	C	7-30
B9DB	SUBTRACT LOGICAL HIGH (32)	SLHHLR	B9DB	RRF-a	C	7-402
B9DD	COMPARE HIGH (32)	CHLR	B9DD	RRE	C	7-152
B9DF	COMPARE LOGICAL HIGH (32)	CLHLR	B9DF	RRE	C	7-158
B9E0	LOAD HIGH ON CONDITION (32)	LOCFHR	B9E0	RRF-c		7-287
B9E1	POPULATION COUNT	POPCNT	B9E1	RRF-c	C	7-369
B9E2	LOAD ON CONDITION (64)	LOCGR	B9E2	RRF-c		7-287
B9E3	SELECT (64)	SELGR	B9E3	RRF-a		7-380
B9E4	AND (64)	NGRK	B9E4	RRF-a	C	7-33
B9E5	AND WITH COMPLEMENT(64)	NCGRK	B9E5	RRF-a	C	7-34
B9E6	OR (64)	OGRK	B9E6	RRF-a	C	7-315
B9E7	EXCLUSIVE OR (64)	XGRK	B9E7	RRF-a	C	7-257
B9E8	ADD (64)	AGRK	B9E8	RRF-a	C IF	7-26
B9E9	SUBTRACT (64)	SGRK	B9E9	RRF-a	C IF	7-399
B9EA	ADD LOGICAL (64)	ALGRK	B9EA	RRF-a	C	7-29
B9EB	SUBTRACT LOGICAL (64)	SLGRK	B9EB	RRF-a	C	7-401

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
B9EC	MULTIPLY (128 <- 64)	MGRK	B9EC	RRF-a	SP	7-307
B9ED	MULTIPLY SINGLE (64)	MSGRKC	B9ED	RRF-a	C IF	7-310
B9F0	SELECT (32)	SELR	B9F0	RRF-a		7-380
B9F2	LOAD ON CONDITION (32)	LOCR	B9F2	RRF-c		7-287
B9F4	AND (32)	NRK	B9F4	RRF-a	C	7-33
B9F5	AND WITH COMPLEMENT(32)	NCRK	B9F5	RRF-a	C	7-34
B9F6	OR (32)	ORK	B9F6	RRF-a	C	7-315
B9F7	EXCLUSIVE OR (32)	XRK	B9F7	RRF-a	C	7-257
B9F8	ADD (32)	ARK	B9F8	RRF-a	C IF	7-26
B9F9	SUBTRACT (32)	SRK	B9F9	RRF-a	C IF	7-399
B9FA	ADD LOGICAL (32)	ALRK	B9FA	RRF-a	C	7-29
B9FB	SUBTRACT LOGICAL (32)	SLRK	B9FB	RRF-a	C	7-401
B9FD	MULTIPLY SINGLE (32)	MSRKC	B9FD	RRF-a	C IF	7-310
C000	LOAD ADDRESS RELATIVE LONG	LARL	C000	RIL-b		7-270
C001	LOAD IMMEDIATE (64 <- 32)	LGFI	C001	RIL-a		7-267
C004	BRANCH RELATIVE ON CONDITION LONG	BRCL	C004	RIL-c	x10 B	7-46
C005	BRANCH RELATIVE AND SAVE LONG	BRASL	C005	RIL-b	x9 B	7-45
C006	EXCLUSIVE OR IMMEDIATE (high)	XIHF	C006	RIL-a	C	7-259
C007	EXCLUSIVE OR IMMEDIATE (low)	XILF	C007	RIL-a	C	7-259
C008	INSERT IMMEDIATE (high)	IIHF	C008	RIL-a		7-266
C009	INSERT IMMEDIATE (low)	IILF	C009	RIL-a		7-266
C00A	AND IMMEDIATE (high)	NIHF	C00A	RIL-a	C	7-34
C00B	AND IMMEDIATE (low)	NILF	C00B	RIL-a	C	7-34
C00C	OR IMMEDIATE (high)	OIHF	C00C	RIL-a	C	7-316
C00D	OR IMMEDIATE (low)	OILF	C00D	RIL-a	C	7-316
C00E	LOAD LOGICAL IMMEDIATE (high)	LLIHF	C00E	RIL-a		7-284
C00F	LOAD LOGICAL IMMEDIATE (low)	LLILF	C00F	RIL-a		7-284
C200	MULTIPLY SINGLE IMMEDIATE (64 <- 32)	MSGFI	C200	RIL-a		7-311
C201	MULTIPLY SINGLE IMMEDIATE (32)	MSFI	C201	RIL-a		7-311
C204	SUBTRACT LOGICAL IMMEDIATE (64 <- 32)	SLGFI	C204	RIL-a	C	7-401
C205	SUBTRACT LOGICAL IMMEDIATE (32)	SLFI	C205	RIL-a	C	7-401
C208	ADD IMMEDIATE (64 <- 32)	AGFI	C208	RIL-a	C IF	7-26
C209	ADD IMMEDIATE (32)	AFI	C209	RIL-a	C IF	7-26
C20A	ADD LOGICAL IMMEDIATE (64 <- 32)	ALGFI	C20A	RIL-a	C	7-30
C20B	ADD LOGICAL IMMEDIATE (32)	ALFI	C20B	RIL-a	C	7-30
C20C	COMPARE IMMEDIATE (64 <- 32)	CGFI	C20C	RIL-a	C	7-136
C20D	COMPARE IMMEDIATE (32)	CFI	C20D	RIL-a	C	7-136
C20E	COMPARE LOGICAL IMMEDIATE (64 <- 32)	CLGFI	C20E	RIL-a	C	7-153
C20F	COMPARE LOGICAL IMMEDIATE (32)	CLFI	C20F	RIL-a	C	7-153
C402	LOAD LOGICAL HALFWORD RELATIVE LONG (32 <- 16)	LLHRL	C402	RIL-b	A*	7-283
C404	LOAD HALFWORD RELATIVE LONG (64 <- 16)	LGHRL	C404	RIL-b	A*	7-279
C405	LOAD HALFWORD RELATIVE LONG (32 <- 16)	LHRL	C405	RIL-b	A*	7-279
C406	LOAD LOGICAL HALFWORD RELATIVE LONG (64 <- 16)	LLGHRL	C406	RIL-b	A*	7-283
C407	STORE HALFWORD RELATIVE LONG (16)	STHRL	C407	RIL-b	A* ST	7-395
C408	LOAD RELATIVE LONG (64)	LGRL	C408	RIL-b	A* SP	7-267
C40B	STORE RELATIVE LONG (64)	STGRL	C40B	RIL-b	A* SP ST	7-388
C40C	LOAD RELATIVE LONG (64 <- 32)	LGFR	C40C	RIL-b	A* SP	7-267
C40D	LOAD RELATIVE LONG (32)	LRL	C40D	RIL-b	A SP	7-267
C40E	LOAD LOGICAL RELATIVE LONG (64 <- 32)	LLGFRL	C40E	RIL-b	A* SP	7-281
C40F	STORE RELATIVE LONG (32)	STR	C40F	RIL-b	A* SP ST	7-388
C600	EXECUTE RELATIVE LONG	EXRL	C600	RIL-b	x9 AI* EX	7-259
C602	PREFETCH DATA RELATIVE LONG	PFDR	C602	RIL-c	x9,11	7-370
C604	COMPARE HALFWORD RELATIVE LONG (64 <- 16)	CGHRL	C604	RIL-b	C A*	7-151
C605	COMPARE HALFWORD RELATIVE LONG (32 <- 16)	CHRL	C605	RIL-b	C A*	7-151

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
C606	COMPARE LOGICAL RELATIVE LONG (64 <- 16)	CLGHRL	C606	RIL-b	C A*	7-154
C607	COMPARE LOGICAL RELATIVE LONG (32 <- 16)	CLHRL	C607	RIL-b	C A*	7-154
C608	COMPARE RELATIVE LONG (64)	CGRL	C608	RIL-b	C A* SP	7-136
C60A	COMPARE LOGICAL RELATIVE LONG (64)	CLGRL	C60A	RIL-b	C A* SP	7-154
C60C	COMPARE RELATIVE LONG (64 <- 32)	CGFRL	C60C	RIL-b	C A* SP	7-136
C60D	COMPARE RELATIVE LONG (32)	CRL	C60D	RIL-b	C A* SP	7-136
C60E	COMPARE LOGICAL RELATIVE LONG (64 <- 32)	CLGFRL	C60E	RIL-b	C A* SP	7-154
C60F	COMPARE LOGICAL RELATIVE LONG (32)	CLRL	C60F	RIL-b	C A* SP	7-154
C800	MOVE WITH OPTIONAL SPECIFICATIONS	MVCOS	C800	SSF	C Q A SO G0 ST B† B‡	10-72
C801	EXTRACT CPU TIME	ECTG	C801	SSF	‡8,9 A GM R3 B1 B2	7-263
C802	COMPARE AND SWAP AND STORE	CSST	C802	SSF	C ‡1 A SP \$ GM ST B1 B2	7-147
C804	LOAD PAIR DISJOINT (32)	LPD	C804	SSF	C ‡9 A SP B1 B2	7-288
C805	LOAD PAIR DISJOINT (64)	LPDG	C805	SSF	C ‡9 A SP B1 B2	7-288
CC06	BRANCH RELATIVE ON COUNT HIGH (32)	BRCTH	CC06	RIL-b	‡9 B	7-47
CC08	ADD IMMEDIATE HIGH (32)	AIH	CC08	RIL-a	C IF	7-29
CC0A	ADD LOGICAL WITH SIGNED IMMEDIATE HIGH (32)	ALSIH	CC0A	RIL-a	C	7-32
CC0B	ADD LOGICAL WITH SIGNED IMMEDIATE HIGH (32)	ALSIHN	CC0B	RIL-a	C	7-32
CC0D	COMPARE IMMEDIATE HIGH (32)	CIH	CC0D	RIL-a	C	7-152
CC0F	COMPARE LOGICAL IMMEDIATE HIGH (32)	CLIH	CC0F	RIL-a	C	7-159
E302	LOAD AND TEST (64)	LTG	E302	RXY-a	C A B2	7-273
E303	LOAD REAL ADDRESS (64)	LRAG	E303	RXY-a	C P A1 * BP	10-58
E304	LOAD (64)	LG	E304	RXY-a	A B2	7-267
E306	CONVERT TO BINARY (32)	CVBY	E306	RXY-a	‡9 A Dg IK B2	7-232
E308	ADD (64)	AG	E308	RXY-a	C A IF B2	7-26
E309	SUBTRACT (64)	SG	E309	RXY-a	C A IF B2	7-399
E30A	ADD LOGICAL (64)	ALG	E30A	RXY-a	C A B2	7-29
E30B	SUBTRACT LOGICAL (64)	SLG	E30B	RXY-a	C A B2	7-401
E30C	MULTIPLY SINGLE (64)	MSG	E30C	RXY-a	A B2	7-310
E30D	DIVIDE SINGLE (64)	DSG	E30D	RXY-a	‡9 A SP IK B2	7-257
E30E	CONVERT TO BINARY (64)	CVBG	E30E	RXY-a	‡9 A Dg IK B2	7-232
E30F	LOAD REVERSED (64)	LRVG	E30F	RXY-a	A B2	7-290
E312	LOAD AND TEST (32)	LT	E312	RXY-a	C A B2	7-273
E313	LOAD REAL ADDRESS (32)	LRAY	E313	RXY-a	C P A1 * SO BP	10-58
E314	LOAD (64 <- 32)	LGF	E314	RXY-a	A B2	7-267
E315	LOAD HALFWORD (64 <- 16)	LGH	E315	RXY-a	A B2	7-279
E316	LOAD LOGICAL (64 <- 32)	LLGF	E316	RXY-a	A B2	7-281
E317	LOAD LOGICAL THIRTY ONE BITS (64 <- 31)	LLGT	E317	RXY-a	A B2	7-284
E318	ADD (64 <- 32)	AGF	E318	RXY-a	C A IF B2	7-26
E319	SUBTRACT (64 <- 32)	SGF	E319	RXY-a	C A IF B2	7-399
E31A	ADD LOGICAL (64 <- 32)	ALGF	E31A	RXY-a	C A B2	7-29
E31B	SUBTRACT LOGICAL (64 <- 32)	SLGF	E31B	RXY-a	C A B2	7-401
E31C	MULTIPLY SINGLE (64 <- 32)	MSGF	E31C	RXY-a	A B2	7-311
E31D	DIVIDE SINGLE (64 <- 32)	DSGF	E31D	RXY-a	‡9 A SP IK B2	7-257
E31E	LOAD REVERSED (32)	LRV	E31E	RXY-a	A B2	7-290
E31F	LOAD REVERSED (16)	LRVH	E31F	RXY-a	A B2	7-290
E320	COMPARE (64)	CG	E320	RXY-a	C A B2	7-136
E321	COMPARE LOGICAL (64)	CLG	E321	RXY-a	C A B2	7-153
E324	STORE (64)	STG	E324	RXY-a	A ST B2	7-388
E325	NONTRANSACTIONAL STORE (64)	NTSTG	E325	RXY-a	‡9 A SP ST B2	7-314
E326	CONVERT TO DECIMAL (32)	CVDY	E326	RXY-a	‡9 A ST B2	7-233
E32A	LOAD AND ZERO RIGHTMOST BYTE (64)	LZRG	E32A	RXY-a	A B2	7-274
E32E	CONVERT TO DECIMAL (64)	CVDG	E32E	RXY-a	‡9 A ST B2	7-233
E32F	STORE REVERSED (64)	STRVG	E32F	RXY-a	A ST B2	7-398
E330	COMPARE (64 <- 32)	CGF	E330	RXY-a	C A B2	7-136

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
E331	COMPARE LOGICAL (64 <- 32)	CLGF	E331	RXY-a	C A B2	7-153
E332	LOAD AND TEST (64 <- 32)	LTGF	E332	RXY-a	C A B2	7-274
E334	COMPARE HALFWORD (64 <- 16)	CGH	E334	RXY-a	C A B2	7-151
E336	PREFETCH DATA	PFDF	E336	RXY-b	9,11 B2	7-370
E338	ADD HALFWORD (64 <- 16)	AGH	E338	RXY-a	C A IF B2	7-28
E339	SUBTRACT HALFWORD (64 <- 16)	SGH	E339	RXY-a	C A IF B2	7-400
E33A	LOAD LOGICAL AND ZERO RIGHTMOST BYTE (64 <- 32)	LLZRGF	E33A	RXY-a	A B2	7-282
E33B	LOAD AND ZERO RIGHTMOST BYTE (32)	LZRF	E33B	RXY-a	A B2	7-274
E33C	MULTIPLY HALFWORD (64 <- 16)	MGH	E33C	RXY-a	A B2	7-308
E33E	STORE REVERSED (32)	STRV	E33E	RXY-a	A ST B2	7-398
E33F	STORE REVERSED (16)	STRVH	E33F	RXY-a	A ST B2	7-398
E346	BRANCH ON COUNT (64)	BCTG	E346	RXY-a	9 B	7-41
E347	BRANCH INDIRECT ON CONDITION	BIC	E347	RXY-b	9 A B B2	7-39
E348	LOAD LOGICAL AND SHIFT GUARDED (64 <- 32)	LLGFSG	E348	RXY-a	12 A SP B ST B2	7-276
E349	STORE GUARDED STORAGE CONTROLS	STGSC	E349	RXY-a	1 A SO ST B2	7-395
E34C	LOAD GUARDED (64)	LGG	E34C	RXY-a	12 A SP B ST B2	7-276
E34D	LOAD GUARDED STORAGE CONTROLS	LGSC	E34D	RXY-a	1 A SO B2	7-278
E350	STORE (32)	STY	E350	RXY-a	A ST B2	7-388
E351	MULTIPLY SINGLE (32)	MSY	E351	RXY-a	A B2	7-310
E353	MULTIPLY SINGLE (32)	MSC	E353	RXY-a	C A IF B2	7-310
E354	AND (32)	NY	E354	RXY-a	C A B2	7-33
E355	COMPARE LOGICAL (32)	CLY	E355	RXY-a	C A B2	7-153
E356	OR (32)	OY	E356	RXY-a	C A B2	7-315
E357	EXCLUSIVE OR (32)	XY	E357	RXY-a	C A B2	7-257
E358	LOAD (32)	LY	E358	RXY-a	A B2	7-267
E359	COMPARE (32)	CY	E359	RXY-a	C A B2	7-136
E35A	ADD (32)	AY	E35A	RXY-a	C A IF B2	7-26
E35B	SUBTRACT (32)	SY	E35B	RXY-a	C A IF B2	7-399
E35C	MULTIPLY (64 <- 32)	MFY	E35C	RXY-a	A SP B2	7-307
E35E	ADD LOGICAL (32)	ALY	E35E	RXY-a	C A B2	7-29
E35F	SUBTRACT LOGICAL (32)	SLY	E35F	RXY-a	C A B2	7-401
E370	STORE HALFWORD (16)	STHY	E370	RXY-a	A ST B2	7-395
E371	LOAD ADDRESS	LAY	E371	RXY-a		7-269
E372	STORE CHARACTER	STCY	E372	RXY-a	A ST B2	7-389
E373	INSERT CHARACTER	ICY	E373	RXY-a	A B2	7-265
E375	LOAD ADDRESS EXTENDED	LAEY	E375	RXY-a	6 U1 BP	7-269
E376	LOAD BYTE (32 <- 8)	LB	E376	RXY-a	A	7-275
E377	LOAD BYTE (64 <- 8)	LGB	E377	RXY-a	A	7-275
E378	LOAD HALFWORD (32 <- 16)	LHY	E378	RXY-a	A B2	7-278
E379	COMPARE HALFWORD (32 <- 16)	CHY	E379	RXY-a	C A B2	7-151
E37A	ADD HALFWORD (32 <- 16)	AHY	E37A	RXY-a	C A IF B2	7-28
E37B	SUBTRACT HALFWORD (32 <- 16)	SHY	E37B	RXY-a	C A IF B2	7-400
E37C	MULTIPLY HALFWORD (32 <- 16)	MHY	E37C	RXY-a	A B2	7-308
E380	AND (64)	NG	E380	RXY-a	C A B2	7-33
E381	OR (64)	OG	E381	RXY-a	C A B2	7-315
E382	EXCLUSIVE OR (64)	XG	E382	RXY-a	C A B2	7-257
E383	MULTIPLY SINGLE (64)	MSGC	E383	RXY-a	C A IF B2	7-310
E384	MULTIPLY (128 <- 64)	MG	E384	RXY-a	A SP B2	7-307
E385	LOAD AND TRAP (64)	LGAT	E385	RXY-a	A Dc B2	7-274
E386	MULTIPLY LOGICAL (128 <- 64)	MLG	E386	RXY-a	A SP B2	7-310
E387	DIVIDE LOGICAL (64 <- 128)	DLG	E387	RXY-a	9 A SP IK B2	7-256
E388	ADD LOGICAL WITH CARRY (64)	ALCG	E388	RXY-a	C A B2	7-31
E389	SUBTRACT LOGICAL WITH BORROW (64)	SLBG	E389	RXY-a	C A B2	7-402
E38E	STORE PAIR TO QUADWORD	STPQ	E38E	RXY-a	9 A SP ST B2	7-398

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
E38F	LOAD PAIR FROM QUADWORD (64&64 <- 128)	LPQ	E38F	RXY-a	⌘9 A SP B2	7-289
E390	LOAD LOGICAL CHARACTER (64 <- 8)	LLGC	E390	RXY-a	A B2	7-282
E391	LOAD LOGICAL HALFWORD (64 <- 16)	LLGH	E391	RXY-a	A B2	7-283
E394	LOAD LOGICAL CHARACTER (32 <- 8)	LLC	E394	RXY-a	A B2	7-282
E395	LOAD LOGICAL HALFWORD (32 <- 16)	LLH	E395	RXY-a	A B2	7-283
E396	MULTIPLY LOGICAL (64 <- 32)	ML	E396	RXY-a	A SP B2	7-309
E397	DIVIDE LOGICAL (32 <- 64)	DL	E397	RXY-a	⌘9 A SP IK B2	7-256
E398	ADD LOGICAL WITH CARRY (32)	ALC	E398	RXY-a	C A B2	7-31
E399	SUBTRACT LOGICAL WITH BORROW (32)	SLB	E399	RXY-a	C A B2	7-402
E39C	LOAD LOGICAL THIRTY ONE BITS AND TRAP (64 <- 31)	LLGTAT	E39C	RXY-a	A Dc B2	7-284
E39D	LOAD LOGICAL AND TRAP (64 <- 32)	LLGFAT	E39D	RXY-a	A Dc B2	7-281
E39F	LOAD AND TRAP (32L <- 32)	LAT	E39F	RXY-a	A Dc B2	7-274
E3C0	LOAD BYTE HIGH (32 <- 8)	LBH	E3C0	RXY-a	A B2	7-275
E3C2	LOAD LOGICAL CHARACTER HIGH (32 <- 8)	LLCH	E3C2	RXY-a	A B2	7-282
E3C3	STORE CHARACTER HIGH (8)	STCH	E3C3	RXY-a	A ST B2	7-389
E3C4	LOAD HALFWORD HIGH (32 <- 16)	LHH	E3C4	RXY-a	A B2	7-279
E3C6	LOAD LOGICAL HALFWORD HIGH (32 <- 16)	LLHH	E3C6	RXY-a	A B2	7-283
E3C7	STORE HALFWORD HIGH (16)	STHH	E3C7	RXY-a	A ST B2	7-396
E3C8	LOAD HIGH AND TRAP (32H <- 32)	LFHAT	E3C8	RXY-a	A Dc B2	7-281
E3CA	LOAD HIGH (32)	LFH	E3CA	RXY-a	A B2	7-280
E3CB	STORE HIGH (32)	STFH	E3CB	RXY-a	A ST B2	7-396
E3CD	COMPARE HIGH (32)	CHF	E3CD	RXY-a	C A B2	7-152
E3CF	COMPARE LOGICAL HIGH (32)	CLHF	E3CF	RXY-a	C A B2	7-158
E500	LOAD ADDRESS SPACE PARAMETERS	LASP	E500	SSE	C P A1 SP SO B1	10-42
E501	TEST PROTECTION	TPROT	E501	SSE	C P A1 * SO B1	10-181
E502	STORE REAL ADDRESS	STRAG	E502	SSE	P A1 SP ST B1 BP	10-150
E50A	MOVE RIGHT TO LEFT	MVCRL	E50A	SSE	⌘9 A G0 ST B1 B2	7-304
E50E	MOVE WITH SOURCE KEY	MVCSK	E50E	SSE	Q A SO GM ST B1 B2	10-75
E50F	MOVE WITH DESTINATION KEY	MVCDK	E50F	SSE	Q A SO GM ST B1 B2	10-69
E544	MOVE (16 <- 16)	MVHHI	E544	SIL	A ST B1	7-292
E548	MOVE (64 <- 16)	MVGHI	E548	SIL	A ST B1	7-292
E54C	MOVE (32 <- 16)	MVHI	E54C	SIL	A ST B1	7-292
E554	COMPARE HALFWORD IMMEDIATE (16 <- 16)	CHHSI	E554	SIL	C A B1	7-151
E555	COMPARE LOGICAL IMMEDIATE (16 <- 16)	CLHHSI	E555	SIL	C A B1	7-153
E558	COMPARE HALFWORD IMMEDIATE (64 <- 16)	CGHSI	E558	SIL	C A B1	7-151
E559	COMPARE LOGICAL IMMEDIATE (64 <- 16)	CLGHSI	E559	SIL	C A B1	7-153
E55C	COMPARE HALFWORD IMMEDIATE (32 <- 16)	CHSI	E55C	SIL	C A B1	7-151
E55D	COMPARE LOGICAL IMMEDIATE (32 <- 16)	CLFHSI	E55D	SIL	C A B1	7-153
E560	TRANSACTION BEGIN (nonconstrained)	TBEGIN	E560	SIL	C ⌘9 A SP SO \$ EX ST	7-406
E561	TRANSACTION BEGIN (constrained)	TBEGINC	E561	SIL	C ⌘9 SP SO \$ EX	7-410
E601	VECTOR LOAD BYTE REVERSED ELEMENT (16)	VLEBRH	E601	VRX	⌘7,9 A SP Dv B2	21-7
E602	VECTOR LOAD BYTE REVERSED ELEMENT (64)	VLEBRG	E602	VRX	⌘7,9 A SP Dv B2	21-7
E603	VECTOR LOAD BYTE REVERSED ELEMENT (32)	VLEBRF	E603	VRX	⌘7,9 A SP Dv B2	21-7
E604	VECTOR LOAD BYTE REVERSED ELEMENT AND ZERO	VLLEBRZ	E604	VRX	⌘7,9 A SP Dv B2	21-8
E605	VECTOR LOAD BYTE REVERSED ELEMENT AND REPLICATE	VLBRREP	E605	VRX	⌘7,9 A SP Dv B2	21-8
E606	VECTOR LOAD BYTE REVERSED ELEMENTS	VLBR	E606	VRX	⌘7,9 A SP Dv B2	21-9
E607	VECTOR LOAD ELEMENTS REVERSED	VLER	E607	VRX	⌘7,9 A SP Dv B2	21-10
E609	VECTOR STORE BYTE REVERSED ELEMENT (16)	VSTEBRH	E609	VRX	⌘7,9 A SP Dv ST B2	21-22
E60A	VECTOR STORE BYTE REVERSED ELEMENT (64)	VSTEBRG	E60A	VRX	⌘7,9 A SP Dv ST B2	21-22
E60B	VECTOR STORE BYTE REVERSED ELEMENT (32)	VSTEBRF	E60B	VRX	⌘7,9 A SP Dv ST B2	21-22
E60E	VECTOR STORE BYTE REVERSED ELEMENTS	VSTBR	E60E	VRX	⌘7,9 A SP Dv ST B2	21-22
E60F	VECTOR STORE ELEMENTS REVERSED	VSTER	E60F	VRX	⌘7,9 A SP Dv ST B2	21-24
E634	VECTOR PACK ZONED	VPKZ	E634	VSI	⌘7,9 A SP Dv B2	25-17
E635	VECTOR LOAD RIGHTMOST WITH LENGTH	VLRL	E635	VSI	⌘7,9 A SP Dv B2	21-13

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
E637	VECTOR LOAD RIGHTMOST WITH LENGTH	VLRLR	E637	VRS-d	α7,9 A Dv B2	21-13
E63C	VECTOR UNPACK ZONED	VUPKZ	E63C	VSI	α7,9 A SP Dv ST B2	25-30
E63D	VECTOR STORE RIGHTMOST WITH LENGTH	VSTRL	E63D	VSI	α7,9 A SP Dv ST B2	21-25
E63F	VECTOR STORE RIGHTMOST WITH LENGTH	VSTRLR	E63F	VRS-d	α7,9 A Dv ST B2	21-25
E649	VECTOR LOAD IMMEDIATE DECIMAL	VLIP	E649	VRI-h	α7,9 Dv Dg	25-14
E650	VECTOR CONVERT TO BINARY	VCVB	E650	VRR-i	C* α7,9 Dv Dg IF*	25-8
E651	VECTOR COUNT LEADING ZERO DIGITS	VCLZDP	E651	VRR-k	C* α7,9 Dv Dg	25-11
E652	VECTOR CONVERT TO BINARY	VCVBG	E652	VRR-i	C* α7,9 Dv Dg IF*	25-8
E654	VECTOR UNPACK ZONED HIGH	VUPKZH	E654	VRR-k	α7,9 Dv Dg	25-30
E655	VECTOR FP CONVERT TO NNP	VCNF	E655	VRR-a	α7,9 Dv Xi Xu Xx	26-124
E656	VECTOR FP CONVERT AND LENGTHEN FROM NNP HIGH	VCLFNH	E656	VRR-a	α7,9 Dv Xi Xx	26-121
E658	VECTOR CONVERT TO DECIMAL	VCVD	E658	VRI-i	C* α7,9 SP Dv DF*	25-10
E659	VECTOR SHIFT AND ROUND DECIMAL	VSRP	E659	VRI-g	C* α7,9 SP Dv Dg DF*	25-24
E65A	VECTOR CONVERT TO DECIMAL	VCVDG	E65A	VRI-i	C* α7,9 SP Dv DF*	25-10
E65B	VECTOR PERFORM SIGN OPERATION DECIMAL	VPSOP	E65B	VRI-g	C* α7,9 SP Dv Dg DF*	25-19
E65C	VECTOR UNPACK ZONED LOW	VUPKZL	E65C	VRR-k	α7,9 Dv Dg	25-31
E65D	VECTOR FP CONVERT FROM NNP	VCFN	E65D	VRR-a	α7,9 Dv Xi Xo Xu Xx	26-123
E65E	VECTOR FP CONVERT AND LENGTHEN FROM NNP LOW	VCLFNL	E65E	VRR-a	α7,9 Dv Xi Xx	26-122
E65F	VECTOR TEST DECIMAL	VTP	E65F	VRR-g	C α7,9 Dv	25-29
E670	VECTOR PACK ZONED REGISTER	VPKZR	E670	VRI-f	C* α7,9 SP Dv Dg DF*	25-18
E671	VECTOR ADD DECIMAL	VAP	E671	VRI-f	C* α7,9 SP Dv Dg DF*	25-6
E672	VECTOR SHIFT AND ROUND DECIMAL REGISTER	VSRPR	E672	VRI-f	C* α7,9 SP Dv Dg DF*	25-26
E673	VECTOR SUBTRACT DECIMAL	VSP	E673	VRI-f	C* α7,9 SP Dv Dg DF*	25-28
E674	DECIMAL SCALE AND CONVERT TO HFP	VSCHP	E674	VRR-b	α7,9 SP Dv Dg	25-5
E675	VECTOR FP CONVERT AND ROUND TO NNP	VCRNF	E675	VRR-c	α7,9 Dv Xi Xo Xu Xx	26-123
E677	VECTOR COMPARE DECIMAL	VCP	E677	VRR-h	C α7,9 Dv Dg	25-7
E678	VECTOR MULTIPLY DECIMAL	VMP	E678	VRI-f	C* α7,9 SP Dv Dg DF*	25-14
E679	VECTOR MULTIPLY AND SHIFT DECIMAL	VMSP	E679	VRI-f	C* α7,9 SP Dv Dg DF*	25-16
E67A	VECTOR DIVIDE DECIMAL	VDP	E67A	VRI-f	C* α7,9 SP Dv Dg DF* DK	25-12
E67B	VECTOR REMAINDER DECIMAL	VRP	E67B	VRI-f	C* α7,9 SP Dv Dg DF* DK	25-22
E67C	DECIMAL SCALE AND CONVERT AND SPLIT TO HFP	VSCSHP	E67C	VRR-b	α7,9 Dv Dg	25-4
E67D	VECTOR CONVERT HFP TO SCALED DECIMAL	VCSHP	E67D	VRR-j	α7,9 Dv	25-11
E67E	VECTOR SHIFT AND DIVIDE DECIMAL	VSDP	E67E	VRI-f	C* α7,9 SP Dv Dg DF* DK	25-23
E700	VECTOR LOAD ELEMENT (8)	VLEB	E700	VRX	α7,9 A SP Dv B2	21-9
E701	VECTOR LOAD ELEMENT (16)	VLEH	E701	VRX	α7,9 A SP Dv B2	21-9
E702	VECTOR LOAD ELEMENT (64)	VLEG	E702	VRX	α7,9 A SP Dv B2	21-9
E703	VECTOR LOAD ELEMENT (32)	VLEF	E703	VRX	α7,9 A SP Dv B2	21-9
E704	VECTOR LOAD LOGICAL ELEMENT AND ZERO	VLLEZ	E704	VRX	α7,9 A SP Dv B2	21-12
E705	VECTOR LOAD AND REPLICATE	VLREP	E705	VRX	α7,9 A SP Dv B2	21-7
E706	VECTOR LOAD	VL	E706	VRX	α7,9 A Dv B2	21-6
E707	VECTOR LOAD TO BLOCK BOUNDARY	VLBB	E707	VRX	α7,9 A SP Dv B2	21-14
E708	VECTOR STORE ELEMENT (8)	VSTEB	E708	VRX	α7,9 A SP Dv ST B2	21-23
E709	VECTOR STORE ELEMENT (16)	VSTEH	E709	VRX	α7,9 A SP Dv ST B2	21-23
E70A	VECTOR STORE ELEMENT (64)	VSTEG	E70A	VRX	α7,9 A SP Dv ST B2	21-23
E70B	VECTOR STORE ELEMENT (32)	VSTEF	E70B	VRX	α7,9 A SP Dv ST B2	21-23
E70E	VECTOR STORE	VST	E70E	VRX	α7,9 A Dv ST B2	21-21
E712	VECTOR GATHER ELEMENT (64)	VGEG	E712	VRV	α7,9 A SP Dv B2	21-5
E713	VECTOR GATHER ELEMENT (32)	VGEF	E713	VRV	α7,9 A SP Dv B2	21-5
E71A	VECTOR SCATTER ELEMENT (64)	VSCEG	E71A	VRV	α7,9 A SP Dv ST B2	21-20
E71B	VECTOR SCATTER ELEMENT (32)	VSCEF	E71B	VRV	α7,9 A SP Dv ST B2	21-20
E721	VECTOR LOAD GR FROM VR ELEMENT	VLGV	E721	VRS-c	α7,9 SP Dv	21-11
E722	VECTOR LOAD VR ELEMENT FROM GR	VLVG	E722	VRS-b	α7,9 SP Dv	21-14
E727	LOAD COUNT TO BLOCK BOUNDARY	LCBB	E727	RXE	C SP	7-276
E730	VECTOR ELEMENT SHIFT LEFT	VESL	E730	VRS-a	α7,9 SP Dv	22-23

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E733	VECTOR ELEMENT ROTATE LEFT LOGICAL	VERLL	E733	VRS-a	⌘7,9 SP Dv	22-21
E736	VECTOR LOAD MULTIPLE	VLM	E736	VRS-a	⌘7,9 A SP Dv B2	21-12
E737	VECTOR LOAD WITH LENGTH	VLL	E737	VRS-b	⌘7,9 A Dv B2	21-15
E738	VECTOR ELEMENT SHIFT RIGHT LOGICAL	VESRL	E738	VRS-a	⌘7,9 SP Dv	22-24
E73A	VECTOR ELEMENT SHIFT RIGHT ARITHMETIC	VESRA	E73A	VRS-a	⌘7,9 SP Dv	22-23
E73E	VECTOR STORE MULTIPLE	VSTM	E73E	VRS-a	⌘7,9 A SP Dv ST B2	21-24
E73F	VECTOR STORE WITH LENGTH	VSTL	E73F	VRS-b	⌘7,9 A Dv ST B2	21-26
E740	VECTOR LOAD ELEMENT IMMEDIATE (8)	VLEIB	E740	VRI-a	⌘7,9 SP Dv	21-10
E741	VECTOR LOAD ELEMENT IMMEDIATE (16)	VLEIH	E741	VRI-a	⌘7,9 SP Dv	21-10
E742	VECTOR LOAD ELEMENT IMMEDIATE (64)	VLEIG	E742	VRI-a	⌘7,9 SP Dv	21-10
E743	VECTOR LOAD ELEMENT IMMEDIATE (32)	VLEIF	E743	VRI-a	⌘7,9 SP Dv	21-10
E744	VECTOR GENERATE BYTE MASK	VGBM	E744	VRI-a	⌘7,9 Dv	21-5
E745	VECTOR REPLICATE IMMEDIATE	VREPI	E745	VRI-a	⌘7,9 SP Dv	21-20
E746	VECTOR GENERATE MASK	VGM	E746	VRI-b	⌘7,9 SP Dv	21-6
E74A	VECTOR FP TEST DATA CLASS IMMEDIATE	VFTCI	E74A	VRI-e	C ⌘7,9 SP Dv	24-47
E74D	VECTOR REPLICATE	VREP	E74D	VRI-c	⌘7,9 SP Dv	21-19
E750	VECTOR POPULATION COUNT	VPOPCT	E750	VRR-a	⌘7,9 SP Dv	22-21
E752	VECTOR COUNT TRAILING ZEROS	VCTZ	E752	VRR-a	⌘7,9 SP Dv	22-10
E753	VECTOR COUNT LEADING ZEROS	VCLZ	E753	VRR-a	⌘7,9 SP Dv	22-10
E756	VECTOR LOAD	VLR	E756	VRR-a	⌘7,9 Dv	21-6
E75C	VECTOR ISOLATE STRING	VISTR	E75C	VRR-a	C* ⌘7,9 SP Dv	23-5
E75F	VECTOR SIGN EXTEND TO DOUBLEWORD	VSEG	E75F	VRR-a	⌘7,9 SP Dv	21-21
E760	VECTOR MERGE LOW	VMRL	E760	VRR-c	⌘7,9 SP Dv	21-16
E761	VECTOR MERGE HIGH	VMRH	E761	VRR-c	⌘7,9 SP Dv	21-15
E762	VECTOR LOAD VR FROM GRS DISJOINT	VLVGP	E762	VRR-f	⌘7,9 Dv	21-15
E764	VECTOR SUM ACROSS WORD	VSUM	E764	VRR-c	⌘7,9 SP Dv	22-30
E765	VECTOR SUM ACROSS DOUBLEWORD	VSUMG	E765	VRR-c	⌘7,9 SP Dv	22-29
E766	VECTOR CHECKSUM	VCKSM	E766	VRR-c	⌘7,9 Dv	22-6
E767	VECTOR SUM ACROSS QUADWORD	VSUMQ	E767	VRR-c	⌘7,9 SP Dv	22-30
E768	VECTOR AND	VN	E768	VRR-c	⌘7,9 Dv	22-5
E769	VECTOR AND WITH COMPLEMENT	VNC	E769	VRR-c	⌘7,9 Dv	22-5
E76A	VECTOR OR	VO	E76A	VRR-c	⌘7,9 Dv	22-20
E76B	VECTOR NOR	VNO	E76B	VRR-c	⌘7,9 Dv	22-20
E76C	VECTOR NOT EXCLUSIVE OR	VNX	E76C	VRR-c	⌘7,9 Dv	22-20
E76D	VECTOR EXCLUSIVE OR	VX	E76D	VRR-c	⌘7,9 Dv	22-11
E76E	VECTOR NAND	VNN	E76E	VRR-c	⌘7,9 DV	22-20
E76F	VECTOR OR WITH COMPLEMENT	VOC	E76F	VRR-c	⌘7,9 Dv	22-21
E770	VECTOR ELEMENT SHIFT LEFT	VESLV	E770	VRR-c	⌘7,9 SP Dv	22-23
E772	VECTOR ELEMENT ROTATE AND INSERT UNDER MASK	VERIM	E772	VRI-d	⌘7,9 SP Dv	22-22
E773	VECTOR ELEMENT ROTATE LEFT LOGICAL	VERLLV	E773	VRR-c	⌘7,9 SP Dv	22-21
E774	VECTOR SHIFT LEFT	VSL	E774	VRR-c	⌘7,9 Dv	22-25
E775	VECTOR SHIFT LEFT BY BYTE	VSLB	E775	VRR-c	⌘7,9 Dv	22-25
E777	VECTOR SHIFT LEFT DOUBLE BY BYTE	VSLDB	E777	VRI-d	⌘7,9 Dv	22-26
E778	VECTOR ELEMENT SHIFT RIGHT LOGICAL	VESRLV	E778	VRR-c	⌘7,9 SP Dv	22-24
E77A	VECTOR ELEMENT SHIFT RIGHT ARITHMETIC	VESRAV	E77A	VRR-c	⌘7,9 SP Dv	22-23
E77C	VECTOR SHIFT RIGHT LOGICAL	VSRL	E77C	VRR-c	⌘7,9 Dv	22-27
E77D	VECTOR SHIFT RIGHT LOGICAL BY BYTE	VSRLB	E77D	VRR-c	⌘7,9 Dv	22-27
E77E	VECTOR SHIFT RIGHT ARITHMETIC	VSRA	E77E	VRR-c	⌘7,9 Dv	22-26
E77F	VECTOR SHIFT RIGHT ARITHMETIC BY BYTE	VSRAB	E77F	VRR-c	⌘7,9 Dv	22-26
E780	VECTOR FIND ELEMENT EQUAL	VFEE	E780	VRR-b	C* ⌘7,9 SP Dv	23-3
E781	VECTOR FIND ELEMENT NOT EQUAL	VFENE	E781	VRR-b	C* ⌘7,9 SP Dv	23-4
E782	VECTOR FIND ANY ELEMENT EQUAL	VFAE	E782	VRR-b	C* ⌘7,9 SP Dv	23-2
E784	VECTOR PERMUTE DOUBLEWORD IMMEDIATE	VPDI	E784	VRR-c	⌘7,9 Dv	21-19
E785	VECTOR BIT PERMUTE	VBPERM	E785	VRR-c	⌘7,9 Dv	21-4

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E786	VECTOR SHIFT LEFT DOUBLE BY BIT	VSLD	E786	VRI-d	7,9 SP Dv	22-25
E787	VECTOR SHIFT RIGHT DOUBLE BY BIT	VSRD	E787	VRI-d	7,9 SP Dv	22-26
E78A	VECTOR STRING RANGE COMPARE	VSTRC	E78A	VRR-d	C* 7,9 SP Dv	23-6
E78B	VECTOR STRING SEARCH	VSTRS	E78B	VRR-d	C 7,9 SP Dv	23-8
E78C	VECTOR PERMUTE	VPERM	E78C	VRR-e	7,9 Dv	21-18
E78D	VECTOR SELECT	VSEL	E78D	VRR-e	7,9 Dv	21-21
E78E	VECTOR FP MULTIPLY AND SUBTRACT	VFMS	E78E	VRR-e	7,9 SP Dv Xi Xo Xu Xx	24-42
E78F	VECTOR FP MULTIPLY AND ADD	VFMA	E78F	VRR-e	7,9 SP Dv Xi Xo Xu Xx	24-42
E794	VECTOR PACK	VPK	E794	VRR-c	7,9 SP Dv	21-16
E795	VECTOR PACK LOGICAL SATURATE	VPKLS	E795	VRR-b	C* 7,9 SP Dv	21-18
E797	VECTOR PACK SATURATE	VPKS	E797	VRR-b	C* 7,9 SP Dv	21-17
E79E	VECTOR FP NEGATIVE MULTIPLY AND SUBTRACT	VFNMS	E79E	VRR-e	7,9 SP Dv Xi Xo Xu Xx	24-42
E79F	VECTOR FP NEGATIVE MULTIPLY AND ADD	VFNMA	E79F	VRR-e	7,9 SP Dv Xi Xo Xu Xx	24-42
E7A1	VECTOR MULTIPLY LOGICAL HIGH	VMLH	E7A1	VRR-c	7,9 SP Dv	22-17
E7A2	VECTOR MULTIPLY LOW	VML	E7A2	VRR-c	7,9 SP Dv	22-17
E7A3	VECTOR MULTIPLY HIGH	VMH	E7A3	VRR-c	7,9 SP Dv	22-16
E7A4	VECTOR MULTIPLY LOGICAL EVEN	VMLE	E7A4	VRR-c	7,9 SP Dv	22-18
E7A5	VECTOR MULTIPLY LOGICAL ODD	VMLO	E7A5	VRR-c	7,9 SP Dv	22-18
E7A6	VECTOR MULTIPLY EVEN	VME	E7A6	VRR-c	7,9 SP Dv	22-18
E7A7	VECTOR MULTIPLY ODD	VMO	E7A7	VRR-c	7,9 SP Dv	22-18
E7A9	VECTOR MULTIPLY AND ADD LOGICAL HIGH	VMALH	E7A9	VRR-d	7,9 SP Dv	22-15
E7AA	VECTOR MULTIPLY AND ADD LOW	VMAL	E7AA	VRR-d	7,9 SP Dv	22-14
E7AB	VECTOR MULTIPLY AND ADD HIGH	VMAH	E7AB	VRR-d	7,9 SP Dv	22-15
E7AC	VECTOR MULTIPLY AND ADD LOGICAL EVEN	VMALE	E7AC	VRR-d	7,9 SP Dv	22-15
E7AD	VECTOR MULTIPLY AND ADD LOGICAL ODD	VMALO	E7AD	VRR-d	7,9 SP Dv	22-16
E7AE	VECTOR MULTIPLY AND ADD EVEN	VMAE	E7AE	VRR-d	7,9 SP Dv	22-15
E7AF	VECTOR MULTIPLY AND ADD ODD	VMAO	E7AF	VRR-d	7,9 SP Dv	22-16
E7B4	VECTOR GALOIS FIELD MULTIPLY SUM	VGFM	E7B4	VRR-c	7,9 SP Dv	22-11
E7B8	VECTOR MULTIPLY SUM LOGICAL	VMSL	E7B8	VRR-d	7,9 SP Dv	22-19
E7B9	VECTOR ADD WITH CARRY COMPUTE CARRY	VACCC	E7B9	VRR-d	7,9 SP Dv	22-5
E7BB	VECTOR ADD WITH CARRY	VAC	E7BB	VRR-d	7,9 SP Dv	22-4
E7BC	VECTOR GALOIS FIELD MULTIPLY SUM AND ACCUMULATE	VGFMA	E7BC	VRR-d	7,9 SP Dv	22-12
E7BD	VECTOR SUBTRACT WITH BORROW COMPUTE BORROW INDICATION	VSBCBI	E7BD	VRR-d	7,9 SP Dv	22-29
E7BF	VECTOR SUBTRACT WITH BORROW INDICATION	VSBI	E7BF	VRR-d	7,9 SP Dv	22-28
E7C0	VECTOR FP CONVERT TO LOGICAL	VCLFP	E7C0	VRR-a	 7,9 SP Dv Xi Xx	24-20
E7C1	VECTOR FP CONVERT FROM LOGICAL	VCFPL	E7C1	VRR-a	 7,9 SP Dv Xx	24-17
E7C2	VECTOR FP CONVERT TO FIXED	VCSFP	E7C2	VRR-a	 7,9 SP Dv Xi Xx	24-18
E7C3	VECTOR FP CONVERT FROM FIXED	VCFPS	E7C3	VRR-a	 7,9 SP Dv Xx	24-15
E7C4	VECTOR FP LOAD LENGTHENED	VFLL	E7C4	VRR-a	7,9 SP Dv Xi	24-26
E7C5	VECTOR FP LOAD ROUNDED	VFLR	E7C5	VRR-a	7,9 SP Dv Xi Xo Xu Xx	24-27
E7C7	VECTOR LOAD FP INTEGER	VFI	E7C7	VRR-a	7,9 SP Dv Xi Xx	24-24
E7CA	VECTOR FP COMPARE AND SIGNAL SCALAR	WFK	E7CA	VRR-a	C 7,9 SP Dv Xi	24-8
E7CB	VECTOR FP COMPARE SCALAR	WFC	E7CB	VRR-a	C 7,9 SP Dv Xi	24-7
E7CC	VECTOR FP PERFORM SIGN OPERATION	VFPSO	E7CC	VRR-a	7,9 SP Dv	24-44
E7CE	VECTOR FP SQUARE ROOT	VFSQ	E7CE	VRR-a	7,9 SP Dv Xi Xx	24-45
E7D4	VECTOR UNPACK LOGICAL LOW	VUPLL	E7D4	VRR-a	7,9 SP Dv	21-27
E7D5	VECTOR UNPACK LOGICAL HIGH	VUPLH	E7D5	VRR-a	7,9 SP Dv	21-26
E7D6	VECTOR UNPACK LOW	VUPL	E7D6	VRR-a	7,9 SP Dv	21-27
E7D7	VECTOR UNPACK HIGH	VUPH	E7D7	VRR-a	7,9 SP Dv	21-26
E7D8	VECTOR TEST UNDER MASK	VTM	E7D8	VRR-a	C 7,9 Dv	22-31
E7D9	VECTOR ELEMENT COMPARE LOGICAL	VECL	E7D9	VRR-a	C 7,9 SP Dv	22-7
E7DB	VECTOR ELEMENT COMPARE	VEC	E7DB	VRR-a	C 7,9 SP Dv	22-7
E7DE	VECTOR LOAD COMPLEMENT	VLC	E7DE	VRR-a	7,9 SP Dv	22-12
E7DF	VECTOR LOAD POSITIVE	VLP	E7DF	VRR-a	7,9 SP Dv	22-12

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E7E2	VECTOR FP SUBTRACT	VFS	E7E2	VRR-c	7,9 SP Dv Xi Xo Xu Xx	24-46
E7E3	VECTOR FP ADD	VFA	E7E3	VRR-c	7,9 SP Dv Xi Xo Xu Xx	24-4
E7E5	VECTOR FP DIVIDE	VFD	E7E5	VRR-c	7,9 SP Dv Xi Xz Xo Xu Xx	24-22
E7E7	VECTOR FP MULTIPLY	VFM	E7E7	VRR-c	7,9 SP Dv Xi Xo Xu Xx	24-40
E7E8	VECTOR FP COMPARE EQUAL	VFCE	E7E8	VRR-c	C* 7,9 SP Dv Xi	24-9
E7EA	VECTOR FP COMPARE HIGH OR EQUAL	VFCHE	E7EA	VRR-c	C* 7,9 SP Dv Xi	24-13
E7EB	VECTOR FP COMPARE HIGH	VFCH	E7EB	VRR-c	C* 7,9 SP Dv Xi	24-11
E7EE	VECTOR FP MINIMUM	VFMIN	E7EE	VRR-c	7,9 SP Dv Xi	24-34
E7EF	VECTOR FP MAXIMUM	VFMAX	E7EF	VRR-c	7,9 SP Dv Xi	24-28
E7F0	VECTOR AVERAGE LOGICAL	VAVGL	E7F0	VRR-c	7,9 SP Dv	22-6
E7F1	VECTOR ADD COMPUTE CARRY	VACC	E7F1	VRR-c	7,9 SP Dv	22-4
E7F2	VECTOR AVERAGE	VAVG	E7F2	VRR-c	7,9 SP Dv	22-6
E7F3	VECTOR ADD	VA	E7F3	VRR-c	7,9 SP Dv	22-3
E7F5	VECTOR SUBTRACT COMPUTE BORROW INDICATION	VSCBI	E7F5	VRR-c	7,9 SP Dv	22-28
E7F7	VECTOR SUBTRACT	VS	E7F7	VRR-c	7,9 SP Dv	22-27
E7F8	VECTOR COMPARE EQUAL	VCEQ	E7F8	VRR-b	C* 7,9 SP Dv	22-7
E7F9	VECTOR COMPARE HIGH LOGICAL	VCHL	E7F9	VRR-b	C* 7,9 SP Dv	22-9
E7FB	VECTOR COMPARE HIGH	VCH	E7FB	VRR-b	C* 7,9 SP Dv	22-8
E7FC	VECTOR MINIMUM LOGICAL	VMNL	E7FC	VRR-c	7,9 SP Dv	22-14
E7FD	VECTOR MAXIMUM LOGICAL	VMXL	E7FD	VRR-c	7,9 SP Dv	22-13
E7FE	VECTOR MINIMUM	VMN	E7FE	VRR-c	7,9 SP Dv	22-13
E7FF	VECTOR MAXIMUM	VMX	E7FF	VRR-c	7,9 SP Dv	22-13
EB04	LOAD MULTIPLE (64)	LMG	EB04	RSY-a	A B2	7-285
EB0A	SHIFT RIGHT SINGLE (64)	SRAG	EB0A	RSY-a	C	7-386
EB0B	SHIFT LEFT SINGLE (64)	SLAG	EB0B	RSY-a	C IF	7-383
EB0C	SHIFT RIGHT SINGLE LOGICAL (64)	SRLG	EB0C	RSY-a		7-387
EB0D	SHIFT LEFT SINGLE LOGICAL (64)	SLLG	EB0D	RSY-a		7-385
EB0F	TRACE (64)	TRACG	EB0F	RSY-a	P A SP T  B2	10-184
EB14	COMPARE AND SWAP (32)	CSY	EB14	RSY-a	C 9 A SP \$ ST B2	7-145
EB17	STORE CPU COUNTER MULTIPLE	STCCTM	EB17	RSY-b	C ?? ??	2-33
EB1C	ROTATE LEFT SINGLE LOGICAL (64)	RLLG	EB1C	RSY-a		7-372
EB1D	ROTATE LEFT SINGLE LOGICAL (32)	RLL	EB1D	RSY-a		7-371
EB20	COMPARE LOGICAL CHAR. UNDER MASK (high)	CLMH	EB20	RSY-b	C A B2	7-158
EB21	COMPARE LOGICAL CHAR. UNDER MASK (low)	CLMY	EB21	RSY-b	C A B2	7-158
EB23	COMPARE LOGICAL AND TRAP (32)	CLT	EB23	RSY-b	A Dc B2	7-156
EB24	STORE MULTIPLE (64)	STMG	EB24	RSY-a	A ST B2	7-396
EB25	STORE CONTROL (64)	STCTG	EB25	RSY-a	P A SP ST B2	10-146
EB26	STORE MULTIPLE HIGH (32)	STMH	EB26	RSY-a	A ST B2	7-397
EB2B	COMPARE LOGICAL AND TRAP (64)	CLGT	EB2B	RSY-b	A Dc B2	7-156
EB2C	STORE CHARACTERS UNDER MASK (high)	STCMH	EB2C	RSY-b	9,11 A ST B2	7-390
EB2D	STORE CHARACTERS UNDER MASK (low)	STCMY	EB2D	RSY-b	A ST B2	7-390
EB2F	LOAD CONTROL (64)	LCTLG	EB2F	RSY-a	P A SP B2	10-51
EB30	COMPARE AND SWAP (64)	CSG	EB30	RSY-a	C 9 A SP \$ ST B2	7-145
EB31	COMPARE DOUBLE AND SWAP (32)	CDSY	EB31	RSY-a	C 9 A SP \$ ST B2	7-145
EB3E	COMPARE DOUBLE AND SWAP (64)	CDSG	EB3E	RSY-a	C 9 A SP \$ ST B2	7-145
EB44	BRANCH ON INDEX HIGH (64)	BXHG	EB44	RSY-a	9 B	7-41
EB45	BRANCH ON INDEX LOW OR EQUAL (64)	BXLEG	EB45	RSY-a	9 B	7-41
EB4C	EXTRACT CPU ATTRIBUTE	ECAG	EB4C	RSY-a	9	7-260
EB51	TEST UNDER MASK	TMY	EB51	SIY	C A B1	7-404
EB52	MOVE (immediate)	MVIY	EB52	SIY	A ST B1	7-292
EB54	AND (immediate)	NIY	EB54	SIY	C A £2 ST B1	7-33
EB55	COMPARE LOGICAL (immediate)	CLIY	EB55	SIY	C A B1	7-153
EB56	OR (immediate)	OIY	EB56	SIY	C A ST B1	7-315
EB57	EXCLUSIVE OR (immediate)	XIY	EB57	SIY	C A ST B1	7-258

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
EB6A	ADD IMMEDIATE (32 <- 8)	ASI	EB6A	SIY	C A IF £1 ST B1	7-26
EB6E	ADD LOGICAL WITH SIGNED IMMEDIATE (32 <- 8)	ALSI	EB6E	SIY	C A £1 ST B1	7-31
EB71	LOAD PSW EXTENDED	LPSWEY	EB71	SIY	L P A SP SO ¢ B2	10-57
EB7A	ADD IMMEDIATE (64 <- 8)	AGSI	EB7A	SIY	C A IF £1 ST B1	7-26
EB7E	ADD LOGICAL WITH SIGNED IMMEDIATE (64 <- 8)	ALGSI	EB7E	SIY	C A £1 ST B1	7-31
EB80	INSERT CHARACTERS UNDER MASK (high)	ICMH	EB80	RSY-b	C A B2	7-265
EB81	INSERT CHARACTERS UNDER MASK (low)	ICMY	EB81	RSY-b	C A B2	7-265
EB8E	MOVE LONG UNICODE	MVCLU	EB8E	RSY-a	C ¤9 A SP IC ST R1 R3	7-300
EB8F	COMPARE LOGICAL LONG UNICODE	CLCLU	EB8F	RSY-a	C ¤9 A SP IC R1 R2	7-164
EB90	STORE MULTIPLE (32)	STMY	EB90	RSY-a	A ST B2	7-396
EB96	LOAD MULTIPLE HIGH (32)	LMH	EB96	RSY-a	A B2	7-286
EB98	LOAD MULTIPLE (32)	LMY	EB98	RSY-a	A B2	7-285
EB9A	LOAD ACCESS MULTIPLE	LAMY	EB9A	RSY-a	¤6 A SP UB	7-268
EB9B	STORE ACCESS MULTIPLE	STAMY	EB9B	RSY-a	A SP ST UB	7-389
EBC0	TEST DECIMAL	TP	EBC0	RSL-a	C ¤9 A B1 B2	8-14
EBDC	SHIFT RIGHT SINGLE (32)	SRAK	EBDC	RSY-a	C	7-386
EBDD	SHIFT LEFT SINGLE (32)	SLAK	EBDD	RSY-a	C IF	7-383
EBDE	SHIFT RIGHT SINGLE LOGICAL (32)	SRLK	EBDE	RSY-a		7-387
EBDF	SHIFT LEFT SINGLE LOGICAL (32)	SLLK	EBDF	RSY-a		7-384
EBE0	LOAD HIGH ON CONDITION (32)	LOCFH	EBE0	RSY-b	A B2	7-287
EBE1	STORE HIGH ON CONDITION	STOCFH	EBE1	RSY-b	A ST B2	7-397
EBE2	LOAD ON CONDITION (64)	LOCG	EBE2	RSY-b	A B2	7-287
EBE3	STORE ON CONDITION (64)	STOCG	EBE3	RSY-b	A ST B2	7-397
EBE4	LOAD AND AND (64)	LANG	EBE4	RSY-a	C ¤9 A SP £ ST B2	7-272
EBE6	LOAD AND OR (64)	LAOG	EBE6	RSY-a	C ¤9 A SP £ ST B2	7-273
EBE7	LOAD AND EXCLUSIVE OR (64)	LAXG	EBE7	RSY-a	C ¤9 A SP £ ST B2	7-272
EBE8	LOAD AND ADD (64)	LAAG	EBE8	RSY-a	C ¤9 A SP IF £ ST B2	7-270
EBEA	LOAD AND ADD LOGICAL (64)	LAALG	EBEA	RSY-a	C ¤9 A SP £ ST B2	7-271
EBF2	LOAD ON CONDITION (32)	LOC	EBF2	RSY-b	A B2	7-287
EBF3	STORE ON CONDITION (32)	STOC	EBF3	RSY-b	A ST B2	7-397
EBF4	LOAD AND AND (32)	LAN	EBF4	RSY-a	C ¤9 A SP £ ST B2	7-272
EBF6	LOAD AND OR (32)	LAO	EBF6	RSY-a	C ¤9 A SP £ ST B2	7-273
EBF7	LOAD AND EXCLUSIVE OR (32)	LAX	EBF7	RSY-a	C ¤9 A SP £ ST B2	7-272
EBF8	LOAD AND ADD (32)	LAA	EBF8	RSY-a	C ¤9 A SP IF £ ST B2	7-270
EBFA	LOAD AND ADD LOGICAL (32)	LAAL	EBFA	RSY-a	C ¤9 A SP £ ST B2	7-271
EC42	LOAD HALFWORD IMMEDIATE ON CONDITION (32 <- 16)	LOCHI	EC42	RIE-g		7-280
EC44	BRANCH RELATIVE ON INDEX HIGH (64)	BRXHG	EC44	RIE-e	¤9 B	7-48
EC45	BRANCH RELATIVE ON INDEX LOW OR EQ. (64)	BRXLG	EC45	RIE-e	¤9 B	7-48
EC46	LOAD HALFWORD IMMEDIATE ON CONDITION (64 <- 16)	LOCGHI	EC46	RIE-g		7-280
EC4E	LOAD HALFWORD HIGH IMMEDIATE ON CONDITION (32 <- 16)	LOCHHI	EC4E	RIE-g		7-280
EC51	ROTATE THEN INSERT SELECTED BITS LOW (64)	RISBLG	EC51	RIE-f		7-375
EC54	ROTATE THEN AND SELECTED BITS (64)	RNSBG	EC54	RIE-f	C	7-372
EC55	ROTATE THEN INSERT SELECTED BITS (64)	RISBG	EC55	RIE-f	C	7-374
EC56	ROTATE THEN OR SELECTED BITS (64)	ROSBG	EC56	RIE-f	C	7-372
EC57	ROTATE THEN EXCLUSIVE OR SELECT. BITS (64)	RXSBG	EC57	RIE-f	C	7-372
EC59	ROTATE THEN INSERT SELECTED BITS (64)	RISBGN	EC59	RIE-f		7-374
EC5D	ROTATE THEN INSERT SELECTED BITS HIGH (64)	RISBHG	EC5D	RIE-f		7-375
EC64	COMPARE AND BRANCH RELATIVE (64)	CGRJ	EC64	RIE-b	¤10 B	7-137
EC65	COMPARE LOGICAL AND BRANCH RELATIVE (64)	CLGRJ	EC65	RIE-b	¤10 B	7-155
EC70	COMPARE IMMEDIATE AND TRAP (64 <- 16)	CGIT	EC70	RIE-a	Dc	7-150
EC71	COMPARE LOGICAL IMMEDIATE AND TRAP (64 <- 16)	CLGIT	EC71	RIE-a	Dc	7-157
EC72	COMPARE IMMEDIATE AND TRAP (32 <- 16)	CIT	EC72	RIE-a	Dc	7-150
EC73	COMPARE LOGICAL IMMEDIATE AND TRAP (32 <- 16)	CLFIT	EC73	RIE-a	Dc	7-157
EC76	COMPARE AND BRANCH RELATIVE (32)	CRJ	EC76	RIE-b	¤10 B	7-137

Opcod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
EC77	COMPARE LOGICAL AND BRANCH RELATIVE (32)	CLRJ	EC77	RIE-b	⌘10 B	7-155
EC7C	COMPARE IMMEDIATE AND BRANCH RELATIVE (64 <- 8)	CGIJ	EC7C	RIE-c	⌘10 B	7-137
EC7D	COMPARE LOGICAL IMMEDIATE AND BRANCH RELATIVE (64 <- 8)	CLGIJ	EC7D	RIE-c	⌘10 B	7-155
EC7E	COMPARE IMMEDIATE AND BRANCH RELATIVE (32 <- 8)	CIJ	EC7E	RIE-c	⌘10 B	7-137
EC7F	COMPARE LOGICAL IMMEDIATE AND BRANCH RELATIVE (32 <- 8)	CLIJ	EC7F	RIE-c	⌘10 B	7-155
ECD8	ADD IMMEDIATE (32 <- 16)	AHIK	ECD8	RIE-d	C IF	7-26
ECD9	ADD IMMEDIATE (64 <- 16)	AGHIK	ECD9	RIE-d	C IF	7-26
ECDA	ADD LOGICAL WITH SIGNED IMMEDIATE (32 <- 16)	ALHSIK	ECDA	RIE-d	C	7-31
ECDB	ADD LOGICAL WITH SIGNED IMMEDIATE (64 <- 16)	ALGHSIK	ECDB	RIE-d	C	7-31
ECE4	COMPARE AND BRANCH (64)	CGRB	ECE4	RRS	⌘9 B	7-137
ECE5	COMPARE LOGICAL AND BRANCH (64)	CLGRB	ECE5	RRS	⌘9 B	7-155
ECF6	COMPARE AND BRANCH (32)	CRB	ECF6	RRS	⌘9 B	7-137
ECF7	COMPARE LOGICAL AND BRANCH (32)	CLRB	ECF7	RRS	⌘9 B	7-155
ECFC	COMPARE IMMEDIATE AND BRANCH (64 <- 8)	CGIB	ECFC	RIS	⌘9 B	7-137
ECFD	COMPARE LOGICAL IMMEDIATE AND BRANCH (64 <- 8)	CLGIB	ECFD	RIS	⌘9 B	7-155
ECFE	COMPARE IMMEDIATE AND BRANCH (32 <- 8)	CIB	ECFE	RIS	⌘9 B	7-137
ECFF	COMPARE LOGICAL IMMEDIATE AND BRANCH (32 <- 8)	CLIB	ECFF	RIS	⌘9 B	7-155
ED04	LOAD LENGTHENED (short to long BFP)	LDEB	ED04	RXE	⌘7,9 A Db Xi B2	19-34
ED05	LOAD LENGTHENED (long to extended BFP)	LXDB	ED05	RXE	⌘7,9 A SP Db Xi B2	19-34
ED06	LOAD LENGTHENED (short to extended BFP)	LXEB	ED06	RXE	⌘7,9 A SP Db Xi B2	19-34
ED07	MULTIPLY (long to extended BFP)	MXDB	ED07	RXE	⌘7,9 A SP Db Xi B2	19-37
ED08	COMPARE AND SIGNAL (short BFP)	KEB	ED08	RXE	C ⌘7,9 A Db Xi B2	19-18
ED09	COMPARE (short BFP)	CEB	ED09	RXE	C ⌘7,9 A Db Xi B2	19-17
ED0A	ADD (short BFP)	AEB	ED0A	RXE	C ⌘7,9 A Db Xi Xo Xu Xx B2	19-15
ED0B	SUBTRACT (short BFP)	SEB	ED0B	RXE	C ⌘7,9 A Db Xi Xo Xu Xx B2	19-40
ED0C	MULTIPLY (short to long BFP)	MDEB	ED0C	RXE	⌘7,9 A Db Xi B2	19-37
ED0D	DIVIDE (short BFP)	DEB	ED0D	RXE	⌘7,9 A Db Xi Xz Xo Xu Xx B2	19-27
ED0E	MULTIPLY AND ADD (short BFP)	MAEB	ED0E	RXF	⌘7,9 A Db Xi Xo Xu Xx B2	19-38
ED0F	MULTIPLY AND SUBTRACT (short BFP)	MSEB	ED0F	RXF	⌘7,9 A Db Xi Xo Xu Xx B2	19-38
ED10	TEST DATA CLASS (short BFP)	TCEB	ED10	RXE	C ⌘7,9 Db	19-41
ED11	TEST DATA CLASS (long BFP)	TCDB	ED11	RXE	C ⌘7,9 Db	19-41
ED12	TEST DATA CLASS (extended BFP)	TCXB	ED12	RXE	C ⌘7,9 SP Db	19-41
ED14	SQUARE ROOT (short BFP)	SQEB	ED14	RXE	⌘7,9 A Db Xi Xx B2	19-40
ED15	SQUARE ROOT (long BFP)	SQDB	ED15	RXE	⌘7,9 A Db Xi Xx B2	19-40
ED17	MULTIPLY (short BFP)	MEEB	ED17	RXE	⌘7,9 A Db Xi Xo Xu Xx B2	19-37
ED18	COMPARE AND SIGNAL (long BFP)	KDB	ED18	RXE	C ⌘7,9 A Db Xi B2	19-18
ED19	COMPARE (long BFP)	CDB	ED19	RXE	C ⌘7,9 A Db Xi B2	19-17
ED1A	ADD (long BFP)	ADB	ED1A	RXE	C ⌘7,9 A Db Xi Xo Xu Xx B2	19-15
ED1B	SUBTRACT (long BFP)	SDB	ED1B	RXE	C ⌘7,9 A Db Xi Xo Xu Xx B2	19-40
ED1C	MULTIPLY (long BFP)	MDB	ED1C	RXE	⌘7,9 A Db Xi Xo Xu Xx B2	19-37
ED1D	DIVIDE (long BFP)	DDB	ED1D	RXE	⌘7,9 A Db Xi Xz Xo Xu Xx B2	19-27
ED1E	MULTIPLY AND ADD (long BFP)	MADB	ED1E	RXF	⌘7,9 A Db Xi Xo Xu Xx B2	19-38
ED1F	MULTIPLY AND SUBTRACT (long BFP)	MSDB	ED1F	RXF	⌘7,9 A Db Xi Xo Xu Xx B2	19-38
ED24	LOAD LENGTHENED (short to long HFP)	LDE	ED24	RXE	⌘7,9 A Da B2	18-15
ED25	LOAD LENGTHENED (long to extended HFP)	LXD	ED25	RXE	⌘7,9 A SP Da B2	18-15
ED26	LOAD LENGTHENED (short to extended HFP)	LXE	ED26	RXE	⌘7,9 A SP Da B2	18-15
ED2E	MULTIPLY AND ADD (short HFP)	MAE	ED2E	RXF	⌘7,9 A Da EU E0 B2	18-19
ED2F	MULTIPLY AND SUBTRACT (short HFP)	MSE	ED2F	RXF	⌘7,9 A Da EU E0 B2	18-19
ED34	SQUARE ROOT (short HFP)	SQE	ED34	RXE	⌘7,9 A Da SQ B2	18-23
ED35	SQUARE ROOT (long HFP)	SQD	ED35	RXE	⌘7,9 A Da SQ B2	18-23
ED37	MULTIPLY (short HFP)	MEE	ED37	RXE	⌘7,9 A Da EU E0 B2	18-18
ED38	MULTIPLY AND ADD UNNRM. (long to ext. low HFP)	MAYL	ED38	RXF	⌘7,9 A Da B2	18-20
ED39	MULTIPLY UNNORM. (long to ext. low HFP)	MYL	ED39	RXF	⌘7,9 A Da B2	18-22
ED3A	MULTIPLY & ADD UNNORMALIZED (long to ext. HFP)	MAY	ED3A	RXF	⌘7,9 A Da B2	18-20

OpCod	Name...	Assm	OpCd	IFmt	Attributes...	Ref
ED3B	MULTIPLY UNNORMALIZED (long to ext. HFP)	MY	ED3B	RXF	⌘7,9 A SP Da B2	18-22
ED3C	MULTIPLY AND ADD UNNRM. (long to ext. high HFP)	MAYH	ED3C	RXF	⌘7,9 A Da B2	18-20
ED3D	MULTIPLY UNNORM. (long to ext. high HFP)	MYH	ED3D	RXF	⌘7,9 A Da B2	18-22
ED3E	MULTIPLY AND ADD (long HFP)	MAD	ED3E	RXF	⌘7,9 A Da EU E0 B2	18-19
ED3F	MULTIPLY AND SUBTRACT (long HFP)	MSD	ED3F	RXF	⌘7,9 A Da EU E0 B2	18-19
ED40	SHIFT SIGNIFICAND LEFT (long DFP)	SLDT	ED40	RXF	⌘7,9 Dt	20-54
ED41	SHIFT SIGNIFICAND RIGHT (long DFP)	SRDT	ED41	RXF	⌘7,9 Dt	20-54
ED48	SHIFT SIGNIFICAND LEFT (extended DFP)	SLXT	ED48	RXF	⌘7,9 SP Dt	20-54
ED49	SHIFT SIGNIFICAND RIGHT (extended DFP)	SRXT	ED49	RXF	⌘7,9 SP Dt	20-54
ED50	TEST DATA CLASS (short DFP)	TDCET	ED50	RXE	C ⌘7,9 Dt	20-56
ED51	TEST DATA GROUP (short DFP)	TDGET	ED51	RXE	C ⌘7,9 Dt	20-57
ED54	TEST DATA CLASS (long DFP)	TDCDT	ED54	RXE	C ⌘7,9 Dt	20-56
ED55	TEST DATA GROUP (long DFP)	TDGDT	ED55	RXE	C ⌘7,9 Dt	20-57
ED58	TEST DATA CLASS (extended DFP)	TDCXT	ED58	RXE	C ⌘7,9 SP Dt	20-56
ED59	TEST DATA GROUP (extended DFP)	TDGXT	ED59	RXE	C ⌘7,9 SP Dt	20-57
ED64	LOAD (short)	LEY	ED64	RXY-a	⌘7,9 A Da B2	9-31
ED65	LOAD (long)	LDY	ED65	RXY-a	⌘7,9 A Da B2	9-31
ED66	STORE (short)	STEY	ED66	RXY-a	⌘7,9 A Da ST B2	9-49
ED67	STORE (long)	STDY	ED67	RXY-a	⌘7,9 A Da ST B2	9-49
EDA8	CONVERT TO ZONED (from long DFP)	CZDT	EDA8	RSL-b	C ⌘7,9 A SP ST B2	20-36
EDA9	CONVERT TO ZONED (from extended DFP)	CZXT	EDA9	RSL-b	C ⌘7,9 A SP ST B2	20-36
EDAA	CONVERT FROM ZONED (to long DFP)	CDZT	EDAA	RSL-b	⌘7,9 A SP Dt Dg B2	20-29
EDAB	CONVERT FROM ZONED (to extended DFP)	CXZT	EDAB	RSL-b	⌘7,9 A SP Dt Dg B2	20-29
EDAC	CONVERT TO PACKED (from long DFP)	CPDT	EDAC	RSL-b	C ⌘7,9 A SP Dt DF ST B2	20-33
EDAD	CONVERT TO PACKED (from extended DFP)	CPXT	EDAD	RSL-b	C ⌘7,9 A SP Dt DF ST B2	20-33
EDAE	CONVERT FROM PACKED (to long DFP)	CDPT	EDAE	RSL-b	⌘7,9 A SP Dt Dg B2	20-26
EDAF	CONVERT FROM PACKED (to extended DFP)	CXPT	EDAF	RSL-b	⌘7,9 A SP Dt Dg B2	20-26