

Disney World: The ARM Cluster

Andrew Hoover Christine Sorensen

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Client

- Dr. Christer Karlsson
- Client Needs
 - Cluster of 6 – 12 single-board ARM computers
 - Benchmarked
 - Fastest and most efficient design

Project Overview

- Project
- Establish proof of concept
- Gather more LINPACK data
- Test new communication
 - GPIO
 - USB
- Test different topologies
 - Ring
 - Hypercube

Tools

- Linux
- Bash
- GitHub
- WiringPi
- C
- LINPACK
- OpenMPI, MPICH
- Python
- Pyplot
- Routing Tables
- USB to Ethernet

Deliverables

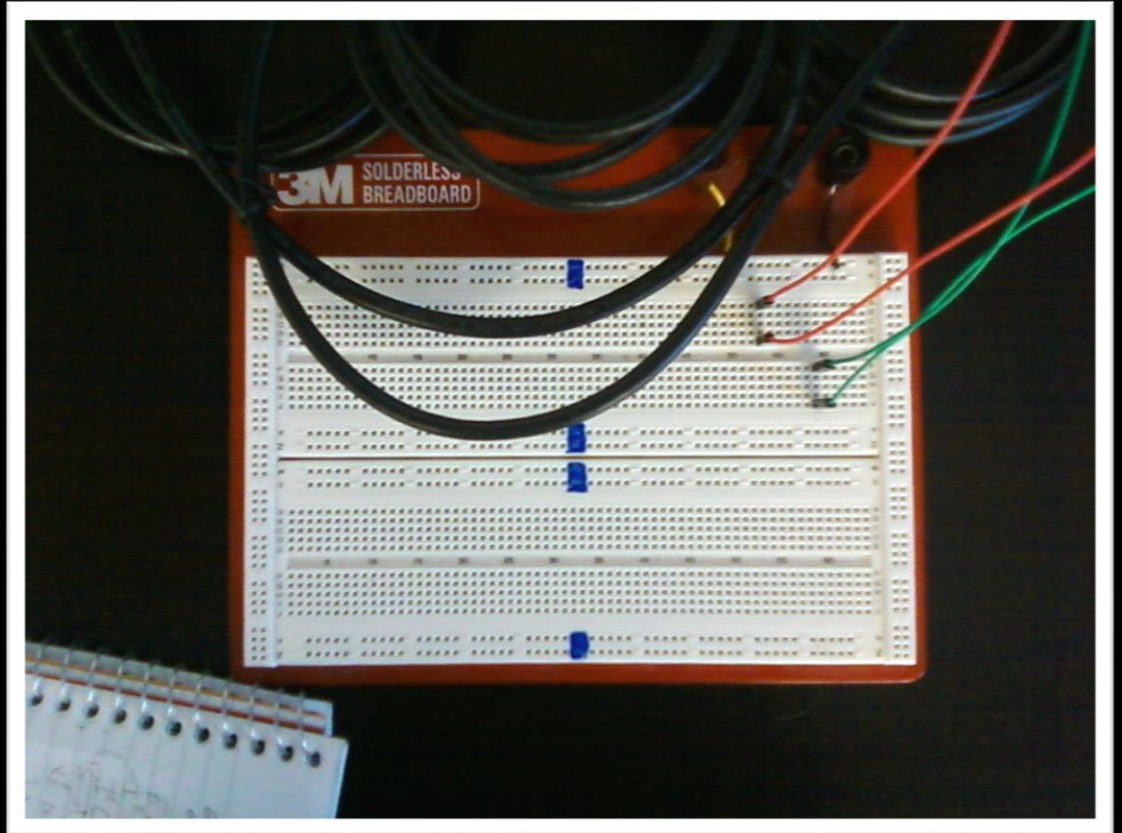
- Benchmark LINPACK on multiple number of devices
- Compared cluster to i7
- LINPACK Debian package
- USB and GPIO
 - Research on USB
 - Tests with GPIO
- Topology Design
 - Ring and hypercube
- Routing tables
- MICS Conference
 - Paper and abstract
- SDSMT Research Symposium
 - Abstract

ARM LINPACK

- Created Debian package of HPL
- Tuned to work on ARM
- Successfully installs on dwarfs with dpkg install command

GPIO

- WiringPi
- Pins connected
- Results



GPIO

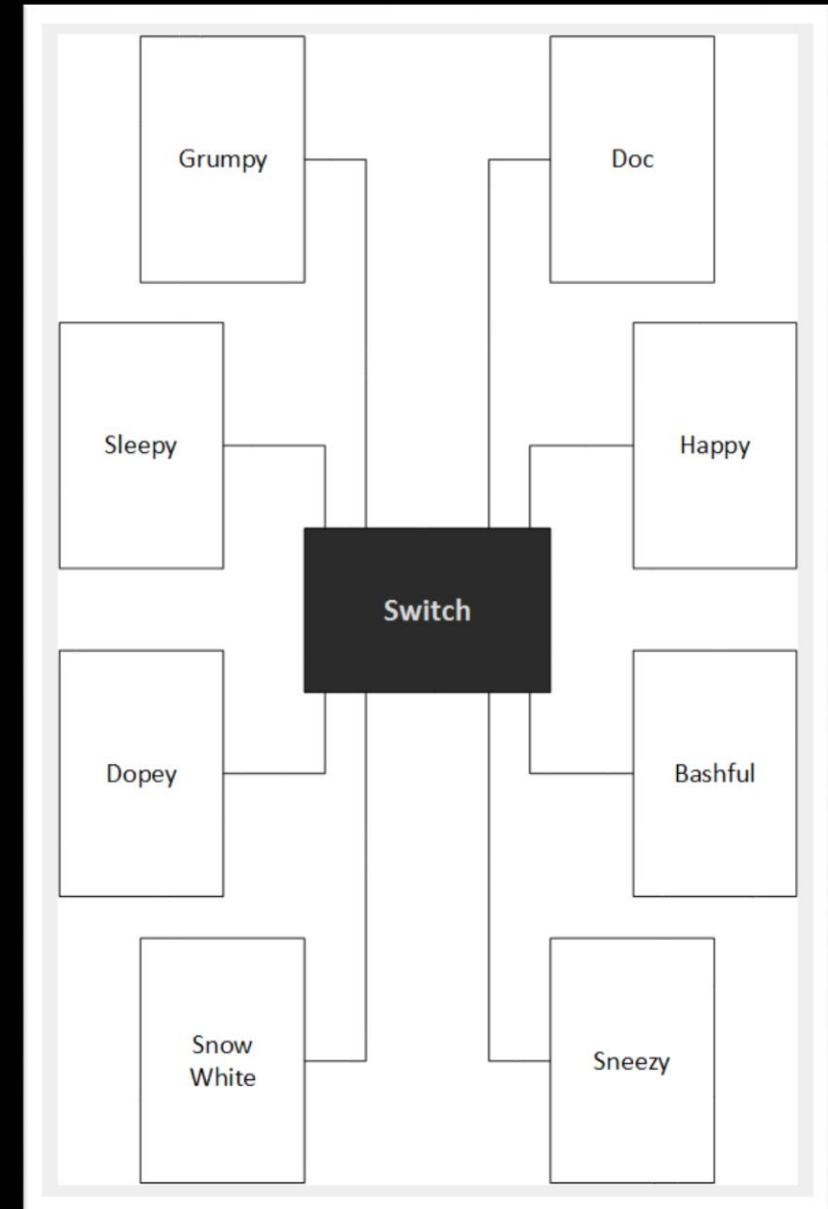
- WiringPi
- Pins connected
- Results

Pin #	GPIO Export #	Name
1	173	Power 5V
2	174	ADC AIN
3	171	UART RTSN Request to Send
4	172	UART RXD Receive
5	173	UART TXD Transmit
6	189	SPI CLK Clock
7	190	POWER ON
8	210	I ² C SCL Clock Line, Sync Data
9	209	I ² C SDA Data Line
10	191	SPI CSN
11	213	18 19 4
12	1815	22 17 21
13	2217	20 28 23
14	3019	22 31 23
15	2921	24 25 11
16	2325	26 24 6
17	3327	28 =
18	3327	30 =
19	3327	30 =
20	3327	30 =
21	3327	30 =
22	3327	30 =
23	3327	30 =
24	3327	30 =
25	3327	30 =
26	3327	30 =
27	3327	30 =
28	3327	30 =
29	3327	30 =
30	3327	30 =

Pin #
GPIO Export #
Name
TL;DR
WiringPi

Star Topology

- Initial setup
- Each dwarf connected to an unmanaged switch



Routing Table

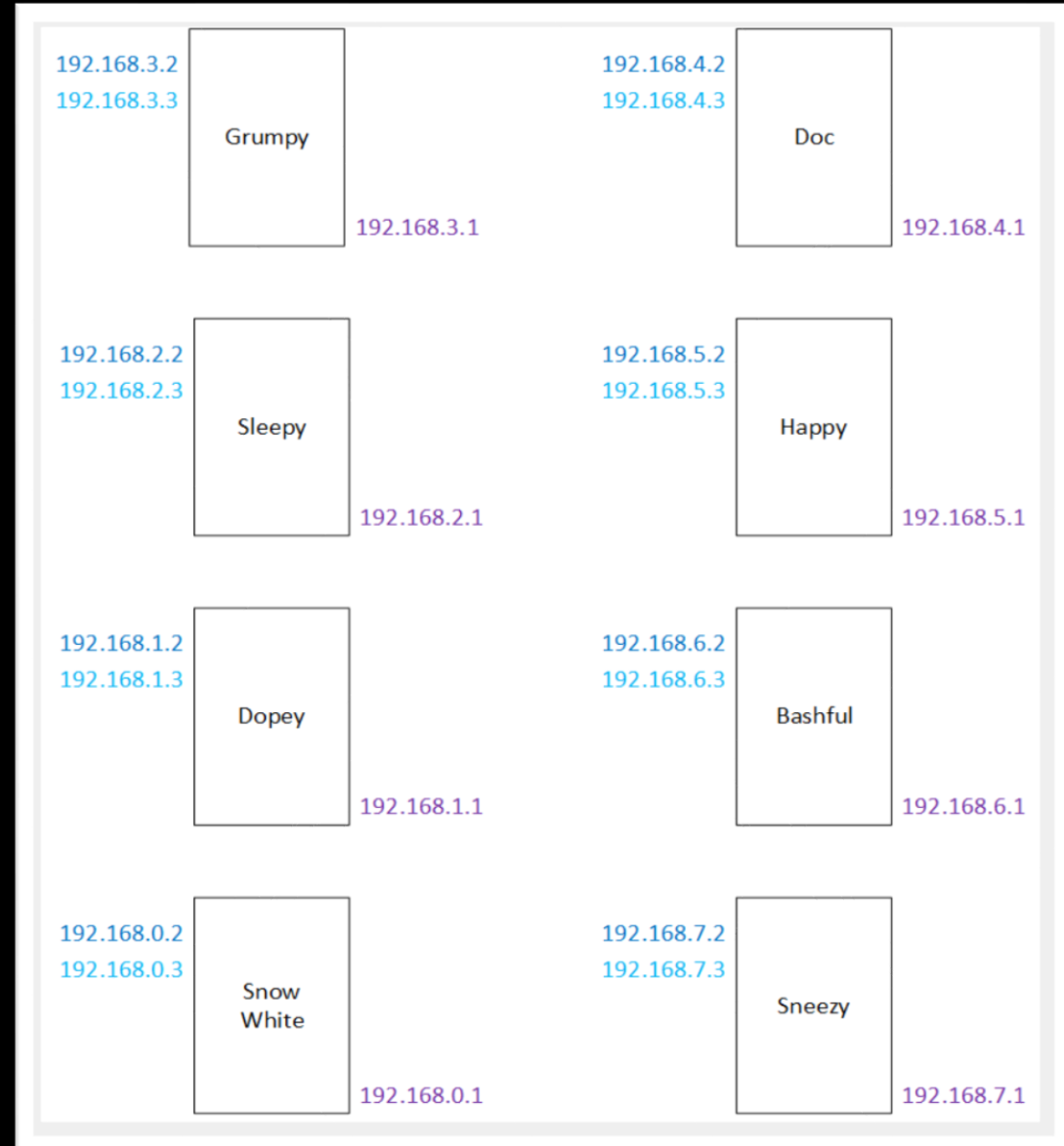
```
# interfaces(5) file used by ifup(8) and ifdown(8)
# Include files from /etc/network/interfaces.d:
source-directory /etc/network/interfaces.d

auto eth0
iface eth0 inet static
    address 192.168.0.11
    netmask 255.255.255.0
    up route add -net 192.168.0.10 netmask 255.255.255.255 gw 192.168.0.11
    up route add -net 192.168.0.9 netmask 255.255.255.255 gw 192.168.0.11
    up route add -net 192.168.0.8 netmask 255.255.255.255 gw 192.168.0.10
    up route add -net 192.168.0.7 netmask 255.255.255.255 gw 192.168.0.10

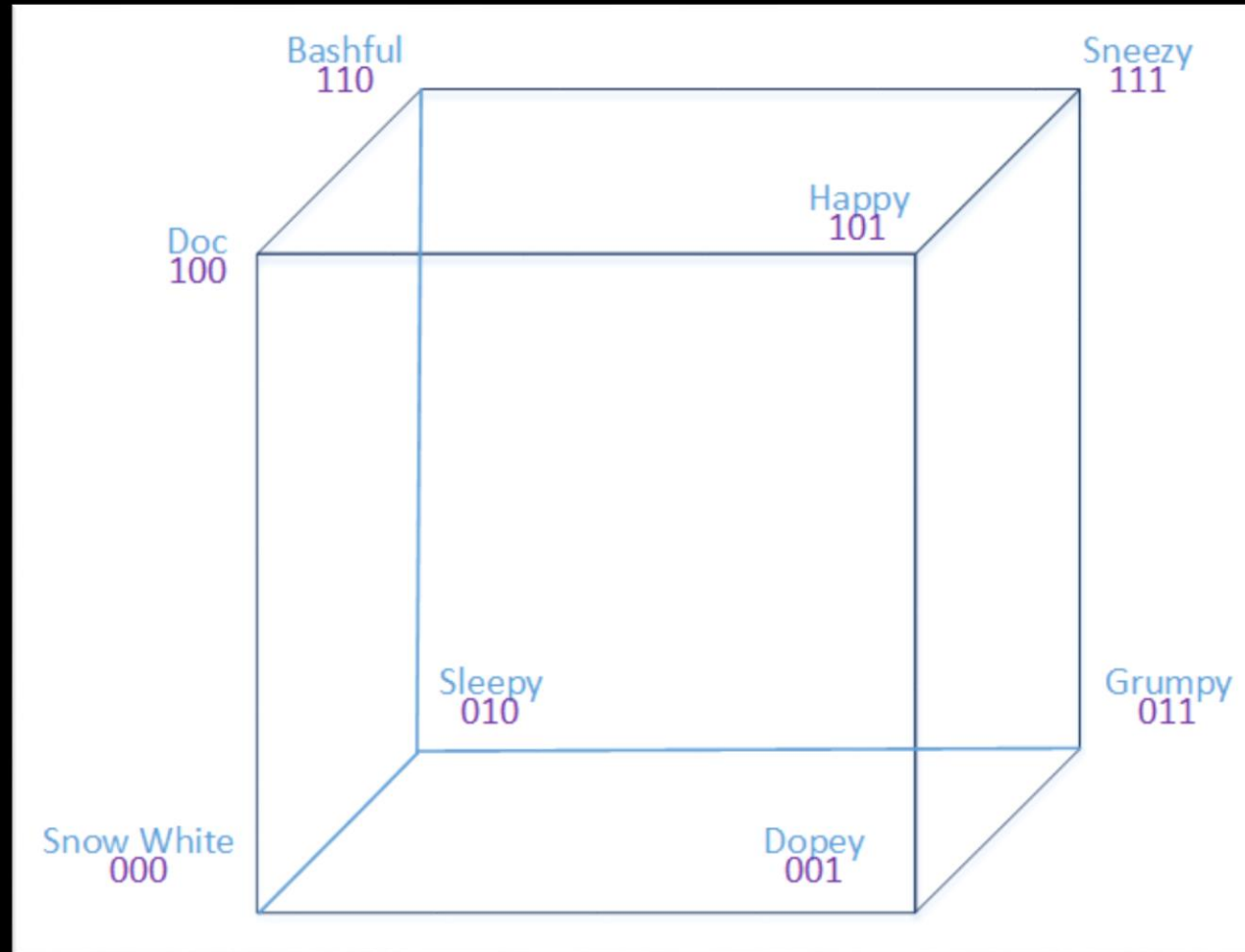
auto eth2
iface eth2 inet static
    address 192.168.0.12
    netmask 255.255.255.0
    up route add -net 192.168.0.13 netmask 255.255.255.255 gw 192.168.0.12
```

IP Addresses

192.168.Dwarf.Port



Hypercube

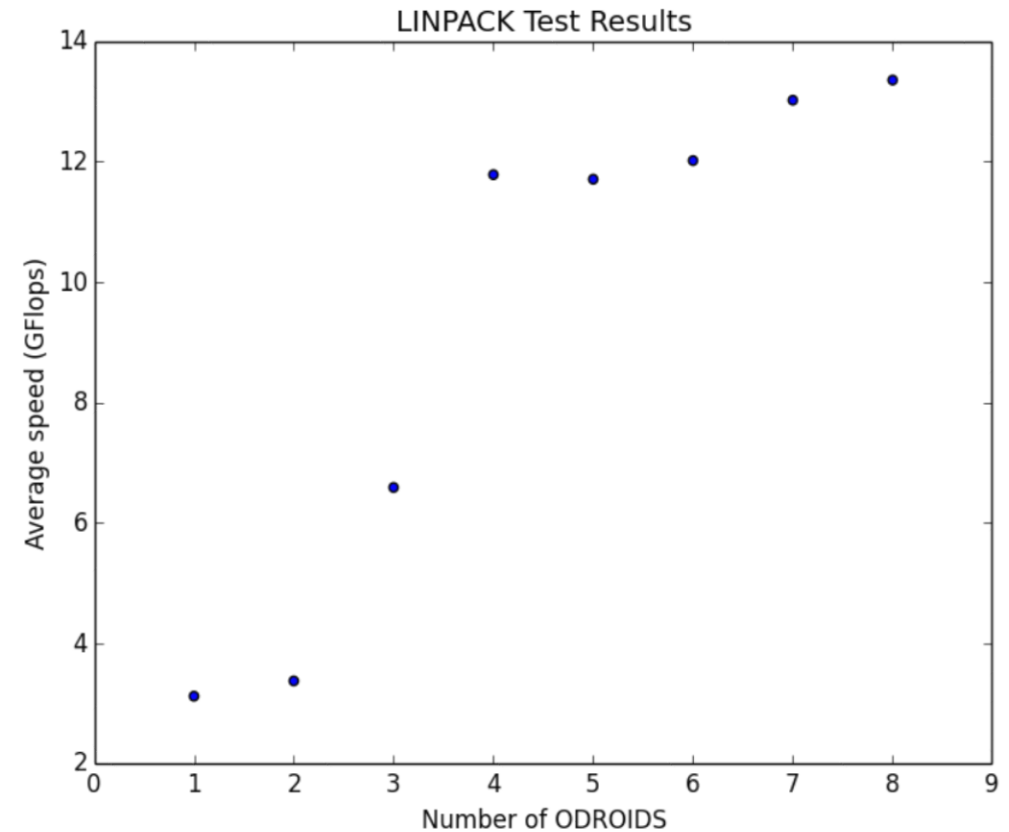


Results

- Gigaflops per number of devices
- USB not practical
- GPIO with WiringPi slow
- Routing tables, ring topology functional

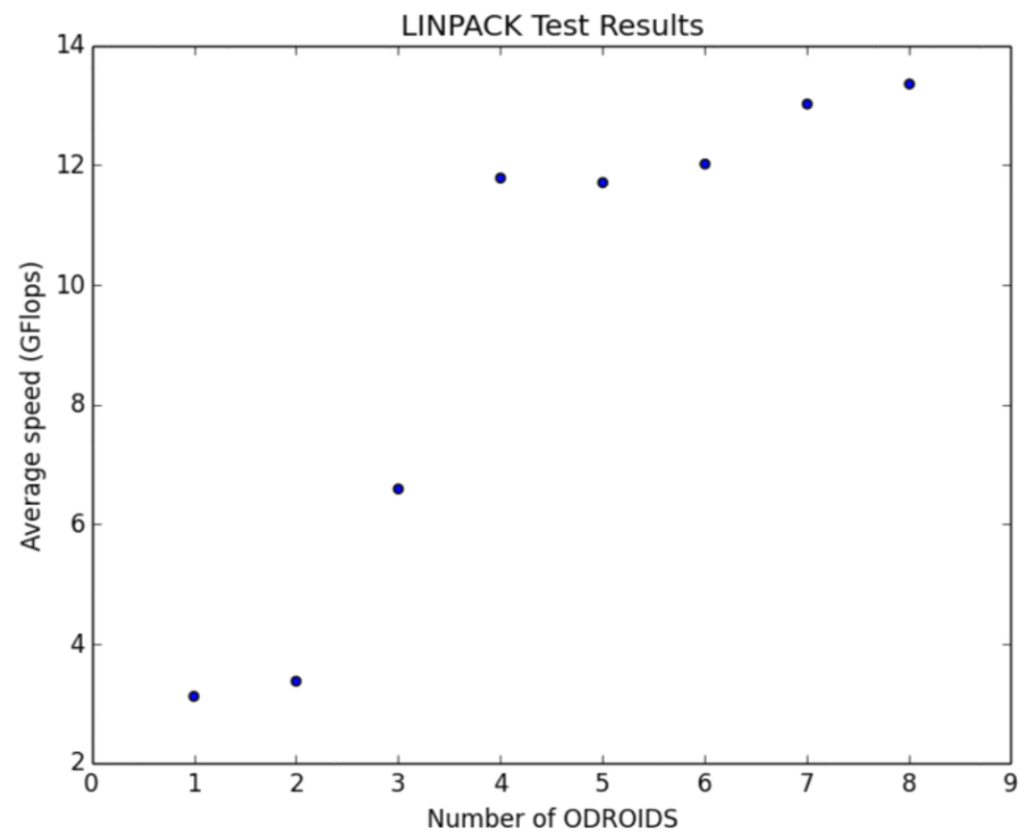
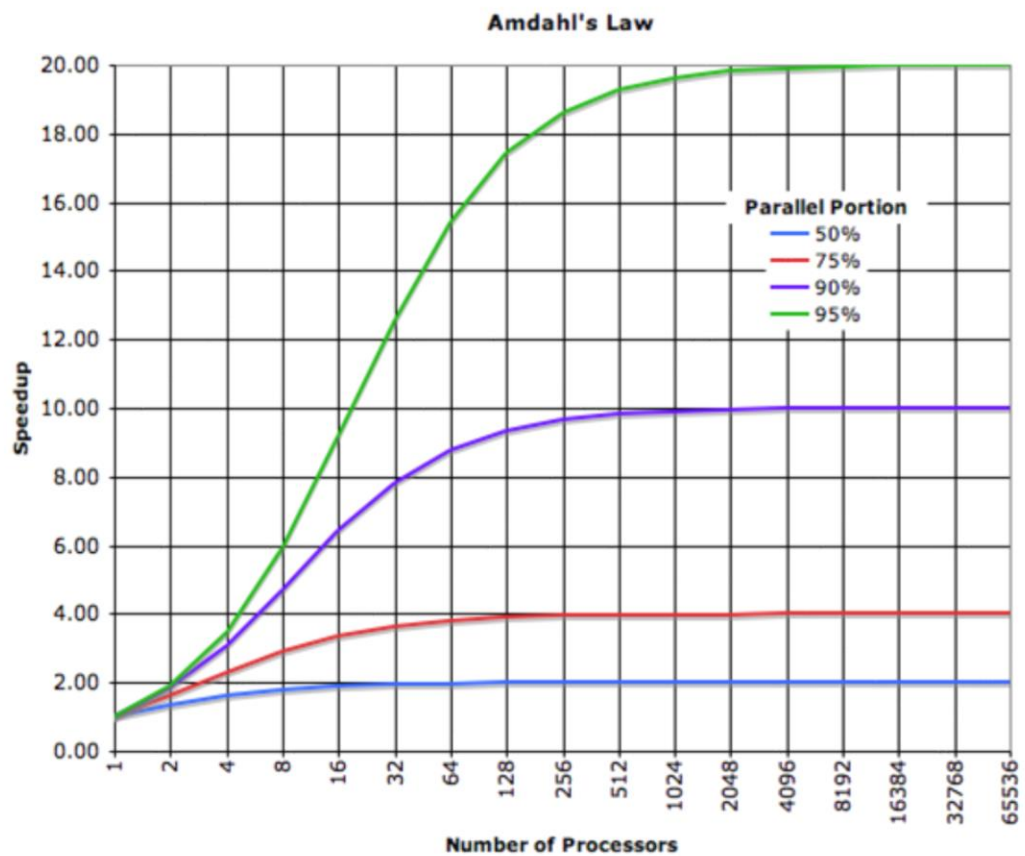
Speedup

- $1 \rightarrow 2$ devices = 1 speedup
- $2 \rightarrow 3 = 4$
- $3 \rightarrow 4 = 5$
 - Max speedup
- $4 \rightarrow 5 = 1$
- $5 \rightarrow 6 = -1$
- $6 \rightarrow 7 = 2$
- $7 \rightarrow 8 = 1$



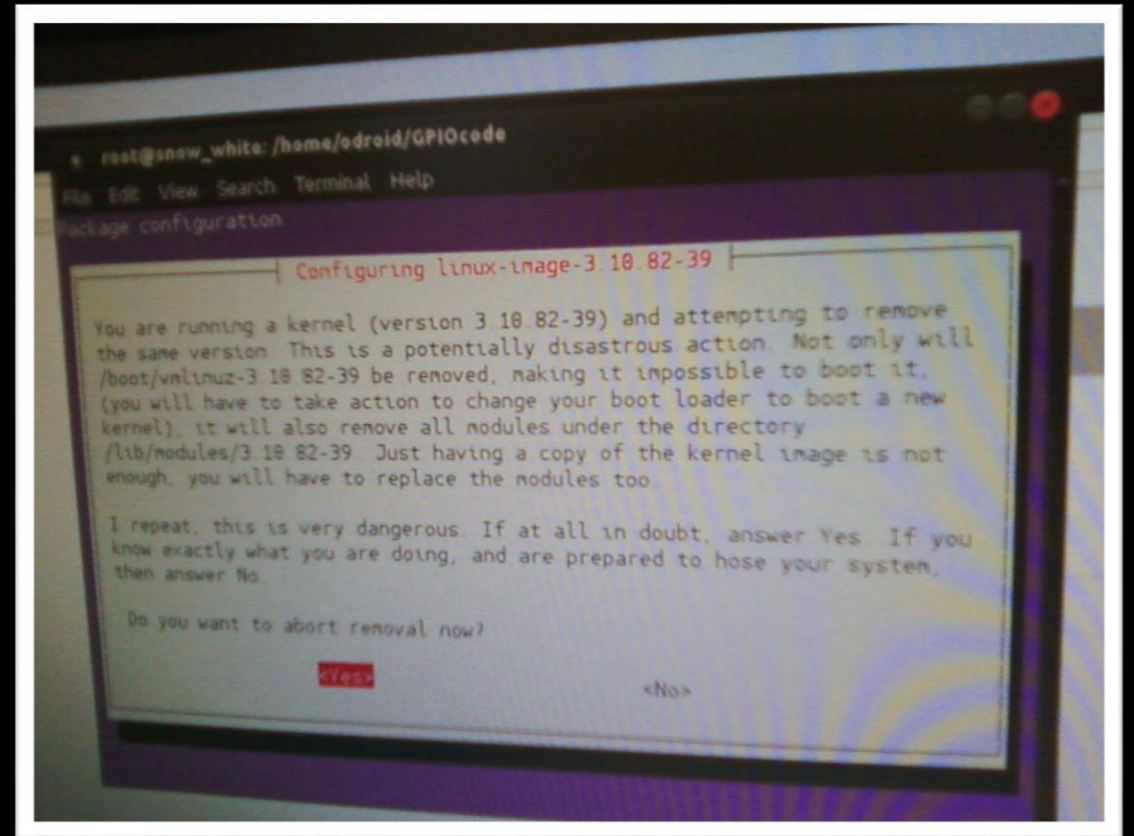
Amdahl's Law

- $speedup \leq \frac{1}{S + \frac{(1-S)}{N}}$
 - S = portion serial
 - N = number of cores
- Unsure of portion sizes
- If all parallel
 - $speedup \leq N$
- All serial
 - $speedup \leq 1$
- Increasing the number of cores
 - $\lim_{N \rightarrow \infty} = \frac{1}{S}$
 - Speedup will not achieve more than $\frac{1}{S}$



Issues

- WiringPi
 - Updating the kernel
 - GPIO pin numbers to physical numbers
- Topologies
 - Create appropriate network
 - Access devices through others
- LINPACK
 - Testing on ring topology



Education Tool

- Answering questions:
 - How computer work?
 - How do we build and setup networks?
 - How do we communicate between computers?
 - How do we benchmark it?

Budget

- MICS Registration
 - \$35.00
- Miscellaneous
 - \$25.00
- Acrylic board
 - \$25.00
- Replacement ORDROID-XU4
 - 2 at \$76.00 each
- ODROID-XU4
 - 8 at \$76.00 each
- Memory
 - 8 at \$38.00 each
- Power Unit
 - \$49.99
- Ethernet cables
 - 8 at \$2.99 each
- Switch
 - \$39.99

Remaining Backlog

- LINPACK over ring and hypercube
- SDSMT Symposium
- MICS Symposium
- Design Fair
- Design Documentation

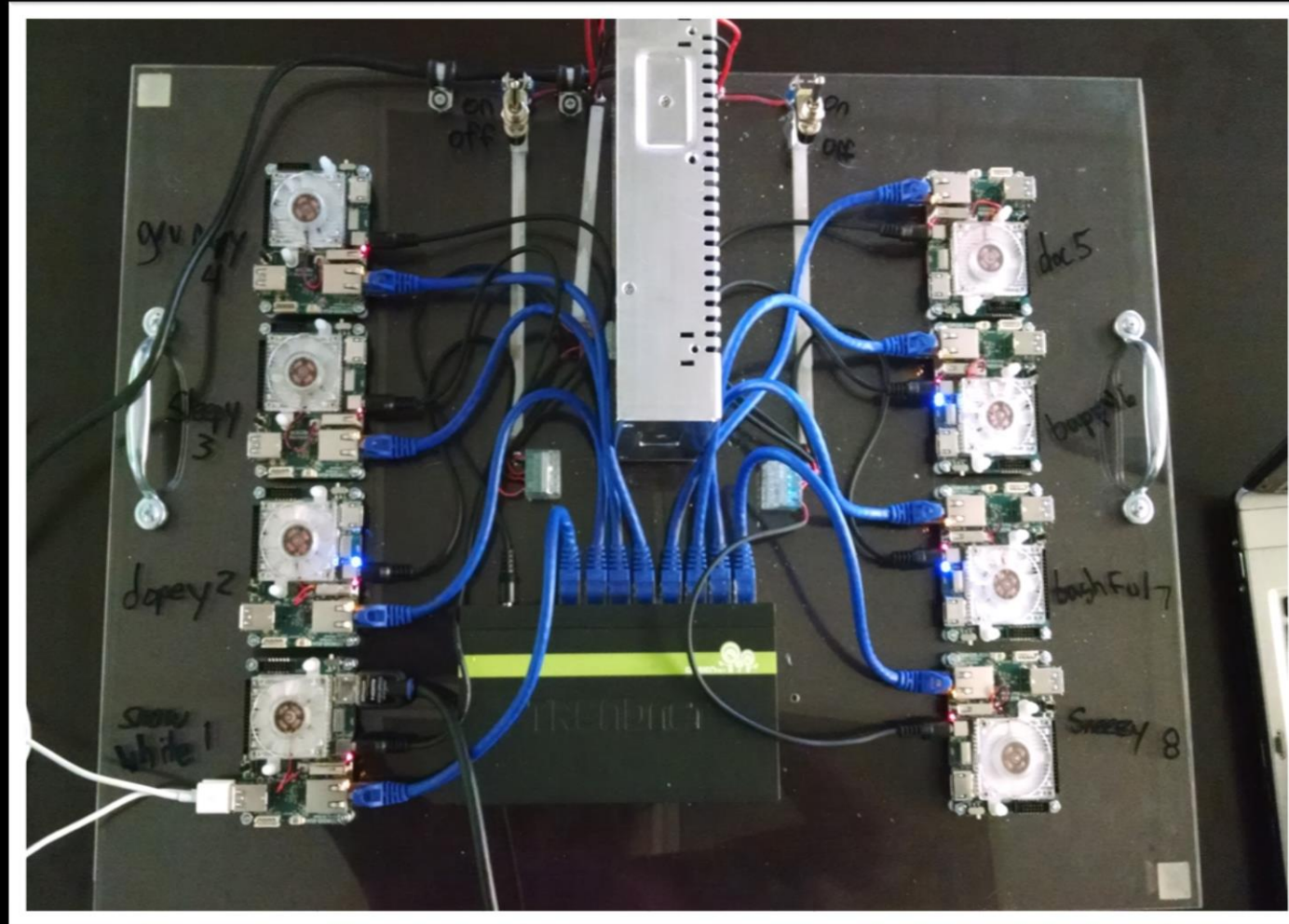
Revised Goals

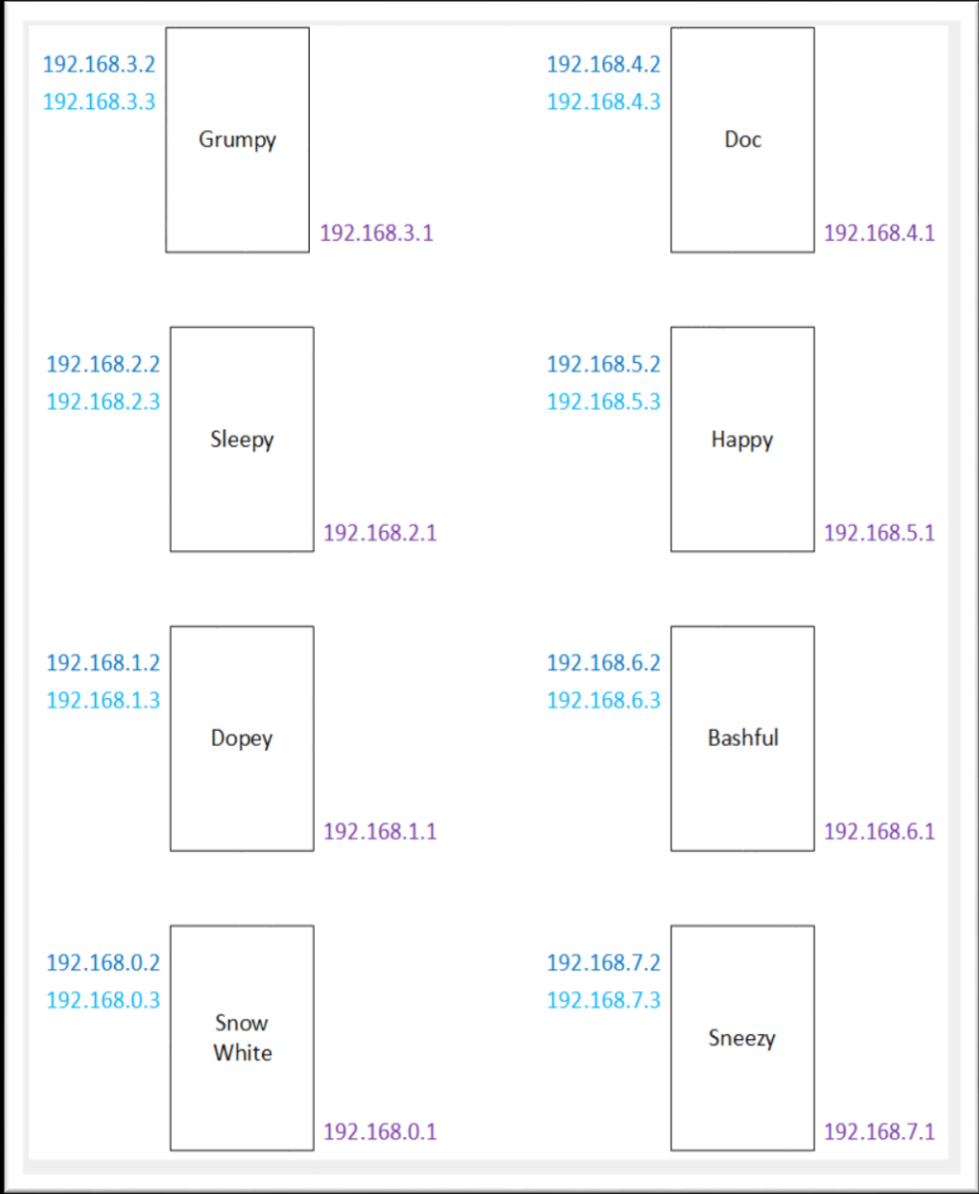
- Debug LINPACK
- SDSMT Symposium

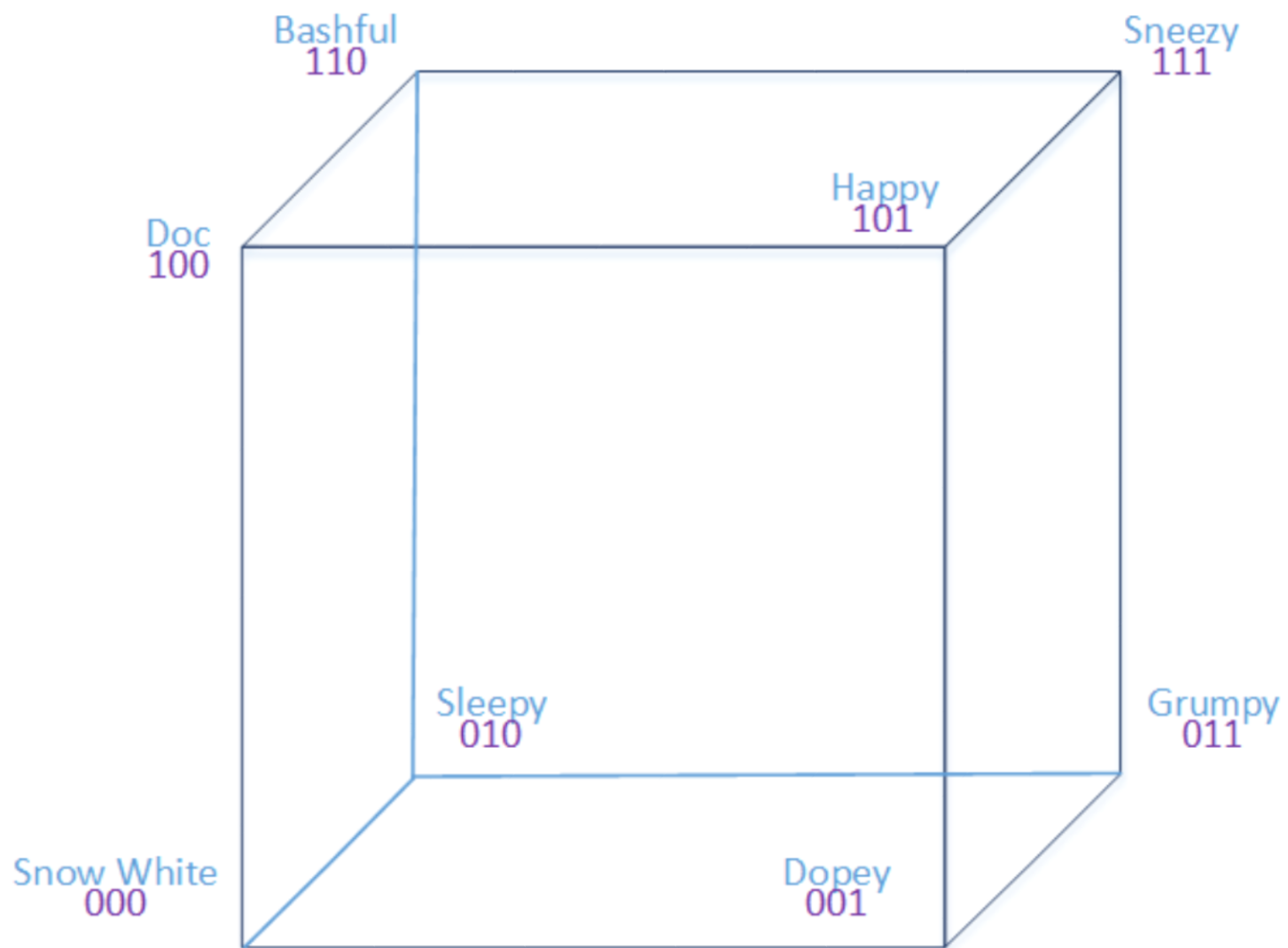
Schedule

- Sprint 6: 3/21/16
 - Documentation
 - Presentation preparations
- SDSMT Research Symposium: 4/7/16
- Design Fair: 4/19/16
- MICS: 4/23/16

Prototype







Questions?