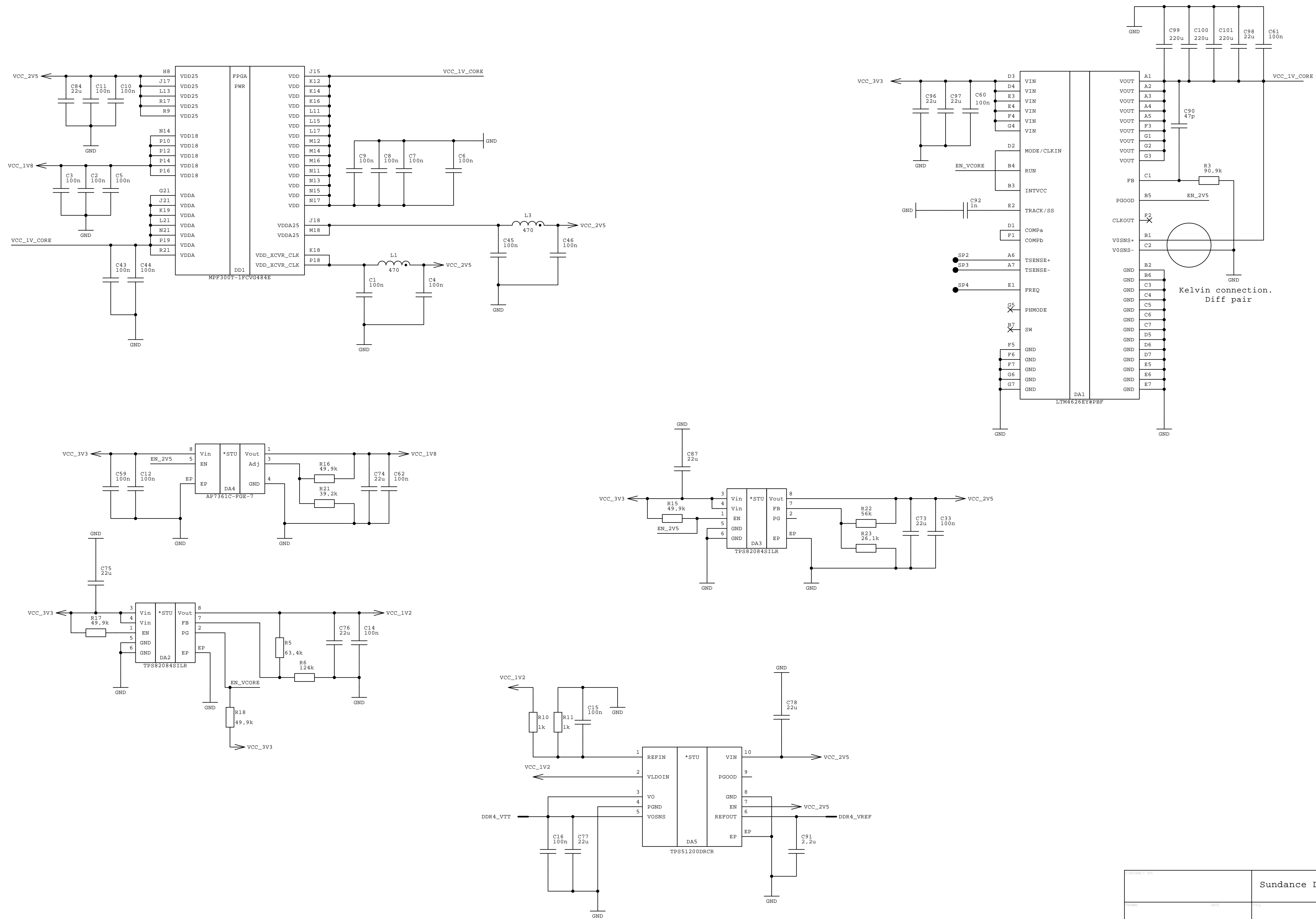


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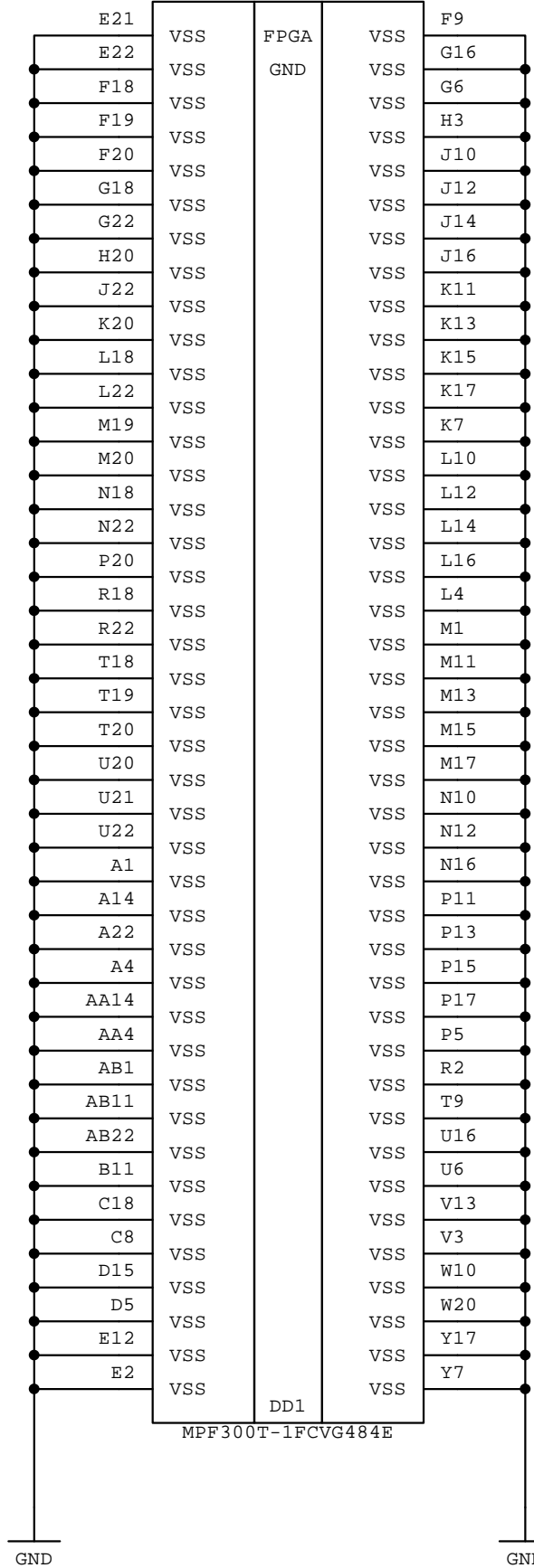
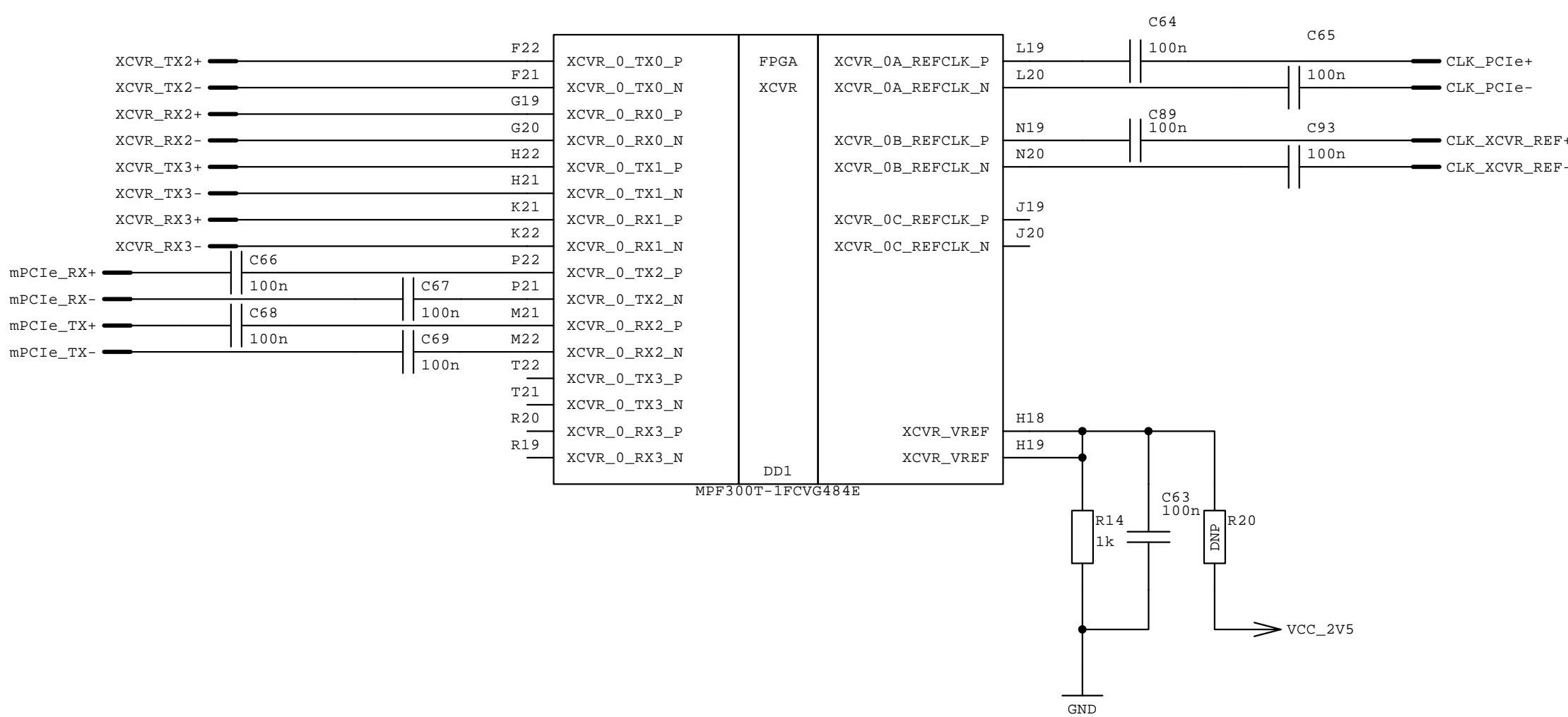
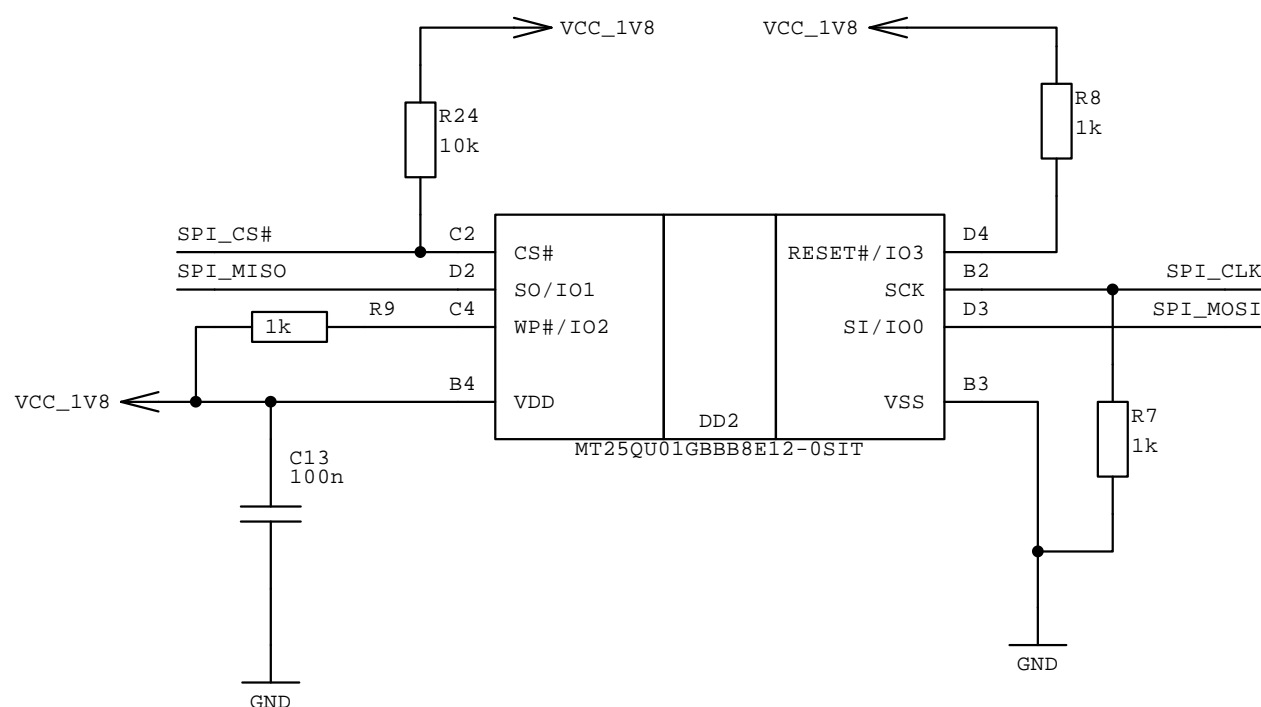
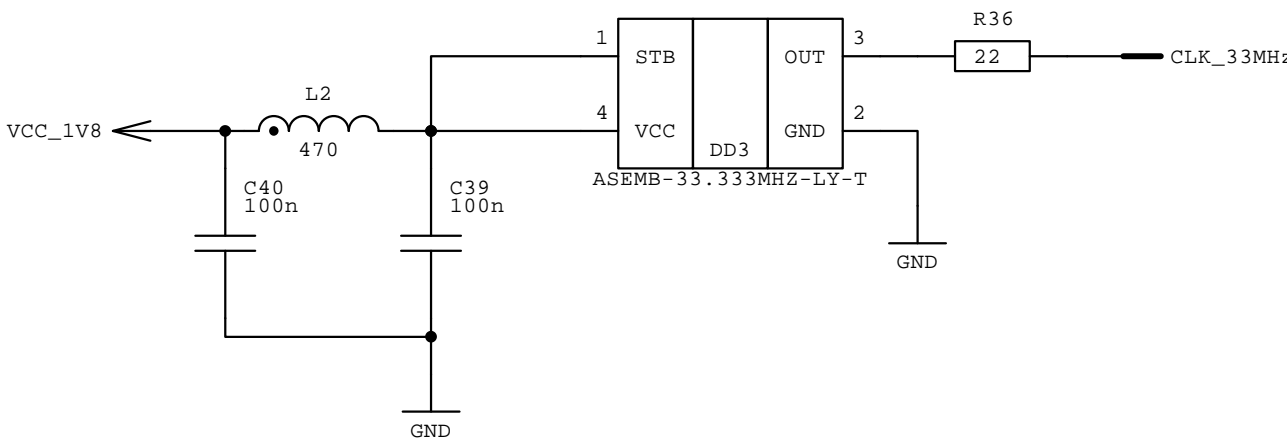
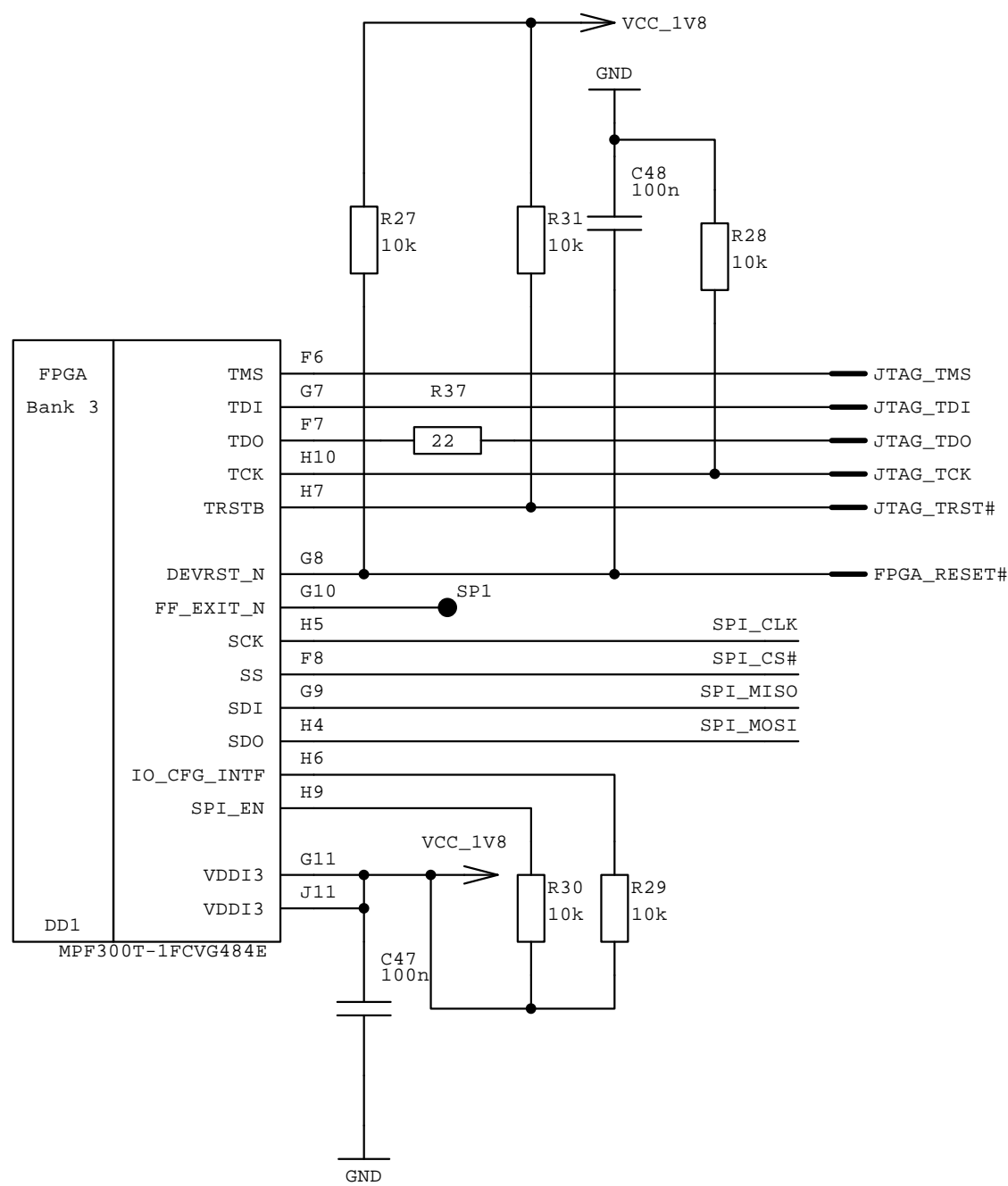
FPGA Power



CONTRACT NO.		Sundance Digital Signal Processing inc. 2019		
DRAWN	DATE	SE50P mPCIe addin Module		
DESIGNED	DATE			
REVIEWER	DATE			
DATE	DATE			
REV. NO.	REV. NO.	D	DATE	REV.
REV. NO.	REV. NO.	REVISION	SHEET	2 OF 7

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JTAG, Clocking



Sundance Digital Signal Processing inc. 2019			
SE50P mPCIe addin Module			
D	ASB/CSSD		
3	3	7	7

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DDR4 SDRAM

D

D

C

C

B

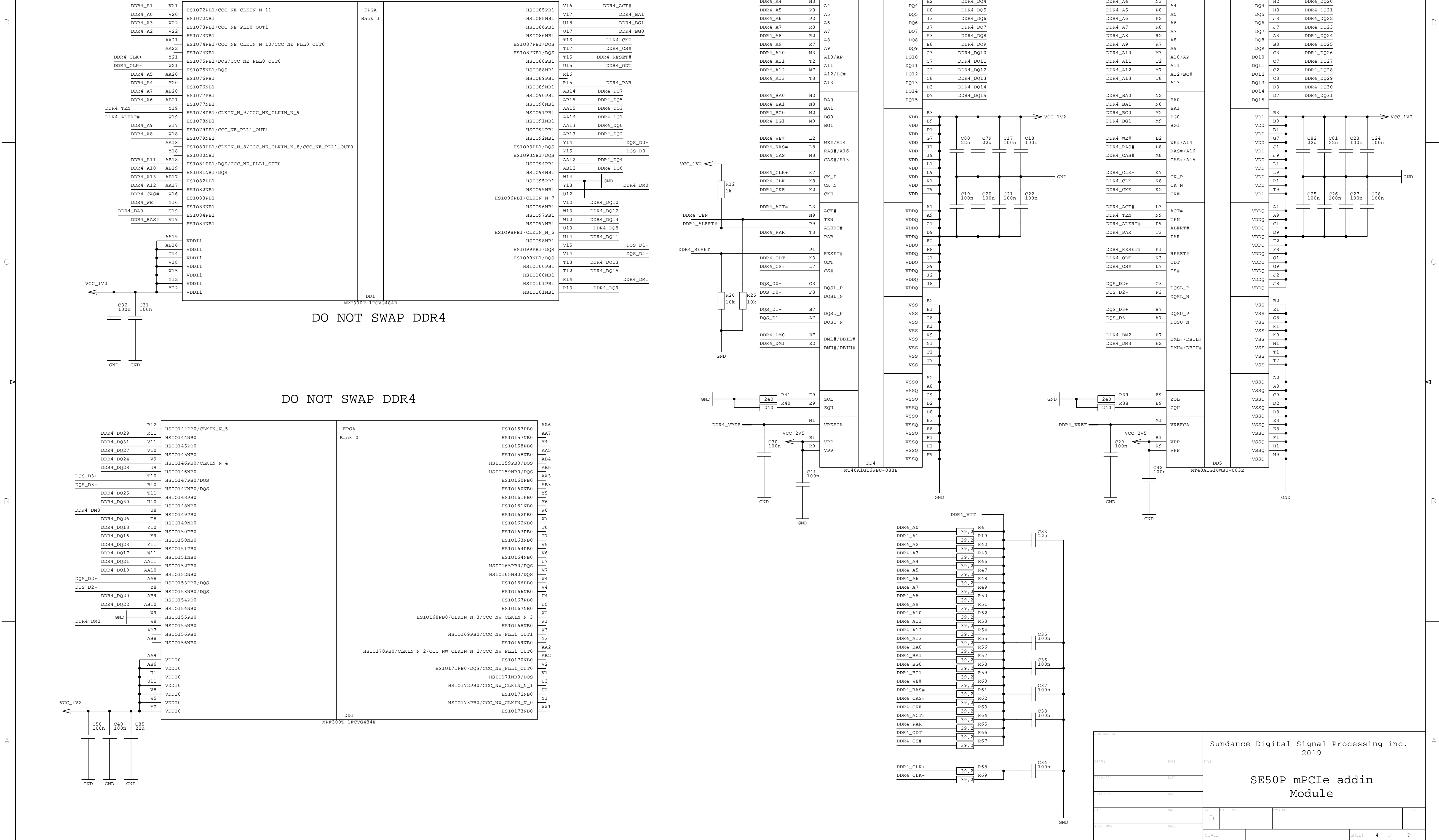
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B

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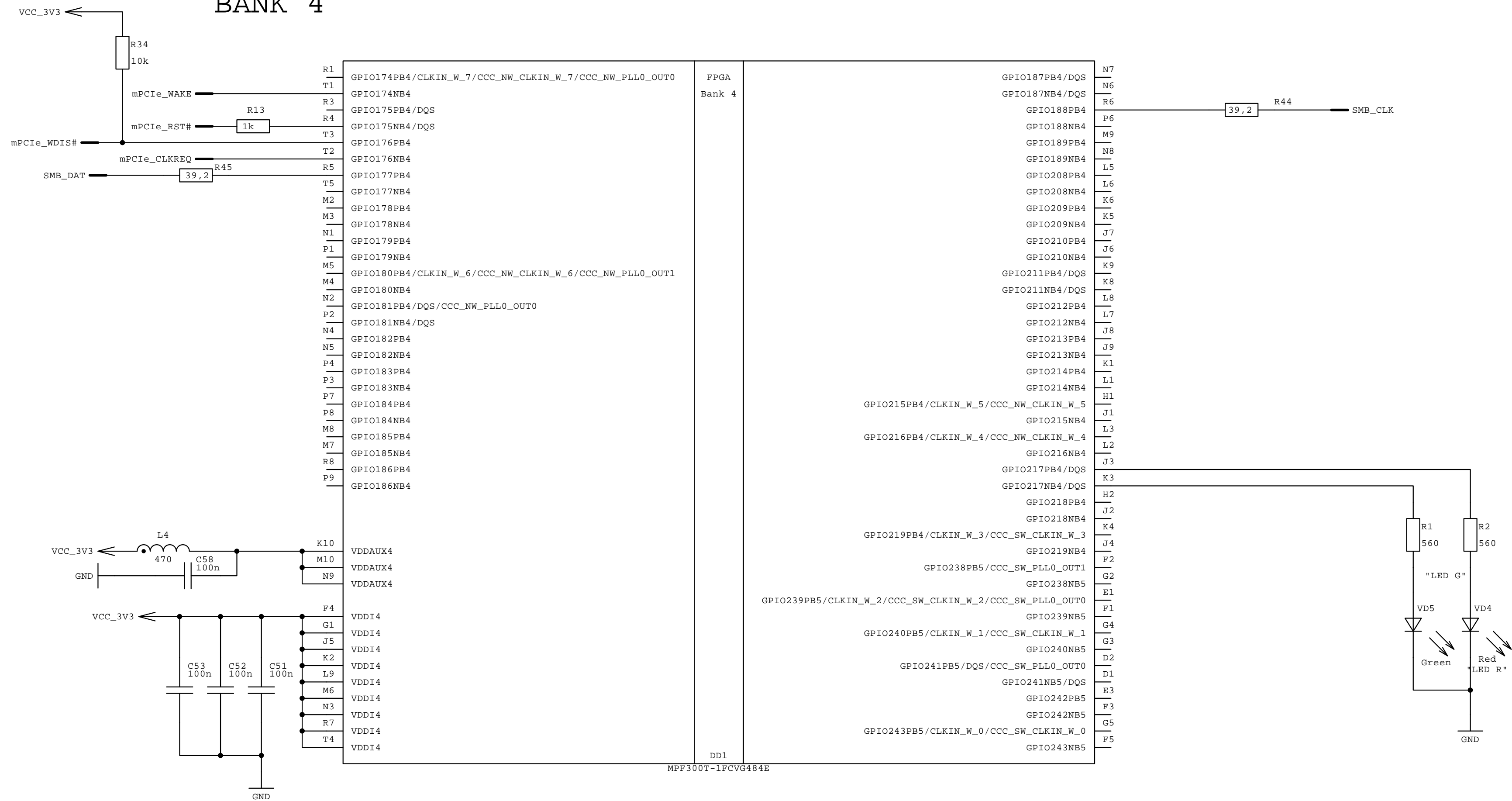
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A



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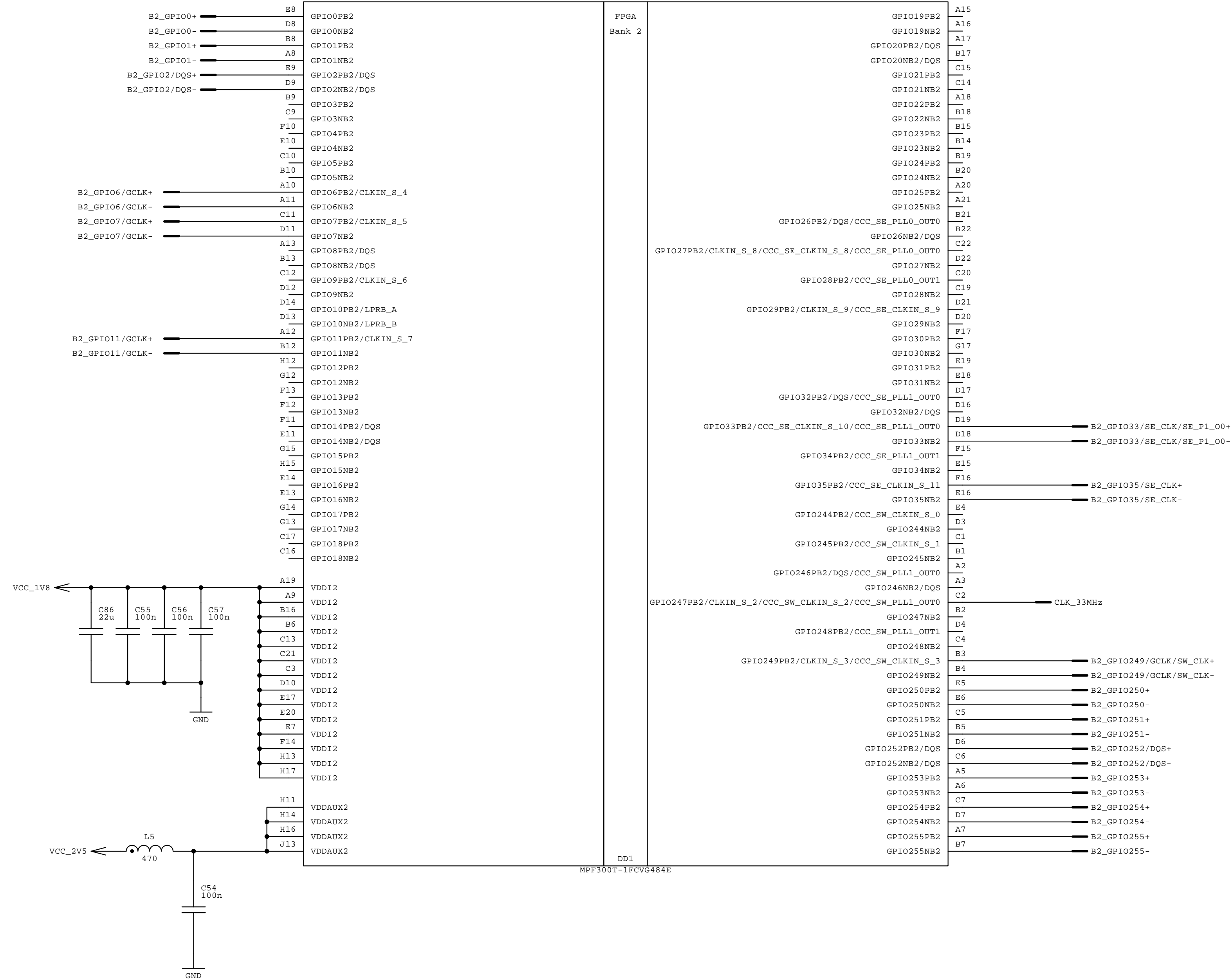
BANK 4



CONTRACT NO.		Sundance Digital Signal Processing inc. 2019		
DRAWN	DATE	SE50P mPCie addin Module		
DESIGNED	DATE			
CHECKED	DATE			
BY	DATE			
REV. ENG.	DATE	D	DATE	REV.
SCALE		SHEET 5 OF 7		

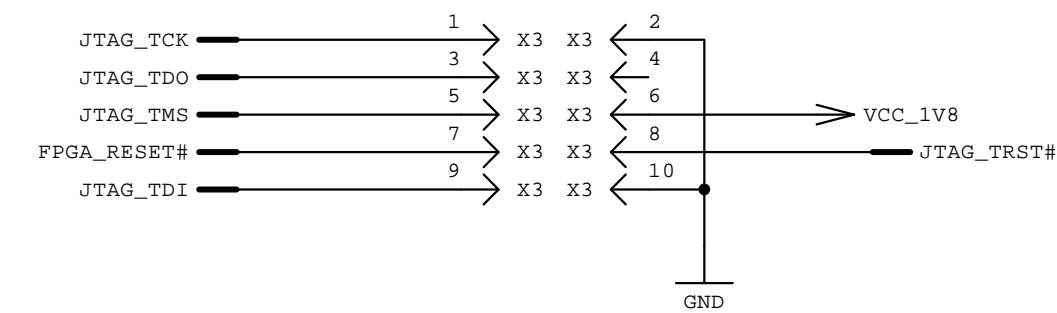
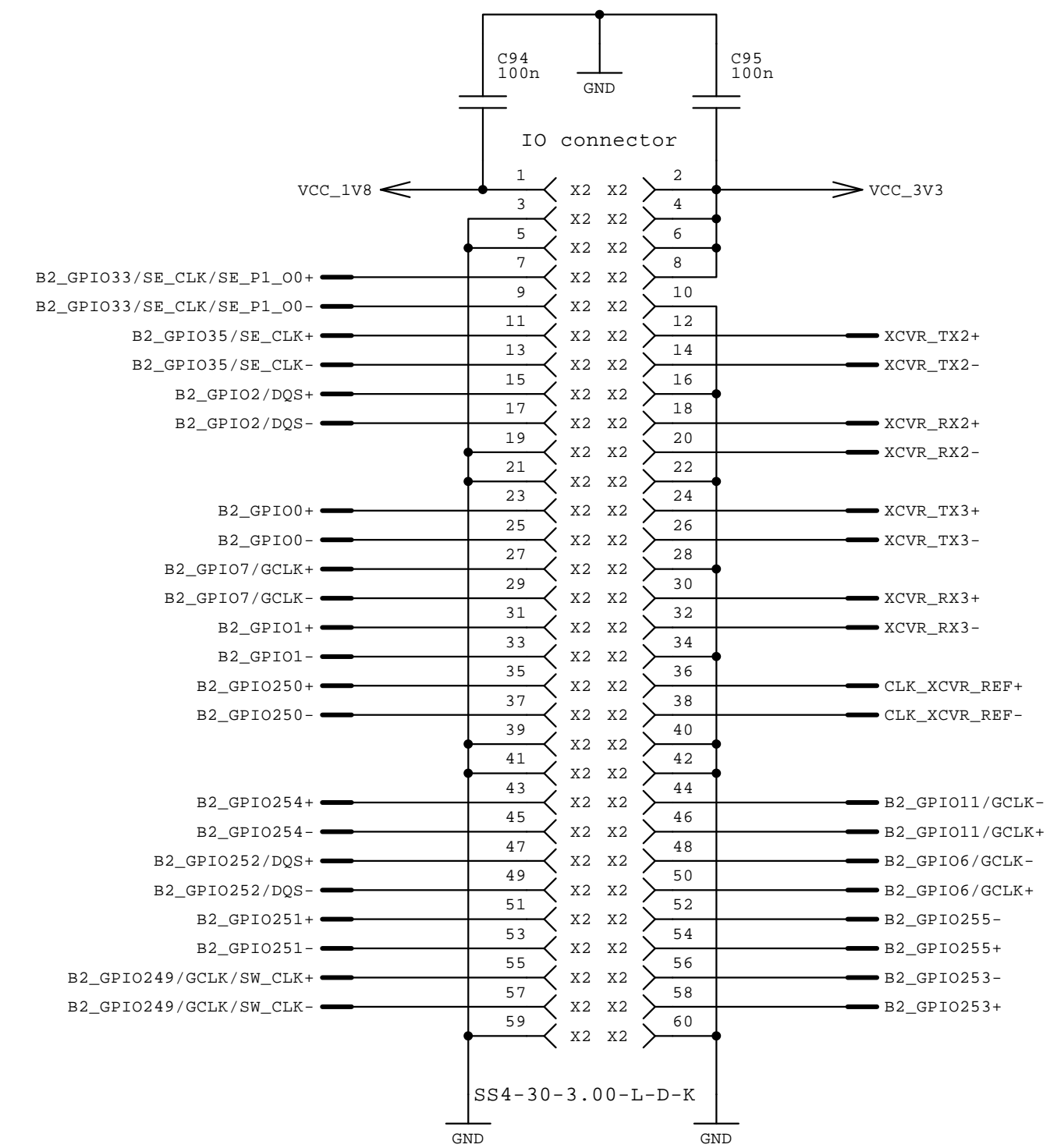
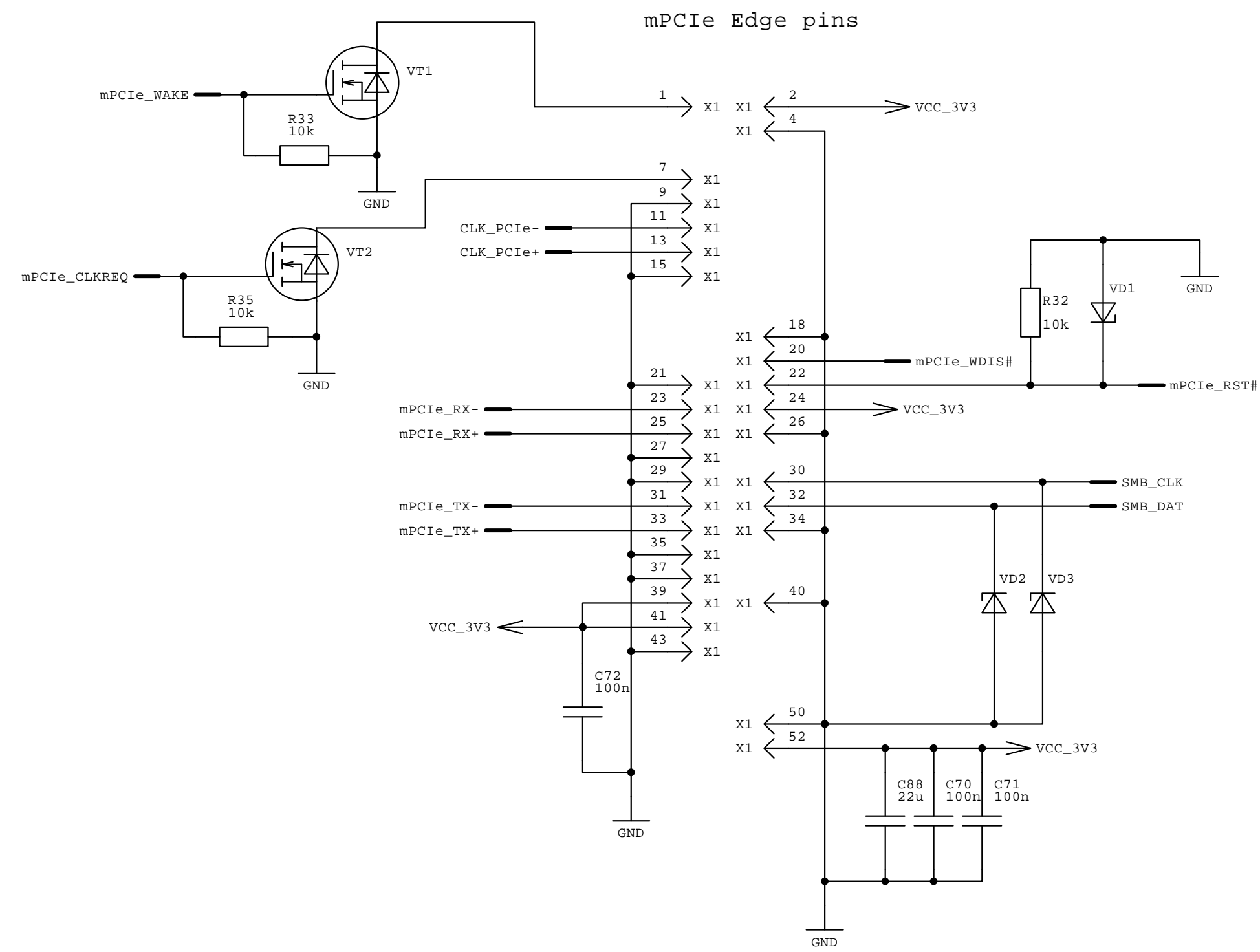
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BANK 2



CONTRACT NO.		Sundance Digital Signal Processing inc. 2019		
DRAWN	DATE	SE50P mPCIe addin Module		
DESIGNED	DATE			
CHECKED	DATE			
BY	DATE			
REV. NO.	DATE	D	REV. NO.	REV.
SCALE	DATE	SHEET 6 OF 7		

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Connectors



INSTRUMENT NO.		Sundance Digital Signal Processing inc. 2019			
OWNER	DATE	SE50P mPCIe addin Module			
RECORDED	DATE				
INDEXED	DATE				
BY	DATE	D	CASE CODE	SERIAL	
PLOT - MODEL	DATE	SCALE			SHEET 7 OF 7