1. What names are applied to the two types of BJT transistors? Sketch the basic construction of each and label the various minority and majority carriers in each. Draw the graphic symbol next to each. Is any of this information altered by changing from a silicon to a germanium base?

2. What is the major difference between a bipolar and a unipolar device?

3. How must the two transistor junctions be biased for proper transistor amplifier operation?

4. What is the source of the leakage current in a transistor?

5. Sketch a figure for the forward-biased junction of an *npn* transistor.
Describe the resulting carrier motion.

6. Sketch a figure for the reverse-biased junction of an *npn* transistor. Describe the resulting carrier motion.

7. Sketch a figure for the majority- and minority-carrier flow of an *npn* transistor.
Describe the resulting carrier motion.

8. Which of the transistor currents is always the largest? Which is always the smallest? Which two currents are relatively close in magnitude?

9. For a common base amplifier, If the emitter current of a transistor is 8 mA and I_B is 1/100 of I_C, determine the levels of I_C and I_B.

10. For a common base amplifier,
a. Using the characteristics of Figs. 3.7 and 3.8 (Below) , determine I_C if V_{CB} = 5 V and V_{BE} = 0.7 V.
b. Determine V_{BE} if I_C = 5 mA and V_{CB} =15 V.
c. Repeat part (b) using the characteristics of Fig. 3.10b .
d. Repeat part (b) using the characteristics of Fig. 3.10c .
e. Compare the solutions for V_{BE} for parts (b) through (d).
Can the difference be ignored if voltage levels greater than a few volts are typically encountered?

11. **a.** Given an α_{dc} of 0.998, determine I_C if I_E = 4 mA.
b. Determine α_{dc} if I_E = 2.8 mA, I_C = 2.75 mA and I_{CBO} = 0.1 mA.

12. From memory only, sketch the common-base BJT transistor configuration (for *npn* and *pnp*) and indicate the polarity of the applied bias and resulting current directions.

13. **a.** For the common-emitter characteristics of Fig. 3.13 , find the dc beta at an operating point of V_{CE} = 6 V and I_C = 2 mA.
b. Find the value of a corresponding to this operating point.
c. At V_{CE} = 6 V, find the corresponding value of I_{CEO} .
d. Calculate the approximate value of I_{CBO} using the dc beta value obtained in part (a).

14. **a.** Using the characteristics of Fig. 3.13a , determine I_{CEO} at V_{CE} = 10 V.
b. Determine β_{dc} at I_B = 10 mA and V_{CE} = 10 V.
c. Using the β_{dc} determined in part (b), calculate I_{CBO} .

15. **a.** Given that α_{dc} = 0.980, determine the corresponding value of β_{dc} .
b. Given β_{dc} = 120, determine the corresponding value of a.
c. Given that β_{dc} = 120 and I_C = 2.0 mA, find I_E and I_B .

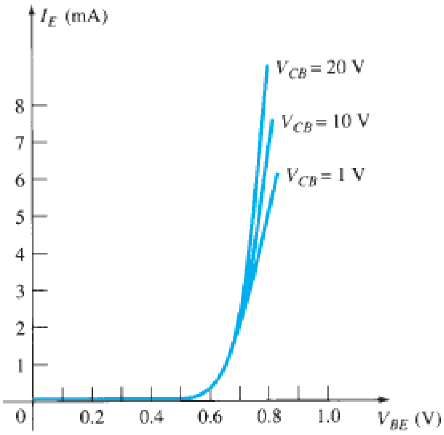

FIG. 3.7
Input or driving point characteristics for a common-base silicon transistor amplifier.

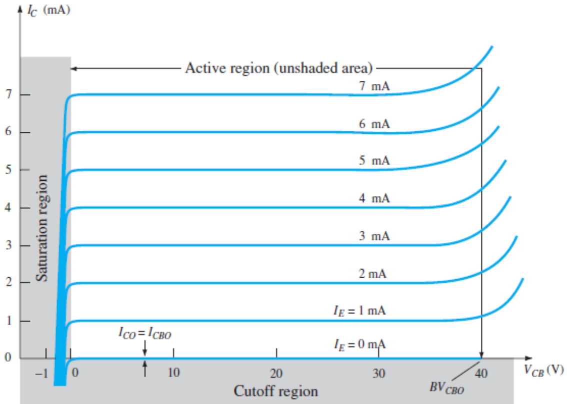

FIG. 3.8
Output or collector characteristics for a common-base transistor amplifier.

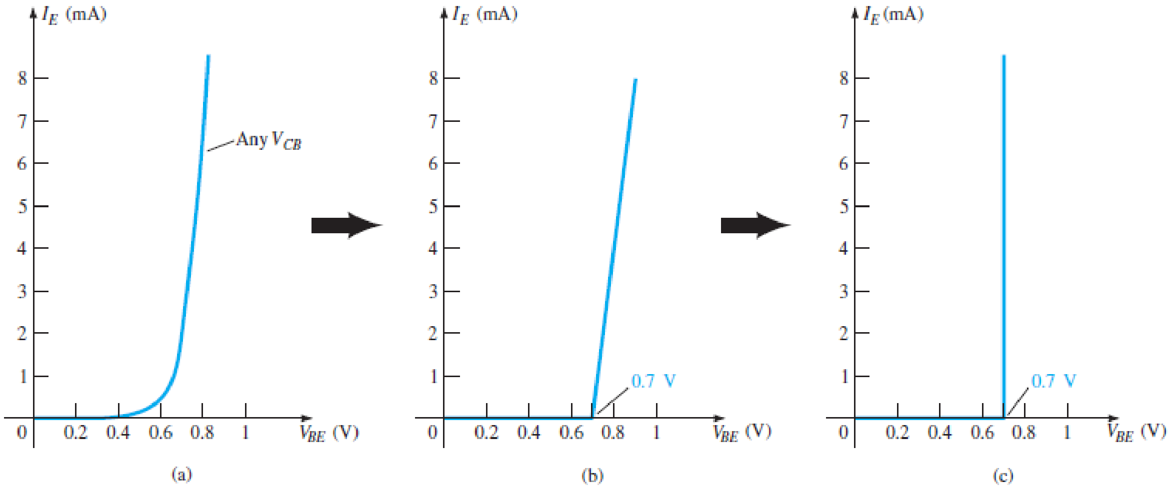

FIG. 3.10
Developing the equivalent model to be employed for the base-to-emitter region of an amplifier in the dc mode.

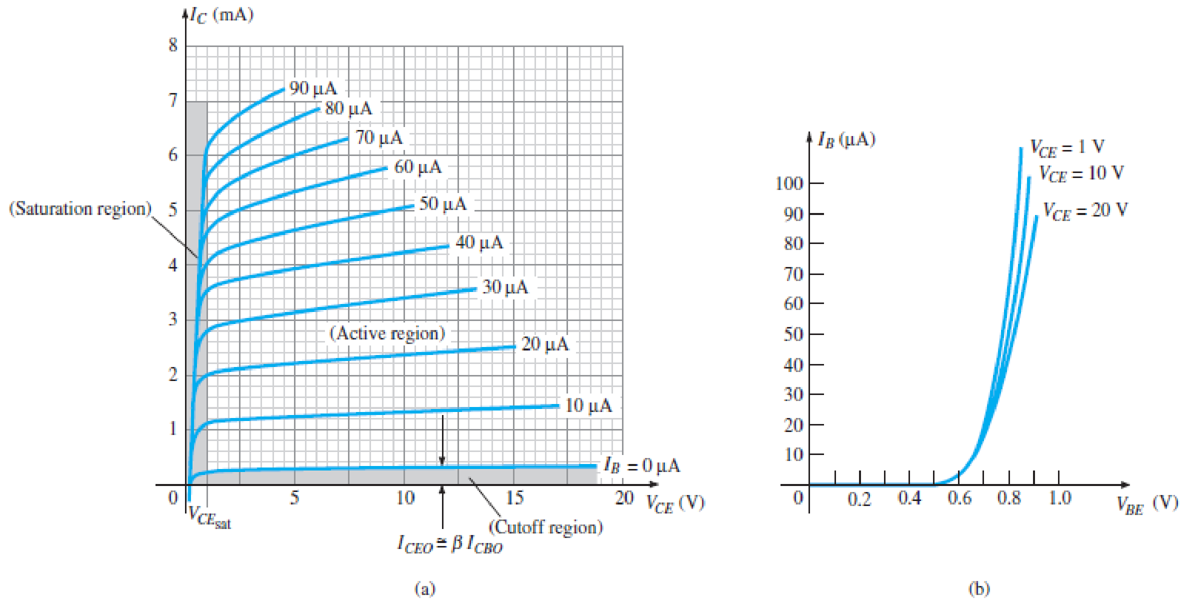

FIG. 3.13

Characteristics of a silicon transistor in the common-emitter configuration: (a) collector characteristics; (b) base characteristics.