

Pre-Laboratorio #3
Electrónica Analógica
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Valores obtenidos en Simulación

Icq etapa 2= 3.518mA

Icq etapa 1 = 2.581mA

Vbb etapa 1 = 1.189 V

Vbb etapa 2 = 1.032 V

Vce etapa 1 = 6.323 V

Vce Etapa 2 = 4.964 V

Amp max = 4.1379 V

frec corte bajo = 74.589 Hz

Frec corte alto = 2.719MHz

Ancho de banda = 2.719MHz

Vo max = 3.042v - 3.3676v

Vo p = 3.205 v

Vi max = 0.07v - 0.7v

Avt =43.457 v

Io max = 1.52mA - 1.683mA

lo p = 1.6015 mA

limax = 16.244 uA -16172 uA

Ait=93.573

Vo 1era etapa =0.357812V

Vi 1era etapa = 69.989mV

Av etapa 1 = 5.112

Ii primera etapa = 16.244uA

Io primera etapa = 166.410 uA

Ai primera etapa = 10.244

Vi etapa 2 = 1.3889 -1.031088 = 0.357812V

Vo etapa 2 = 3.0412V

Av etapa 2 = 8.499

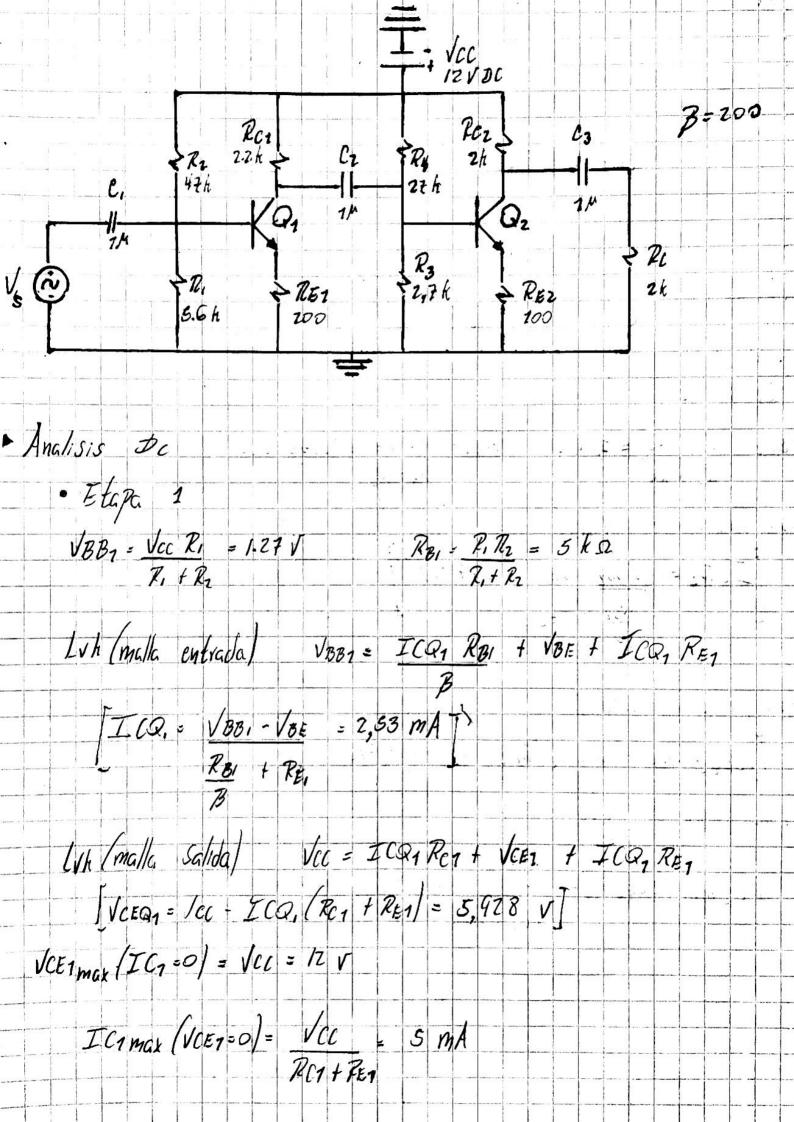
Io Etapa2 = 1.5206 mA

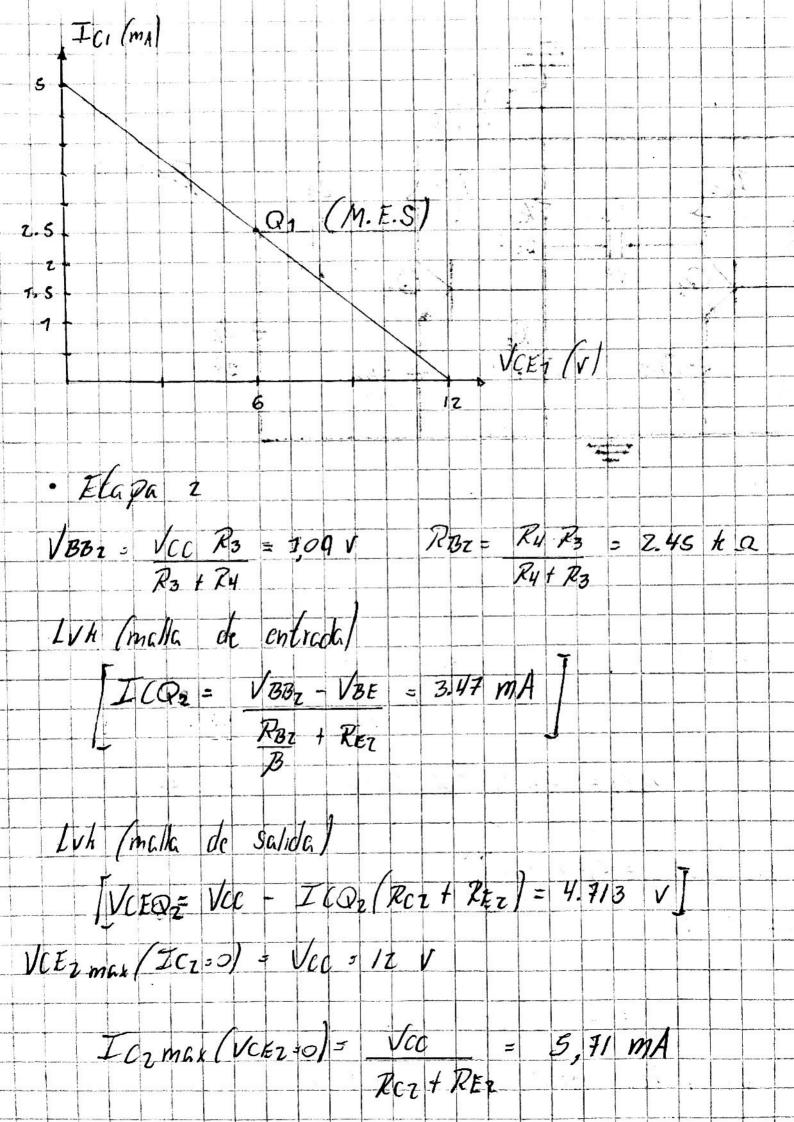
Ii etapa2 = 169.087 uA

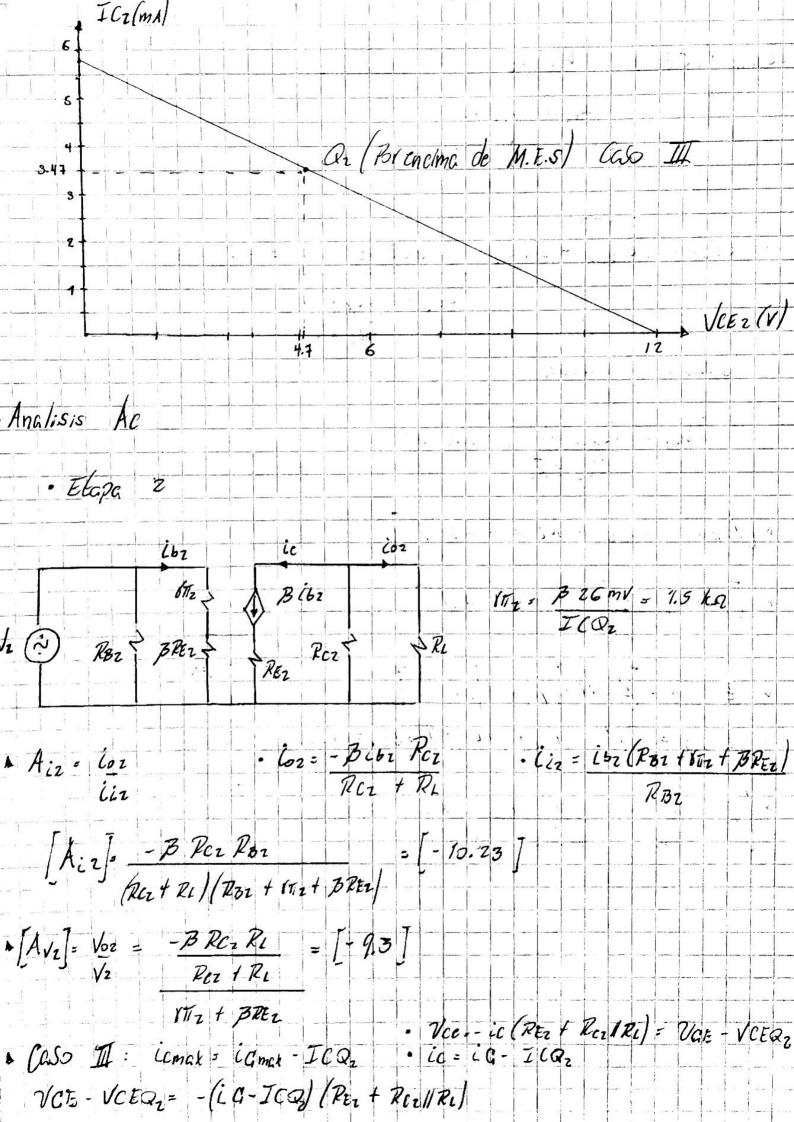
Ai Etapa 2 = 8.989

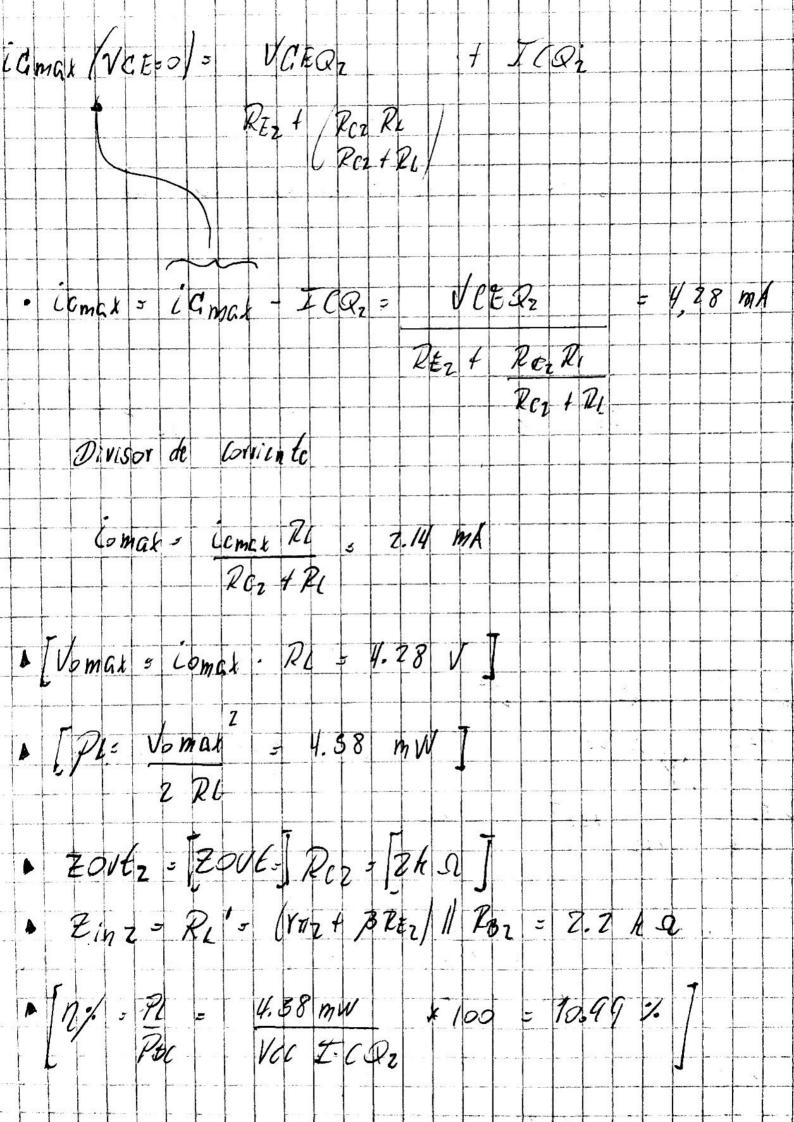
Pot p = 2.568 mW

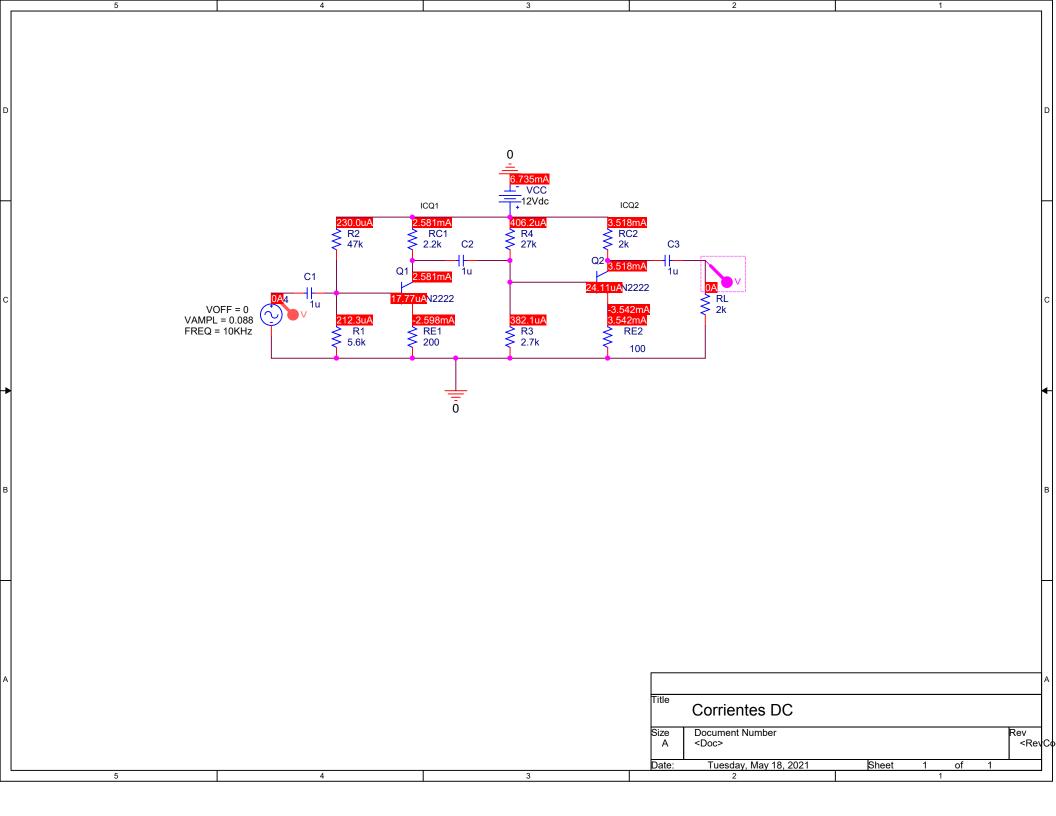
Eficiencia = 6.083%

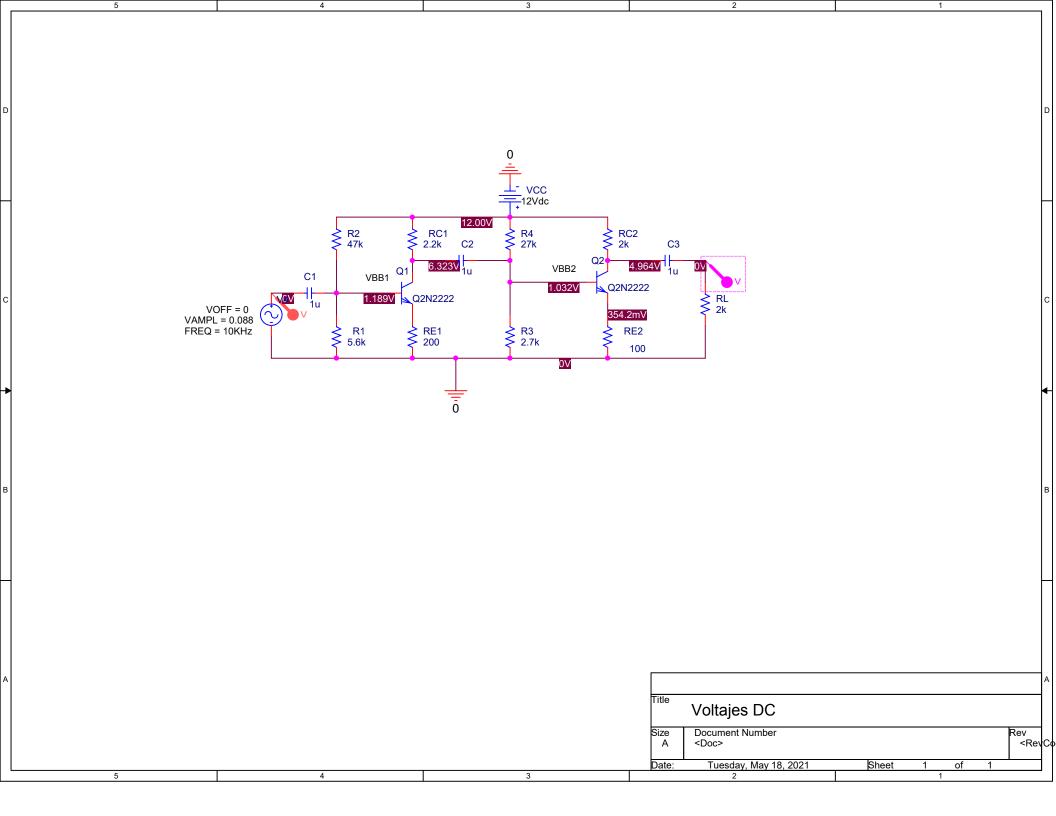






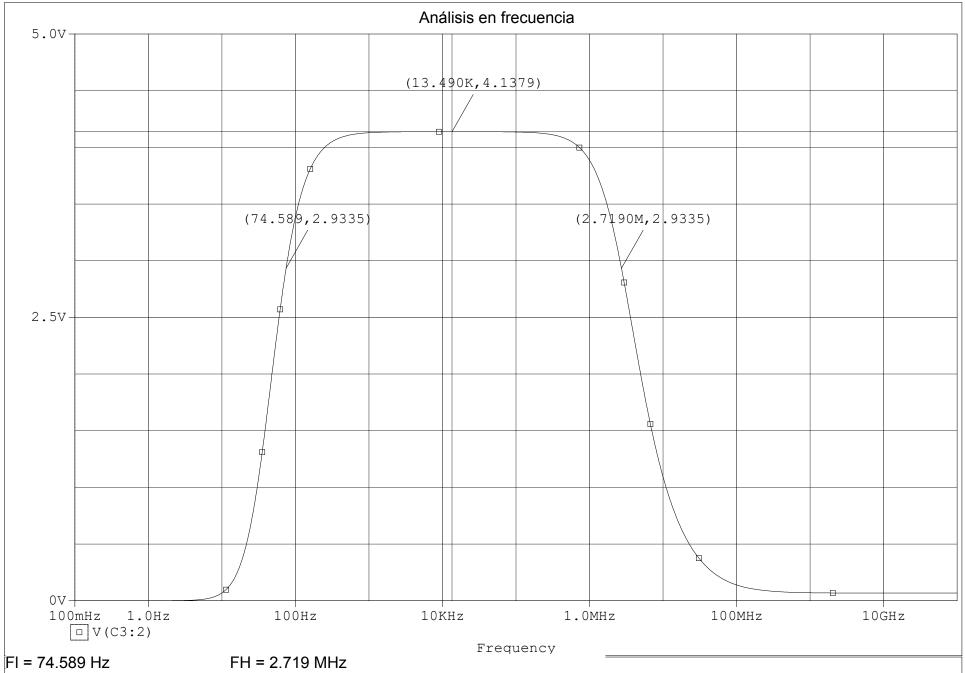




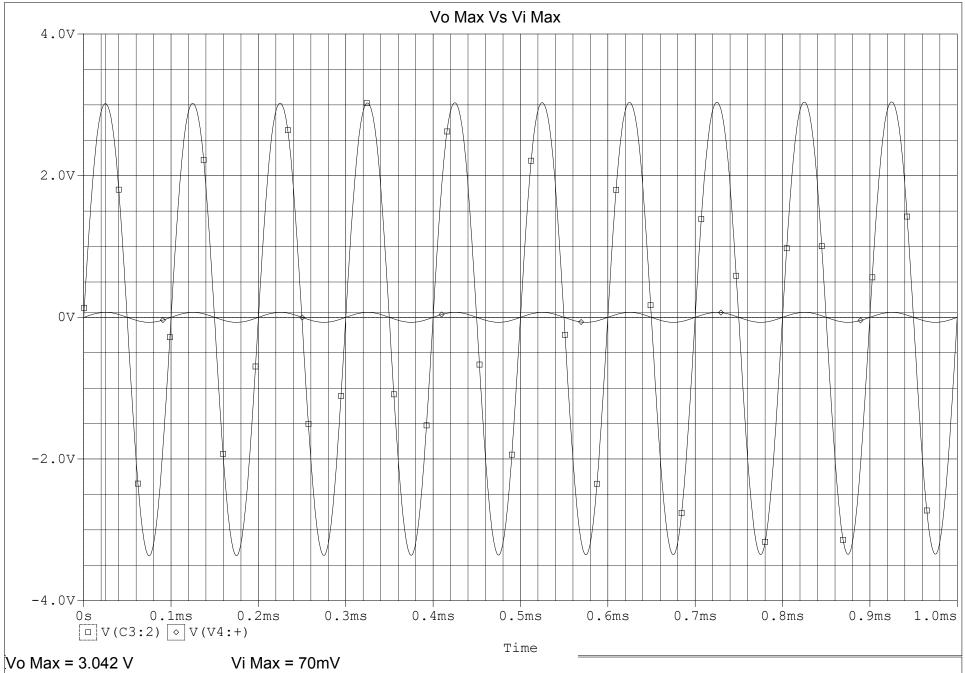


** Profile: "SCHEMATIC1-Bias" [C:\Program Files\Orcad\prelab 3-schematic1-bias.sim]

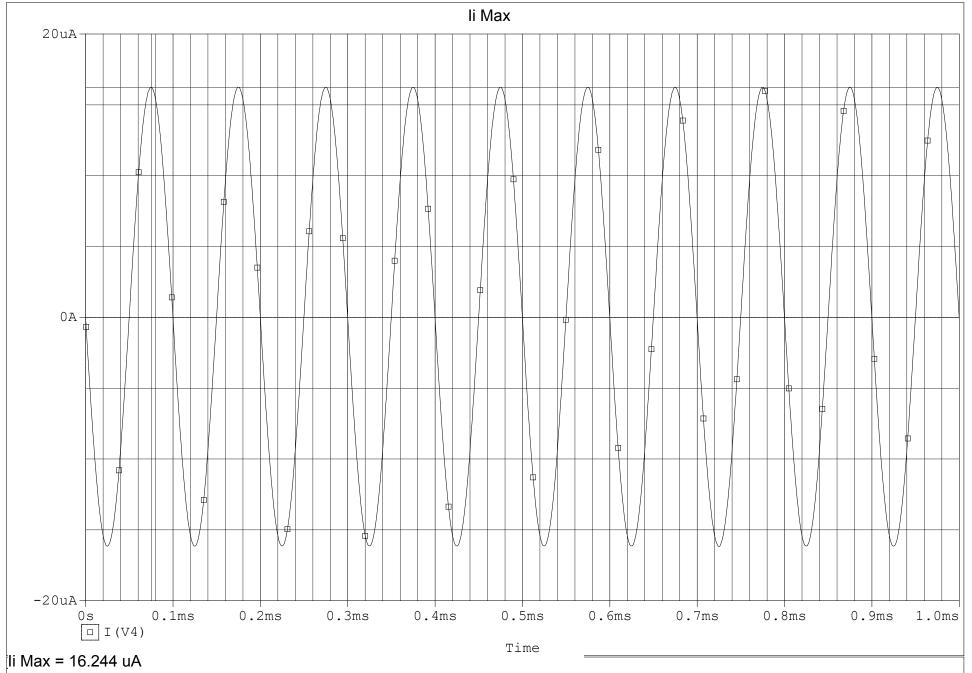
Date/Time run: 05/18/21 13:56:12 Temperature: 27.0



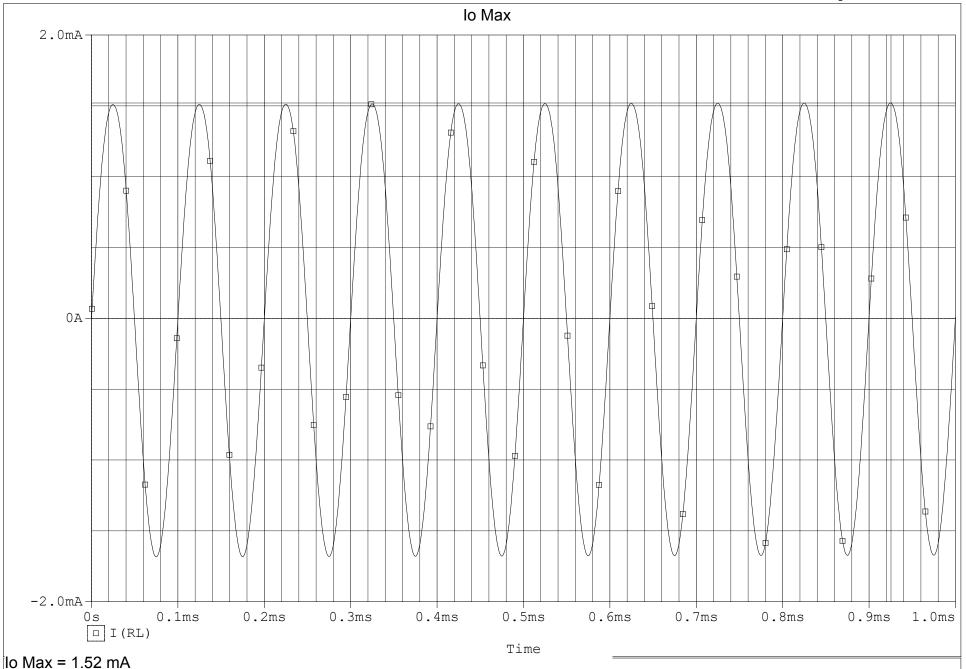
Date/Time run: 05/18/21 15:29:07 Temperature: 27.0



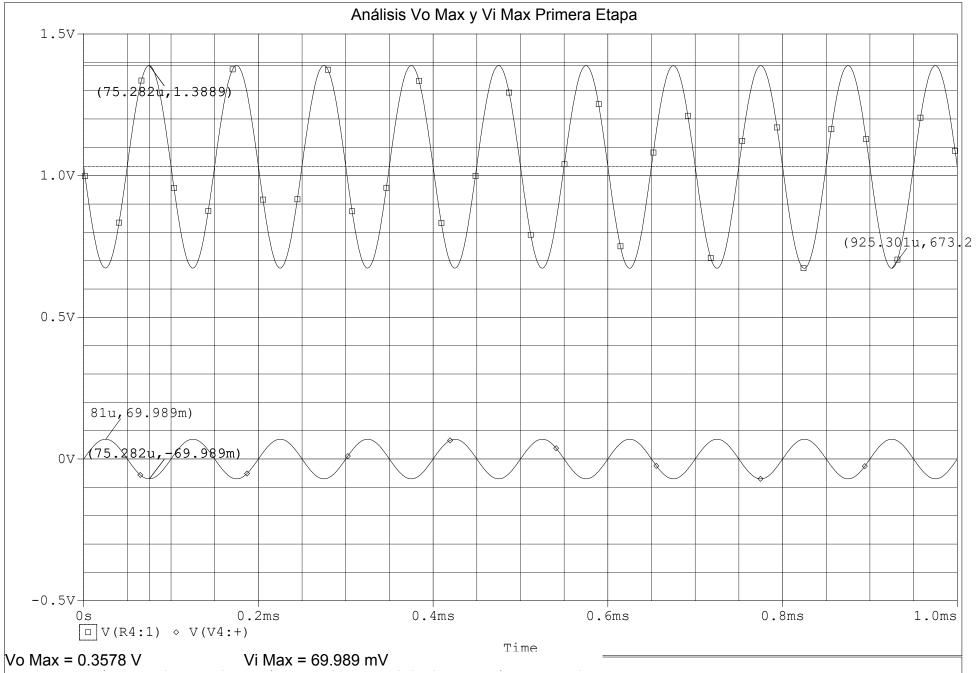
Date/Time run: 05/18/21 15:33:20 Temperature: 27.0



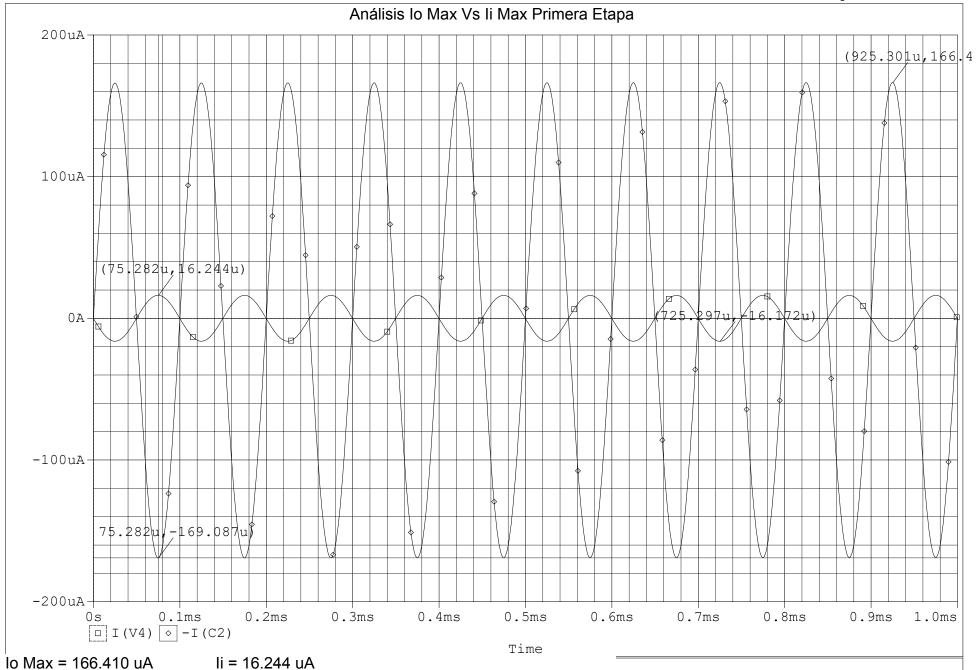
Date/Time run: 05/18/21 16:44:34 Temperature: 27.0



Date/Time run: 05/18/21 15:54:14 Temperature: 27.0



Date/Time run: 05/18/21 16:07:33 Temperature: 27.0



** Profile: "SCHEMATIC1-Bias" [C:\Program Files\Orcad\prelab 3-schematic1-bias.sim]

Date/Time run: 05/18/21 16:03:33 Temperature: 27.0 Análisis Vo max Vs Vi Max segunda Etapa 4.0V-(925.301u,3.04 2.0V-(7\$.282u, 1.3889) (924.3014,673.2 0V--2.0V-(75.28/24, -4.0V-0.2ms 0.4ms 0.6ms 0.8ms 1.0ms □ V(C3:2) ◇ V(R4:1) Time Vo max = 3.0412 V Vi Max = 0.357 V

Date/Time run: 05/18/21 16:15:50 Temperature: 27.0

