# PRE-INFORME PRÁCTICA DE LABORATORIO Nº. 3

Juliana Garzon Fajardo <sup>1</sup>, Jose Luis Mazuera Cardenas<sup>2</sup>, Gabriela Quintero Moreno<sup>3</sup>

8959024<sup>1</sup>,8958129<sup>2</sup>, 8957920<sup>3</sup>

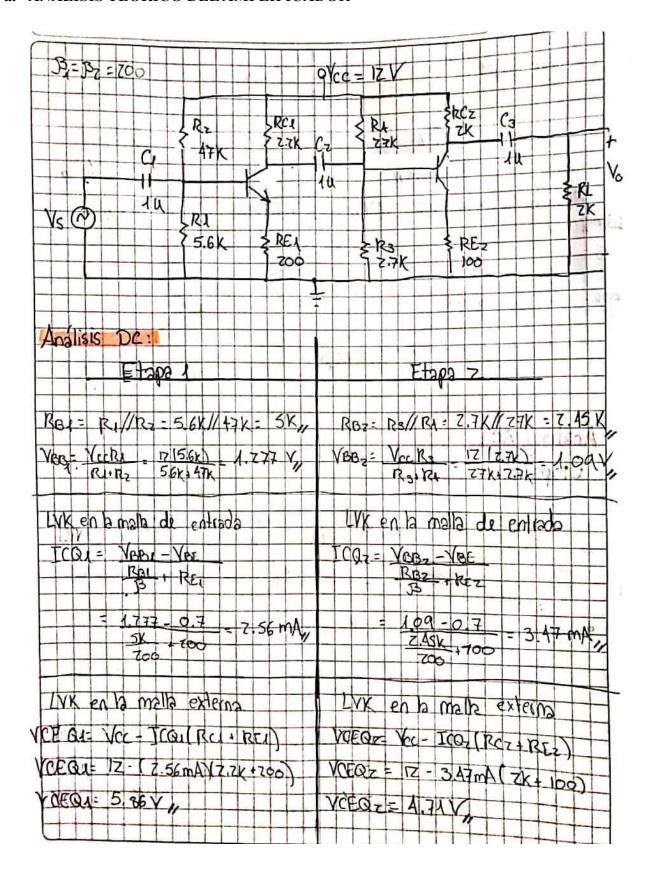
juligf2001@javerianacali.edu.co<sup>1</sup>, jmazuera24@javerianacali.edu.co<sup>2</sup>, gabyquimo@javerianacali.edu.co<sup>3</sup>

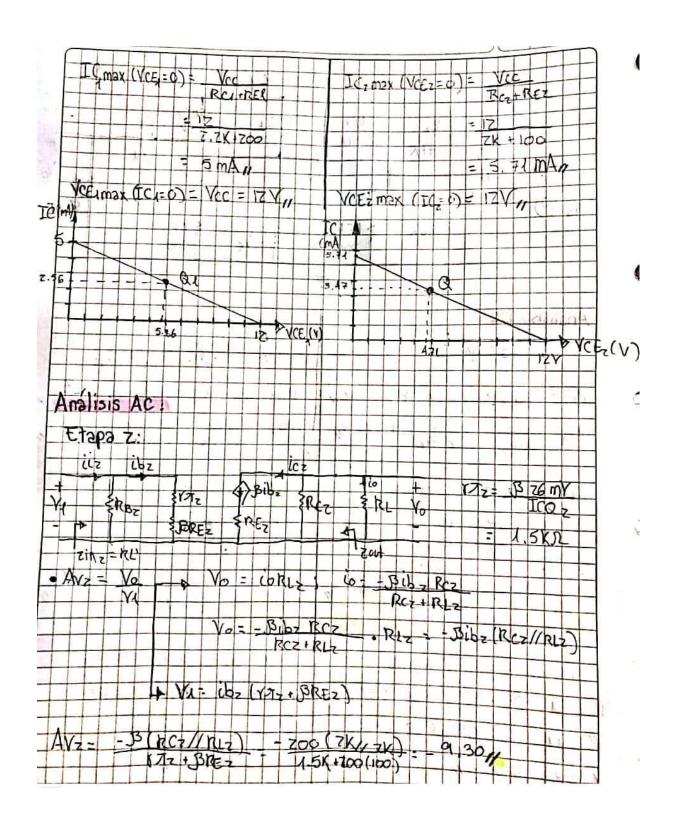
Hernan Dario Vargas Cardona

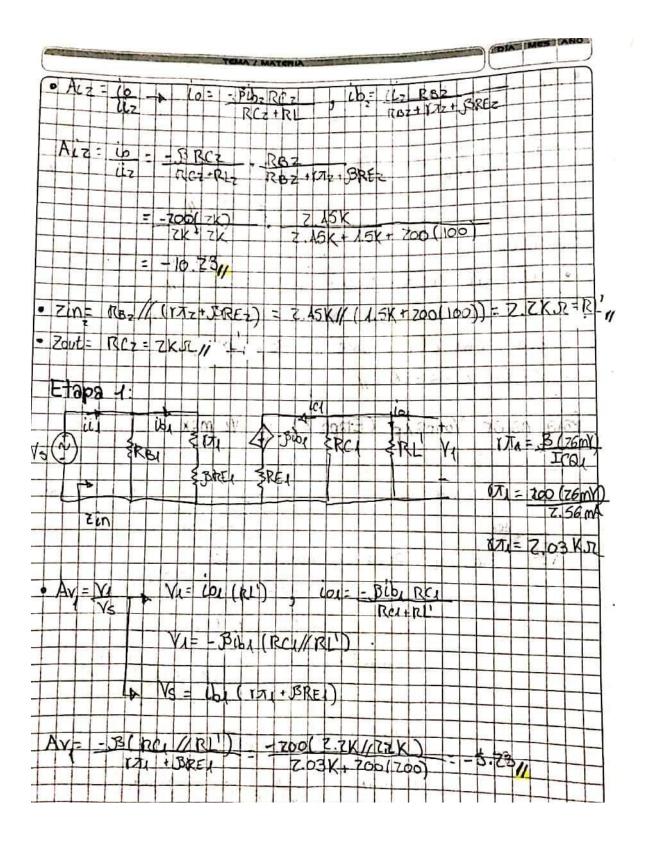
Universidad Javeriana Cali, Facultad de ingeniería y ciencias, Laboratorio de electrónica análoga.

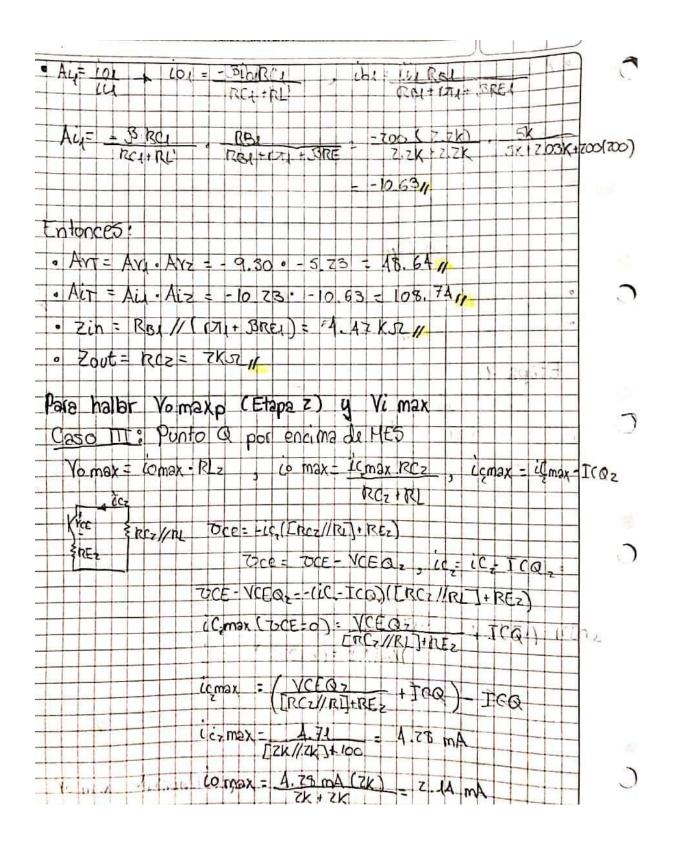
Laboratorio #3
Santiago de Cali, Colombia
(Realización 19 abril 2022 / Initiation april 19th, 2022)
(Entrega 20 abril 2022/ Submission april 20th, 2022)

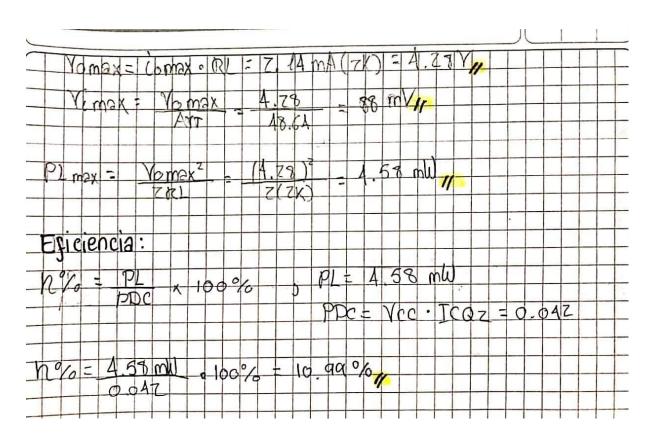
# a. ANÁLISIS TEÓRICO DEL AMPLIFICADOR











# Etapa 1

DC:

*ICQ1*: 2.56mA, *VCEQ1*: 5.86v

AC:

<u>AV1:</u> -5.23, <u>Ai1:</u> -10.63

#### Etapa 2

DC:

*ICQ2*: 3.47mA, *VCEQ2*: 4.71v

AC:

<u>AV2:</u> -9.3, <u>Ai2:</u> -10.23

**AVT:** 48.64

*AiT*: 108.74

**Zin:** 4.47KΩ

Zout: 2KΩ

<u>Vomax:</u> 4.28V <u>Vimax:</u> 88mV

**PLmax:** 4.58mW

#### b. SIMULACIONES

Resultados obtenidos en simulaciones:

# Etapa 1

#### ICQ1 = 2.638 mA

VBB1 = 1.200 V

VCE1 = 6.196 V

Vo1 = 0.357812V

Vi1 = 69.989 mV

### Av1 = 5.112

Ii1 = 16.244uA

Io1 = 166.410 uA

Ai1 = 10.244

### Etapa 2

# ICO2 = 3.595 mA

VBB2 = 1.040 V

VCE2 = 4.810 V

Vi 2 = 1.3889 - 1.031088 = 0.357812V

 $V_{02} = 3.0412V$ 

#### Av2 = 8.499

Io2 = 1.5206 mA

Ii2 = 169.087 uA

Ai2 = 8.989

AMPLITUD MÁXIMA = 4.1379 V FRECUENCIA CORTE BAJO = 74.589 Hz

FRECUENCIA CORTE ALTO = 2.719MHz

<u> ANCHO DE BANDA = 2.719MHz</u>

Vomax = 3.042v

 $\underline{Vimax} = 0.07v$ 

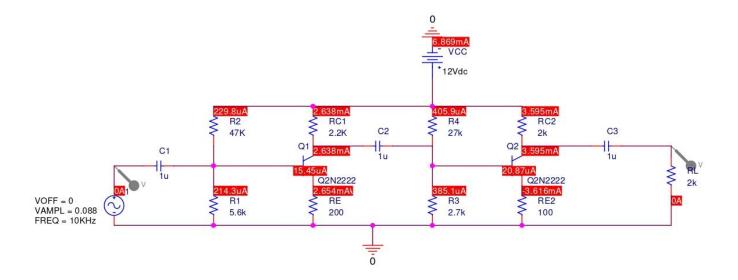
Iomax = 1.71mA

*<u>Iimax</u> = 19.55 uA* 

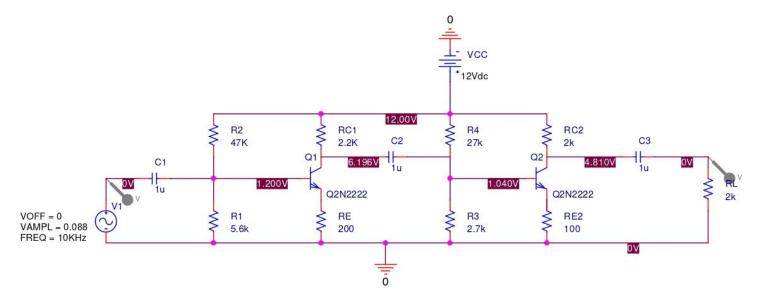
PL = 2.568 mW

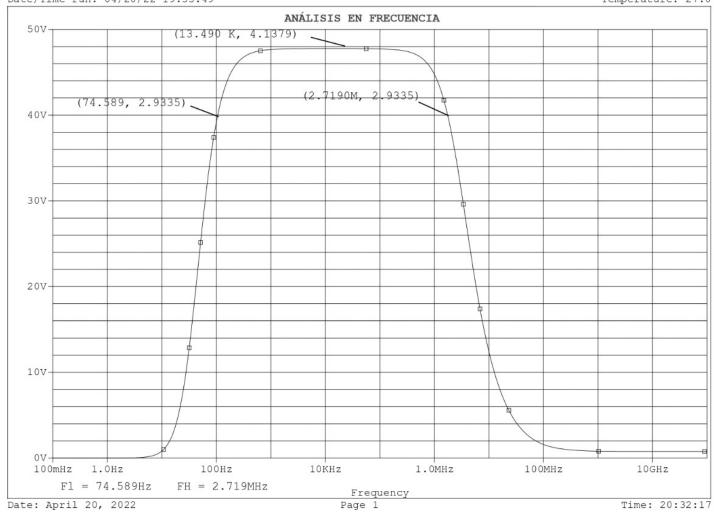
 $n\% = (PL/PDC) \times 100\% = 6.083\%$ 

## **Corrientes DC**



# **Voltajes DC**





\*\* Profile: "SCHEMATICI-Sumulacion esta si" [ c:\laboratorios analoga\prelap 3 este si-schematici-sumul...

