



**R.M.D. ENGINEERING COLLEGE**  
R.S.M. NAGAR, KAVARAIPETTAI -601206, THIRUVALLUR, TAMILNADU



**DEPARTMENT OF ELECTRICAL AND ELECTRONICS  
ENGINEERING**

**TWO HOURS WEBINAR SESSION ON**

**DESIGN OF PROCESSOR CIRCUITS WITH VERILOG HDL  
(WEBINAR SERIES-1)  
09-05-2020**

**EXPERT ON SESSION**

**Mr.GANESH SKANDHA**  
Senior Engineer  
Microchip Technology  
Chennai

[Click here for Registration](#)

**CONVENOR**

**Dr .V. TAMIL SELVI, PROFESSOR & HEAD , EEE**  
**DEPARTMENT OF EEE**

**ORGANIZER**

**Mr. S.SAYEEKUMAR, ASSISTANT PROFESSOR / EEE**  
**DEPARTMENT OF EEE**