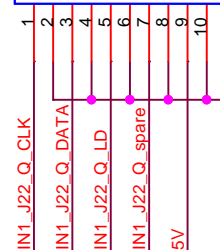
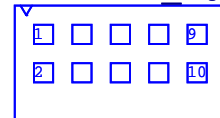


J1 DF11_10DP_2DS_52

On PCB Top

GREEN

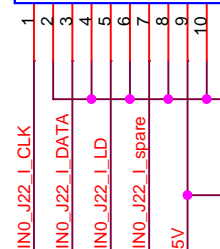
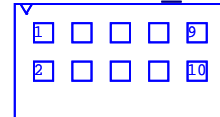
connects to HiRose DF11-10DS-2C,
and via twisted pair wires to
the electrical feed thrus on the
attemplifier modules.



J2 DF11_10DP_2DS_52

On PCB bottom

BLUE

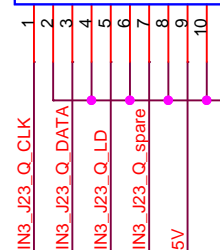
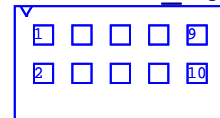


C5
.01uF

J3 DF11_10DP_2DS_52

On PCB Top

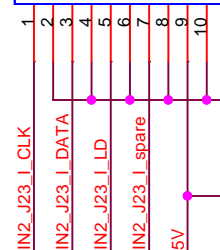
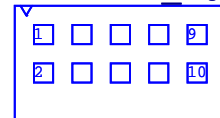
RED



J4 DF11_10DP_2DS_52

On PCB bottom

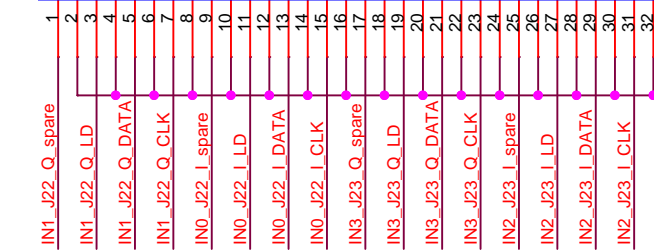
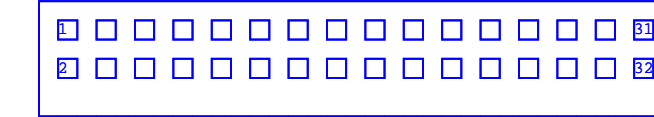
YELLOW



C4
.01uF

J5 DF11_32DP_2DS_52

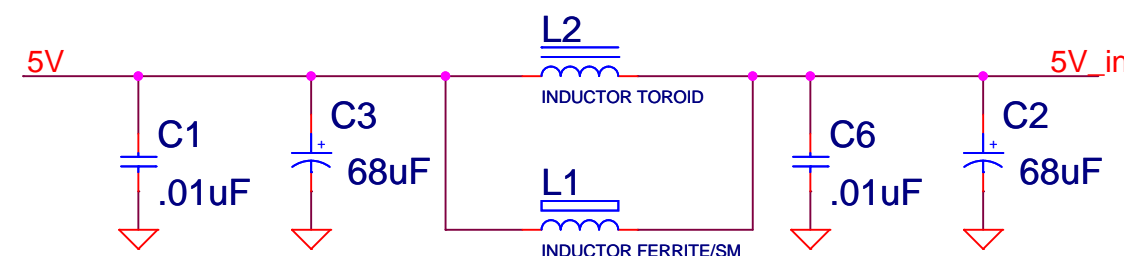
On PCB bottom



GREEN BLUE RED YELLOW

connects to HiRose DF11-32DS-2C,
and via twisted pair wires :
J5-1 to iBob J9-1 and J5-2 to J9-2
and so on.
still thinking about
cable assembly options.

The plan is to installed C2 and C3 and not
populate C1, C4,C5 and C6.



Either L1 or L2 is
installed. Not both.

TP1

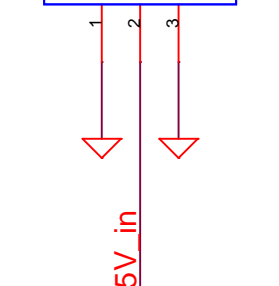
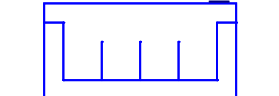
T POINT R

TP2

T POINT R

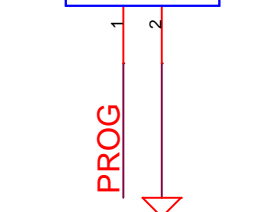
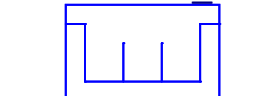
5V

P3 S3B EH

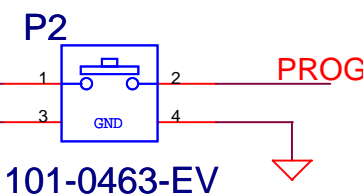


On PCB Top
connects to JST EHR-3
and via twisted pair wires :
J6-2 to iBob J19-1 and J19-2
J6-1 and J6-3 to iBob J19-3 and J19-4

P2 S2B EH



On PCB Top
connects to JST EHR-2
and via twisted pair wires to
2 pin socket housing on 0.1" pitch
J7-1 to iBob J1-1 and
J7-2 to iBob J1-2.



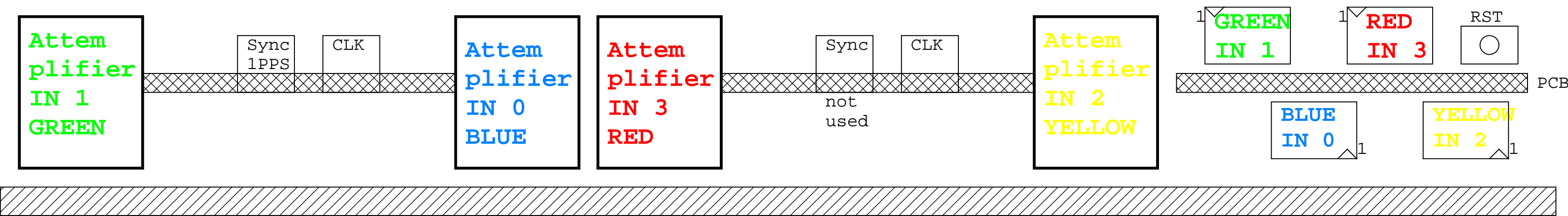
Reset button for
the iBob's FPGA.
button is parallel
to plane of PCB

C:\ata\correlator\production\iBob\Schematics\attemplier_iface.opj			
Title interface board for 4-way attemplier control			
Size Custom	Document Number attemplier_iface: schematic1: page1	Rev 1.0	
Date: Monday, March 31, 2008	Sheet 1	of 1	

Edge on view from the back of the iBob + iADC chassis

J22 iADC

J23 iADC



Aluminum mounting plate

NOT TO SCALE

PCB will rest on 4 #4-40 0.25" tall swage standoffs. P/N is TBD. And be held down with #4 5/8" screw. OD of standoffs is 0.25"

