

VERY WIDEBAND 50 to 10000 MHz



ADCH

	FREQUENCY MHz	INSERTION LOSS [†] (dB)		VSWR [†] (:1)		DC CURRENT (mA)	INDUCTANCE (μΗ) Typ.		CASE STYLE	CONNEC	PRICE \$	
MODEL◆ NO.		Тур.	Max.	Тур.	Max.	Max.	@ 0 mA	@ 50 mA	@ 100 mA	Note B	T O N	Qty. (10-49)
ADCH-80*	50 - 8000 50-10000	0.3 0.3	1.0 2.0	1.1 1.1	1.35 1.6	100	7.0	1.8	1.0	CD542	lv	2.75
ADCH-80A*	50 - 8000 50-10000	0.3 0.3	1.0 2.0	1.1 1.1	1.35 1.6	100	7.0	1.8	1.0	CD542	mk	2.75

[†] Tested with circuit shown below, Zo=50 ohms

features

- low parasitic capacitance 0.1 PF typ.
- effective parallel resistance, Rch 800 ohm typ.

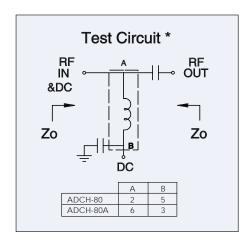
applications

amplifier biasing

NOTES:

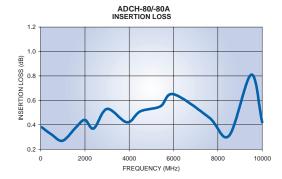
- Aqueous washable.Protected under U.S. Patent 6133525.
- A. Environmental specifications and re-flow soldering information soldering in Constal Information Soldering
- tion available in General Information Section.

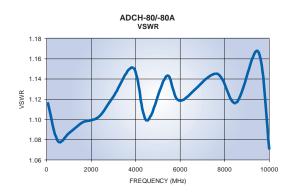
 B. Units are non-hermic unless otherwise noted. For details on
- B. Units are non-hermetic unless otherwise noted. For details or case dimensions & finishes see "Case Styles & Outline Drawings".
- C. Prices and Specifications subject to change without notice.
- Absolute maximum power, voltage and current rating:
 DC current 250mA



pin connections

see case style outline drawings								
PORT	lv	mk						
INPUT	2	6						
OUTPUT	5	3						
NOT USED	1,3,4,6	1,2,4,5						
DEMO BOARD	TR-52	TR-113						





Mini-Circuits

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