CyberSecurity Club at San Francisco State University

Initial Design Document

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Abstract

This research focuses on designing and implementing a secure MISC device that will be built to adhere to the standards of the functional and security requirements. Our design will aim to withstand all attack scenarios presented by the MITRE documentation, as well as provide a new approach to embedded system programming with the popular and recent memory-safe Rust programming language.

1. Introduction

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1.1. Methods

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Table 1. A cool table

Column A	Column B	Column C
1	2	3

- Lorem
- Ipsum

This text contains a citation to a paper[1] and to another paper by Someone *et al.*[2].

Now look at this cool equation:

$$Rt = KEP = 93.02(\pm 9.62) - 13.45 \tag{1}$$

2.   Functional Requirements

We plan to meet the functional requirements by doing x, y, and z.

3.   Security Requirements

We plan to meet the security requirements by doing x, y, and z.

References

[1] S. Scholes, Discuss. Faraday Soc. No. 50 (1970) 222.

[2] O.V. Mazurin and E.A. Porai-Koshits (eds.),

Acknowledgements