

KIRCHHOFF's LAW

→ KVL → conservation of energy  $\sum V = 0$

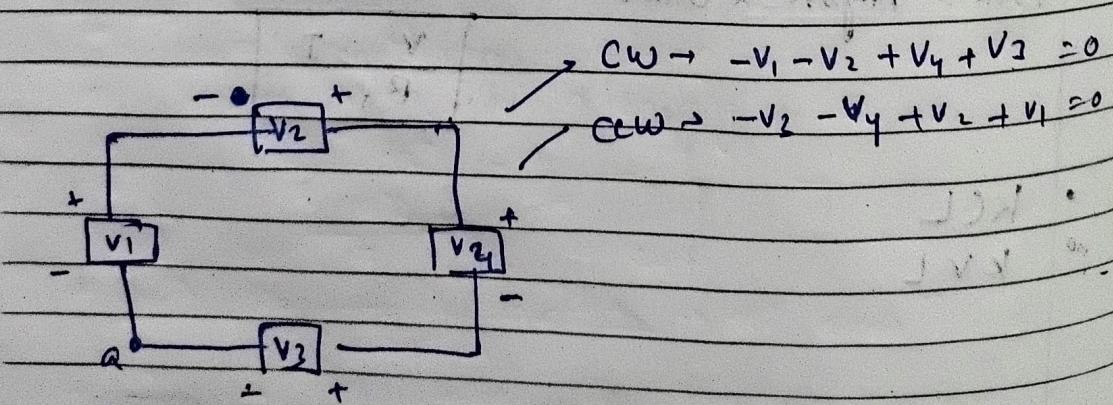
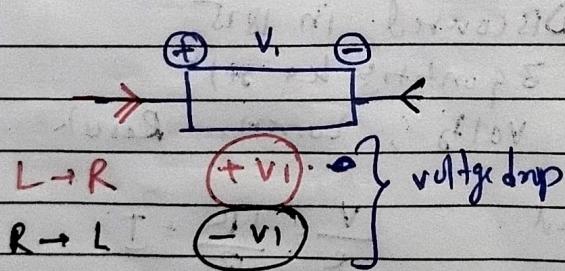
→ KCL → Based on conservation of electric charges  
 $\sum i = 0$   
 $(i_{in} = i_{out})$

Kirchhoff's Voltage Law

- how to handle voltages in an electric circuit
- algebraic sum of the voltages around any closed path = 0
- 3 ways → we can interpret the algebraic sum of voltages in a closed path = 0.

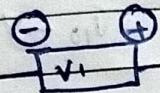
consideration 1: The sum of the voltage drops around a circuit is equal to zero.

We define a voltage drop as +ve if we enter the terminal and leave -ve terminal

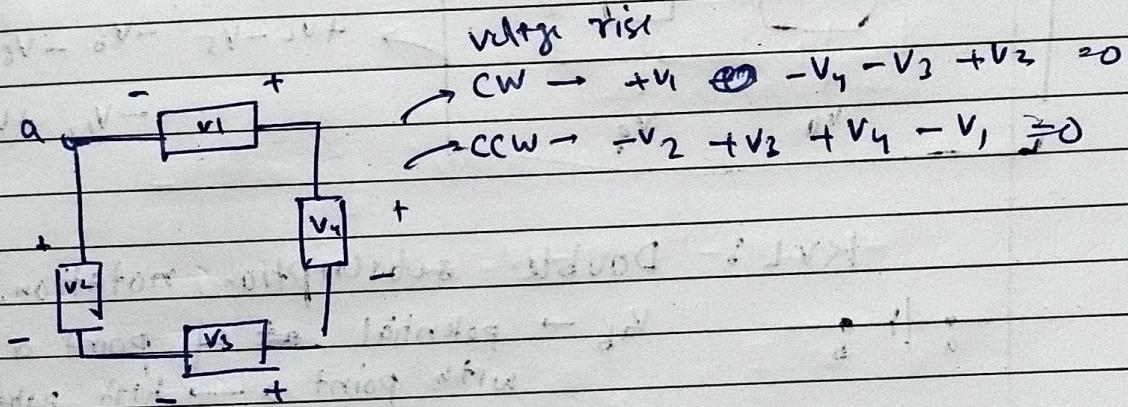


consideration 2: The Sum of Voltages around a circuit = 0.

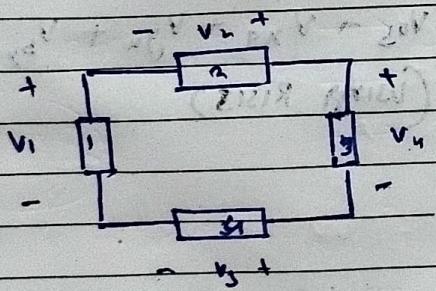
We define a voltage rise



$$\begin{aligned} I \rightarrow R &\Rightarrow +V_1 \\ R \rightarrow I &\Rightarrow -V_1 \end{aligned} \quad \left. \begin{array}{l} \text{Voltage Rise} \\ \hline \end{array} \right\}$$



consideration 3: Sum of voltage rises = sum of voltage drops around the circuit



while crossing 1 & 2  $\rightarrow$  voltage rise (forward)  
while crossing 3 & 4  $\rightarrow$  voltage drop

$$+V_1 + V_2 = +V_4 + V_3$$

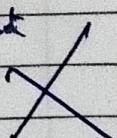
$\rightarrow$  positive voltage drop = negative voltage rise

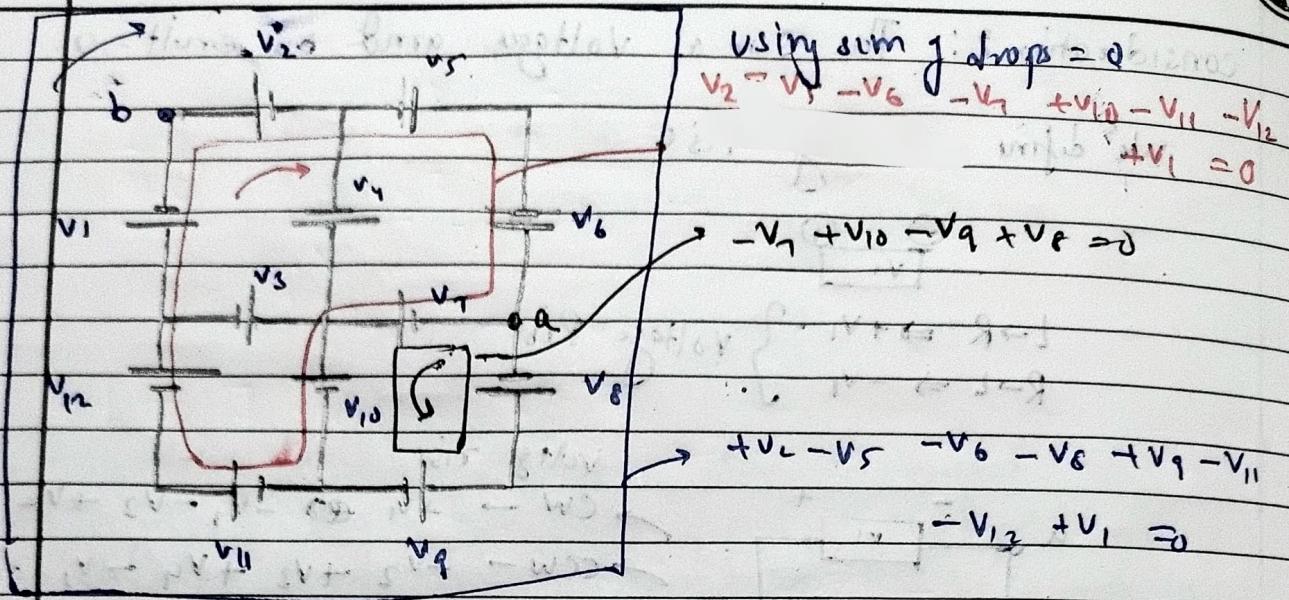
positive voltage rise = negative voltage drop

Similarly in KVL & KCL, it sums --

Never! sum of voltages entr. = 0 at junction point

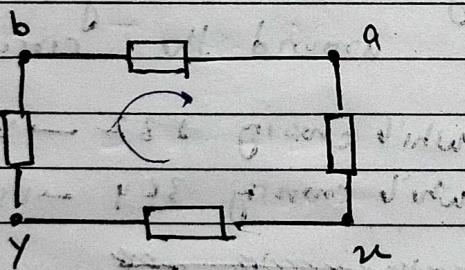
sum of currents ground clnd. port = 0.





KVL: Double subscription notation.

$V_{ab}$  → potential of point a wrt point b  
with point a → high potential (+)  
point b → low potential (-)

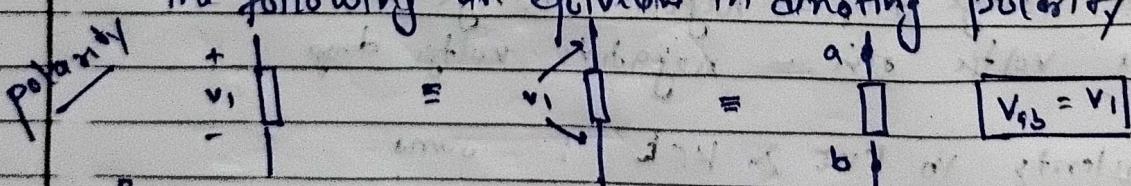


$$\text{CW: } V_{ax} + V_{xa} + V_{yb} + V_{by} = 0$$

(using Rises)

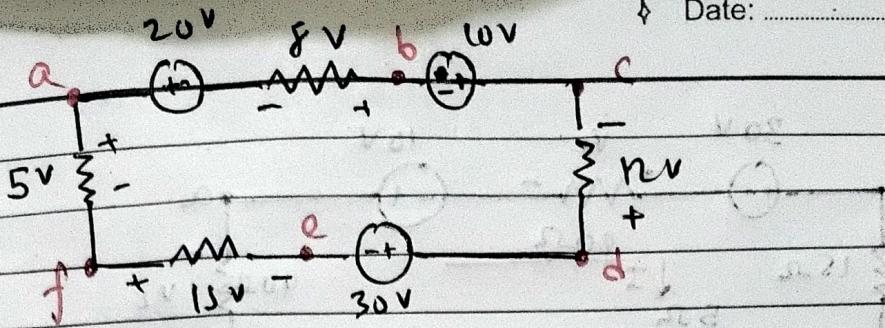
Kirchhoff's Voltage Law: Equivalence in voltage notation

The following are equivalent in denoting polarity:



[Assumed the upper terminal +ve in all cases]

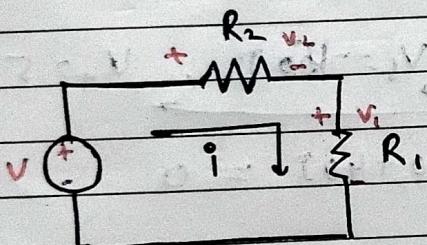
$$+ \underline{\underline{V_2}} - : V_2 = -9 \text{ V.} - R_{TB} \text{ of element} \rightarrow (+ve)$$



using drops = 0 find  $V_{ad}$  &  $V_{dc}$

### Single loop circuits

→ Voltage Divider ~~Precognition Rule~~



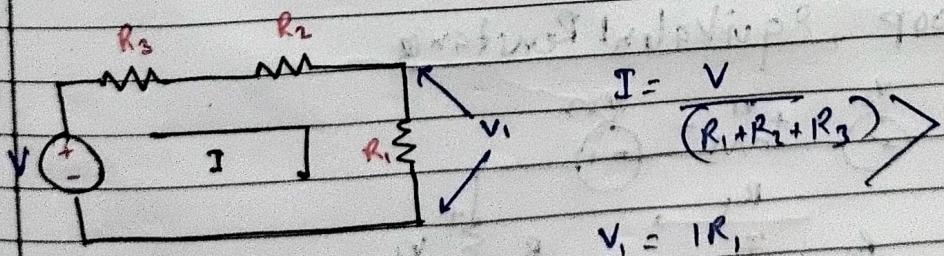
$$V = V_1 + V_2$$

$$V_1 = iR_1, \quad V_2 = iR_2$$

then,  $V = i(R_1 + R_2)$  and

$$i = \frac{V}{(R_1 + R_2)}$$

$$V_1 = \frac{VR_1}{(R_1 + R_2)} *$$



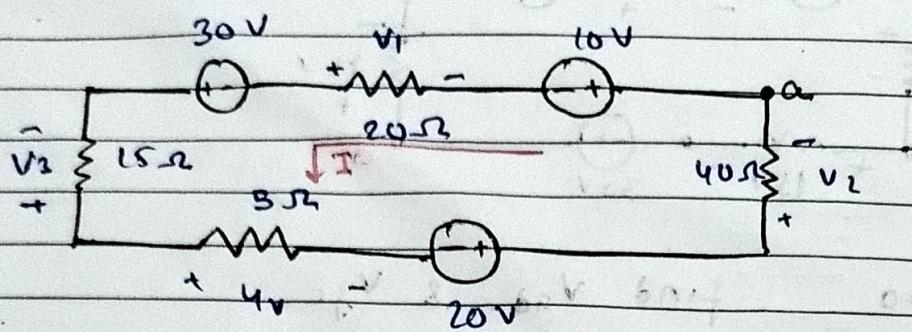
$$I = \frac{V}{(R_1 + R_2 + R_3)}$$

$$V_1 = \frac{VR_1}{(R_1 + R_2 + R_3)}$$

Eg.  $R_1 = 4\Omega$        $V = 50V$   
 $R_2 = 11\Omega$        $P_1 = 16W$   
 $R_3 = ?$        $I^2 R_1 = 16 \Rightarrow I^2 = 16/4 = 4 \Rightarrow I = 2A$

$$V = I(R_1 + R_2 + R_3)$$

$$R_1 + R_2 + R_3 = 25 \Rightarrow R_3 = 25 - 15 = 10\Omega$$



Find  $I$ ,  $V_1$ ,  $V_2$ ,  $V_3$ ,  $V_4$  and Power supplied by  $10V$  source

start from pt. A, sum of voltage drops = 0 in dir<sup>n</sup> g currnt.  $I$

$$+10 - V_1 - 30 - V_3 + V_4 - 20 + V_2 = 0$$

$$V_1 = -20I, \quad V_2 = 40I, \quad V_3 = -150I, \quad V_4 = 5I$$

$$10 + 20I - 30 + (5I + 8I) - 20 + 40I = 0$$

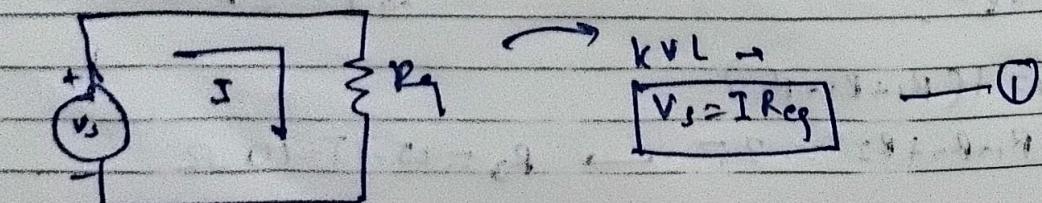
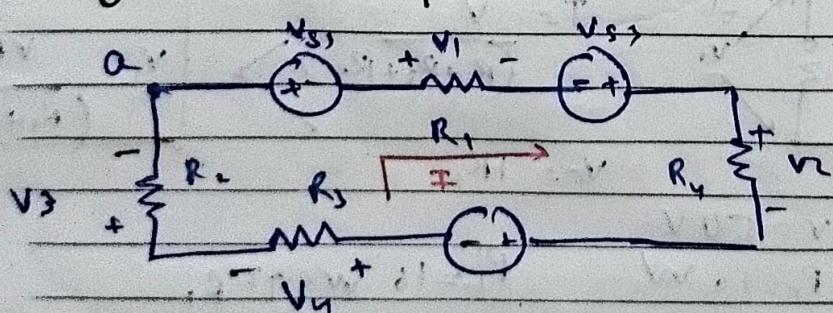
$$I = 0.5 \text{ A}$$

$$V_1 = -10V \quad V_3 = -7.5V$$

$$V_2 = 20V \quad V_4 = 2.5V$$

$$P_{10V(\text{supplied})} = -10I = -5W$$

### Single Loop, Equivalent Resistance



apply KVL start from 'a' using drops = 0 (cw)

$$V_{S_1} + V_1 - V_{S_3} + V_2 + V_{S_2} + V_4 + V_3 = 0$$

OR

$$-V_{S_1} - V_{S_2} + V_{S_3} = I(R_1 + R_2 + R_3 + R_4) \quad \rightarrow \textcircled{11}$$

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$$V_s = V_{S_1} - V_{S_2} + V_{S_3}$$

$$R_q = R_1 + R_2 + R_3 + R_4$$

Kirchoff's Current Law

At any junction (node) ; total current flowing in = total current flowing out

→ ~~net~~ charge can't pile up or disperse at 'pt. -

$$\sum i_{in} - \sum i_{out} = 0.$$

$$\sum i_{in} = \sum i_{out}$$