

Console

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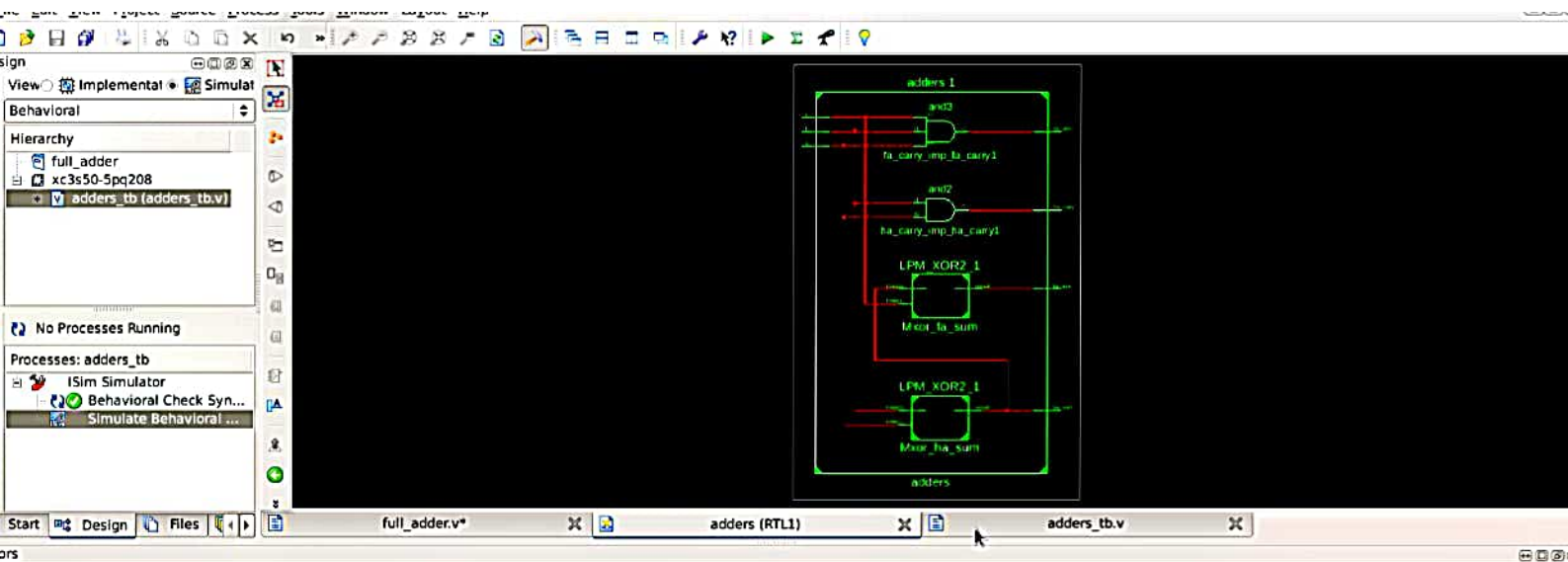
This is a Full version of ISim.

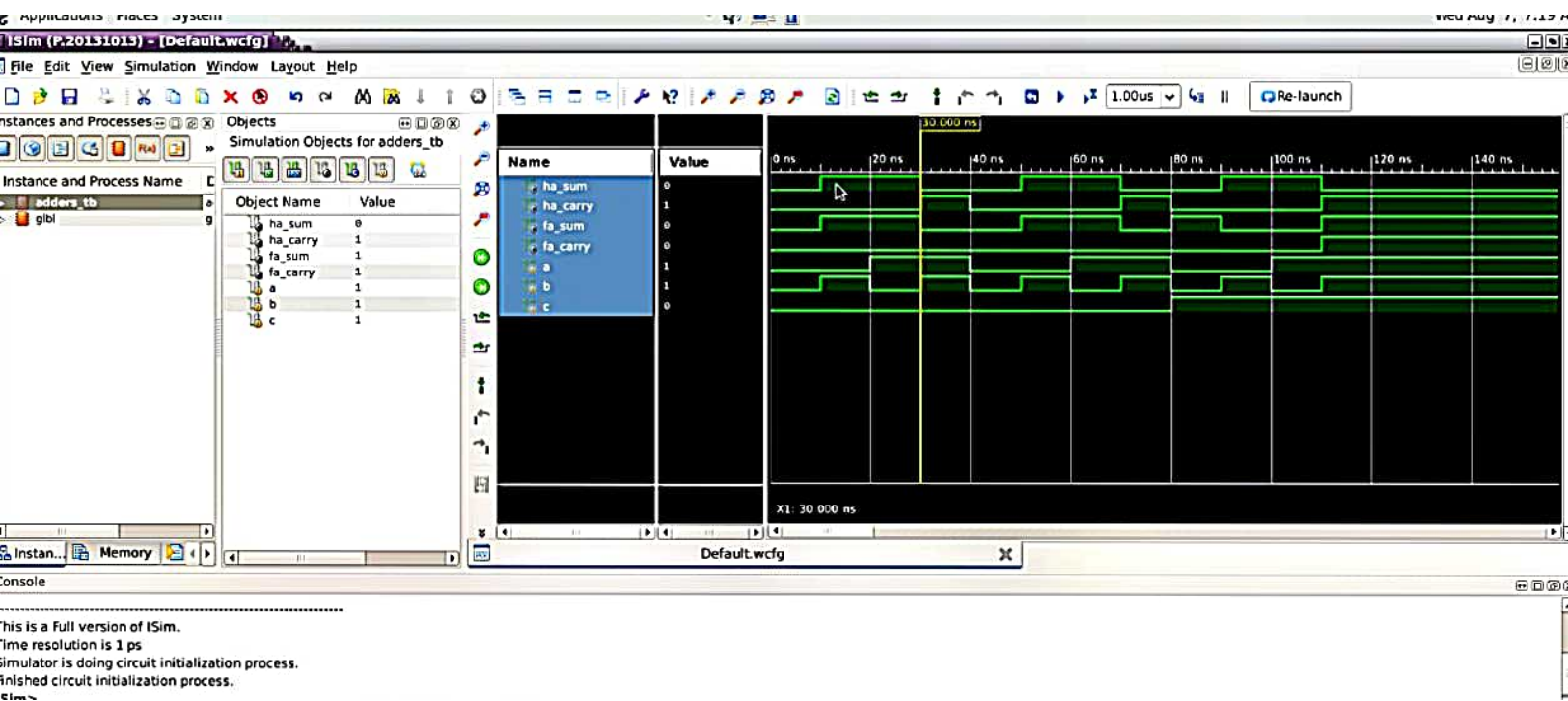
Time resolution is 1 ps

simulator is doing circuit initialization process.

Finished circuit initialization process.

Time ~





ISE Project Navigator (V2015.10.1) - 7/10/mux/mux_4_1/mux_4_1_tb (mux_4_1 (RTL))

File Edit View Project Source Process Tools Window Layout Help

Design View Implement Simulat

Behavioral

Hierarchy

- mux4_1
- xc3s50-5pq208
- mux_4_1_tb (mux4_1.tb.v)

No Processes Running

Processes: mux_4_1_tb

- ISim Simulator
- Behavioral Check Syn...
- Simulate Behavioral ...

Start Design Files

mux4_1.v Design Summary (Synthesized) mux_4_1 (RTL1) mux4_1_tb.v

View by Category

Design Objects of Top Level Block

Instances	Pins	Signals
mux_4_1	mux_4_1	mux_4_1
m1	i0	i0

Properties of Instance: mux_4_1

Name	Value
Type	mux_4_1:1
Part	xc3s50-5-pq208

