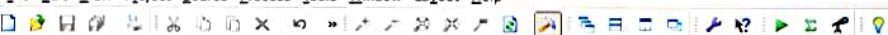


ISE Project Navigator (P.20131013) - /home/ise/fsm/fsm.xise - [fsm (RTL1)]

File Edit View Project Source Process Tools Window Layout Help



design View Implementat Simulat

Behavioral

Hierarchy

fsm

xc3s50-5pq208

fsm\_tb (fsm\_tb.v)

No Processes Running

Processes: fsm\_tb

ISim Simulator

Behavioral Check Syn...

Simulate Behavioral ...

Start Design Files

fsm.v

Design Summary (out of date)

fsm (RTL1)

fsm\_tb.v

Design Objects of Top Level Block

Instances

fsm

count1

Pins

fsm

Signals

fsm

Name

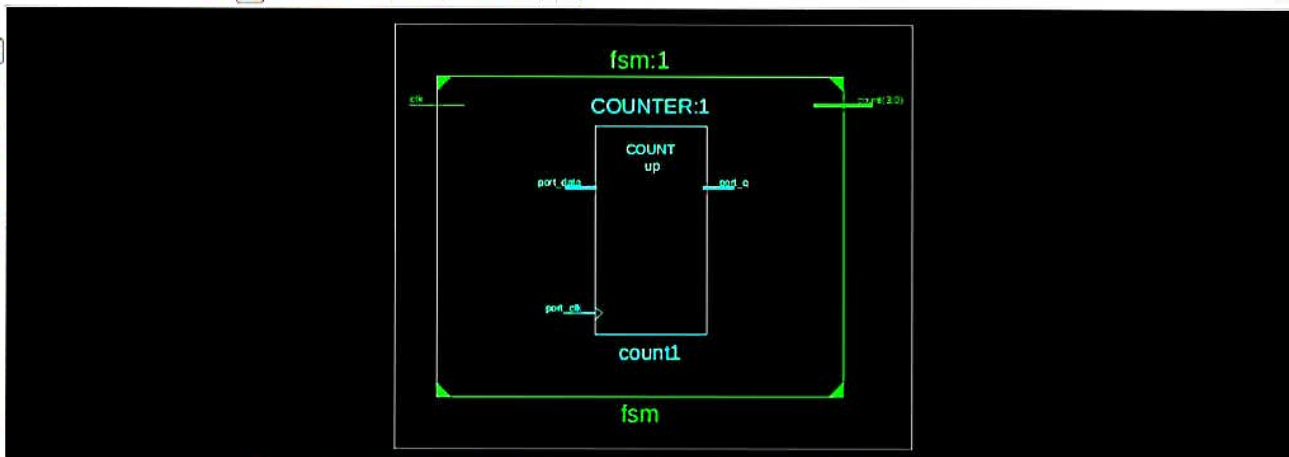
Type

Module

Value

COUNTER:1

Properties of Instance: count1



fsm.v Design Summary (out of date) fsm (RTL1) fsm\_tb.v

Design Objects of Top Level Block

Instances

fsm

count1

Pins

fsm

Signals

fsm

Name

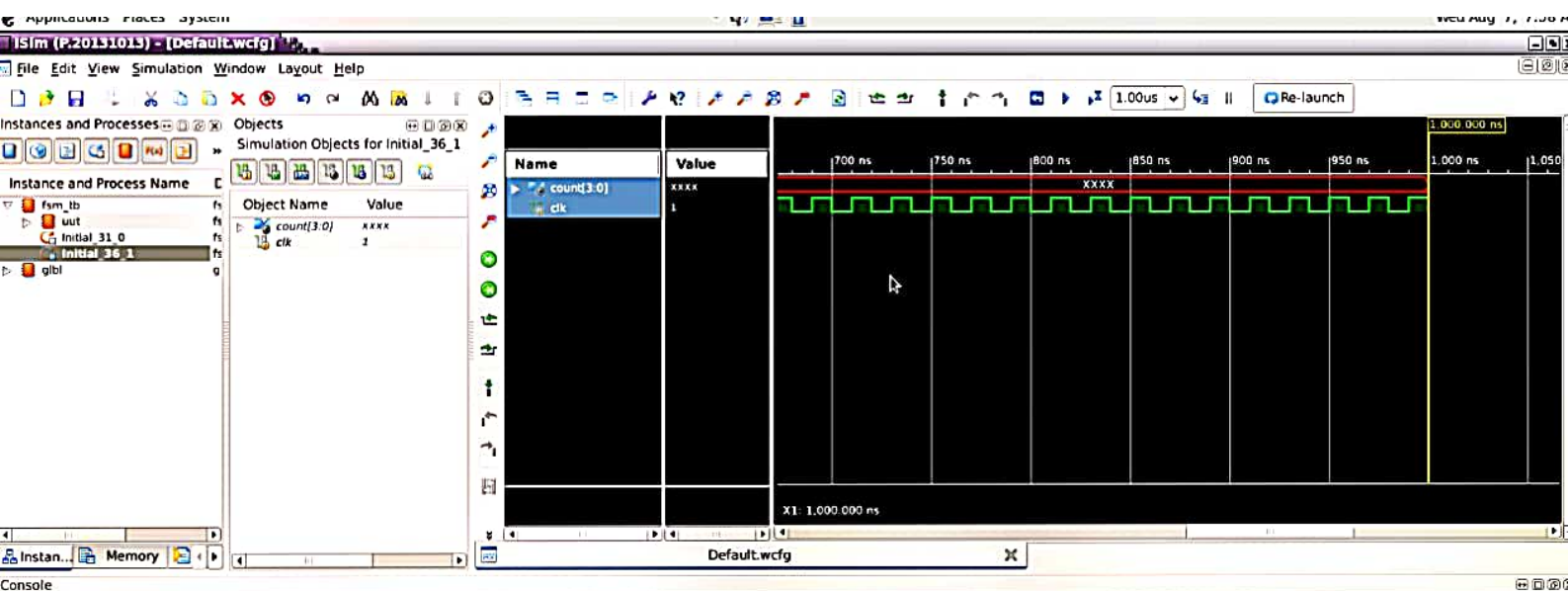
Type

Module

Value

COUNTER:1

Properties of Instance: count1



Console

This is a Full version of ISim.  
Time resolution is 1 ps  
Simulator is doing circuit initialization process.  
Finished circuit initialization process.  
Stopped at time : 1 us : File "fsm\_tb.v" Line 37  
ISim>