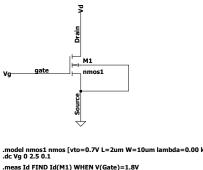
SHINOY JAYAN K D 26-08-2024

Analog_VLSI LAB_1 Task: 2 small signal analysis - Vds constant Vgs varing



.model nmos1 nmos [vto=0.7V L=2um W=10um lambda=0.00 kp=50u gamma=0.0] .dc Vg 0 2.5 0.1

.meas gm FIND d(Id(M1)) WHEN V(Gate)=1.8V .meas Ron FIND 1/d(Id(M1)) WHEN V(Gate)=1.8V

.meas Id_1 FIND Id(M1) WHEN V(Gate)=2V .meas gm_1 FIND d(Id(M1)) WHEN V(Gate)=2V .meas Ron_1 FIND 1/d(Id(M1)) WHEN V(Gate)=2V

.meas Id_2 FIND Id(M1) WHEN V(Gate)=2.2V .meas gm_2 FIND d(Id(M1)) WHEN V(Gate)=2.2V .meas Ron_2 FIND 1/d(Id(M1)) WHEN V(Gate)=2.2V

By Calculation

Vgs = 1.8V

id: id(m1)=151.25u at 1.8 gm: d(id(m1))=275u at 1.8 ron: 1/d(id(m1))=3633 at 1.8

Vgs = 2.0V

id_1: id(m1)=211.25u at 2 gm_1: d(id(m1))=325u at 2 ron_1: 1/d(id(m1))=3076 at 2

Vgs = 2.2V

id_2: id(m1)=281.25u at 2.2 gm_2: d(id(m1))=375u at 2.2 ron_2: 1/d(id(m1))=2666 at 2.2

By Simulation

Vgs = 1.8V

id: id(m1)=0.00015125 at 1.8 gm: d(id(m1))=0.0002875 at 1.8 ron: 1/d(id(m1))=3478.26 at 1.8

Vgs = 2.0V

id_1: id(m1)=0.00021125 at 2 gm_1: d(id(m1))=0.000325 at 2 ron_1: 1/d(id(m1))=3076.92 at 2

Vgs = 2.2V

id_2: id(m1)=0.00028125 at 2.2 gm_2: d(id(m1))=0.0003625 at 2.2 ron_2: 1/d(id(m1))=2758.62 at 2.2

