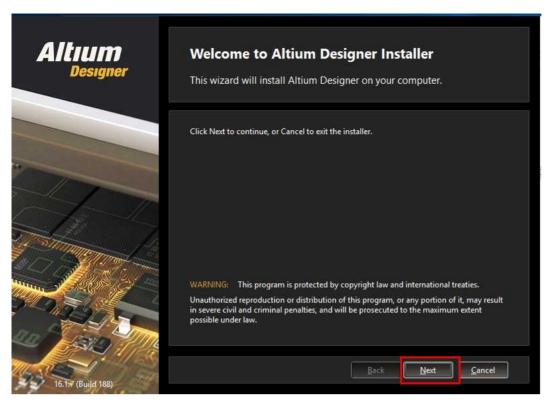
Xilinx Zynq FPGA, TI DSP, MCU 기반의 회로 설계 및 임베디드 전문가 과정

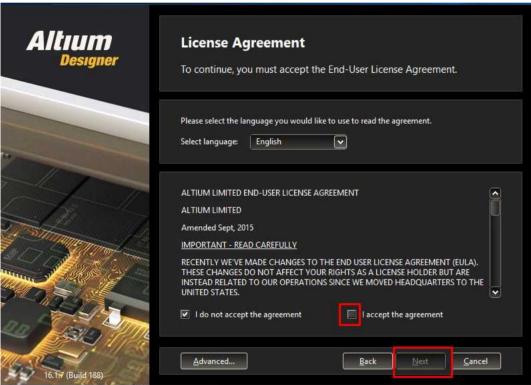
1.압축을 해제한다.

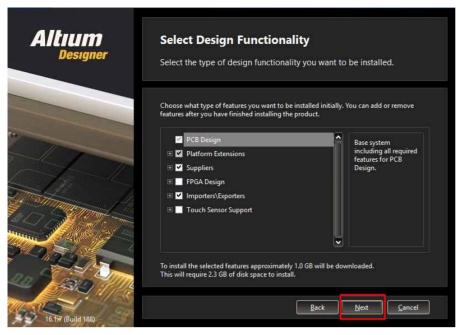


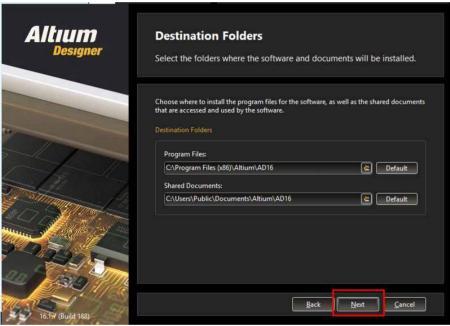
2.AltiumDesignerSetup_16_1_7을 실행한다.

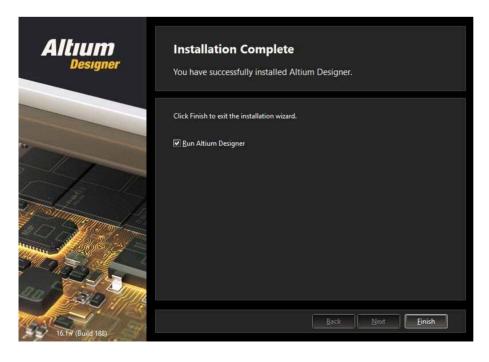


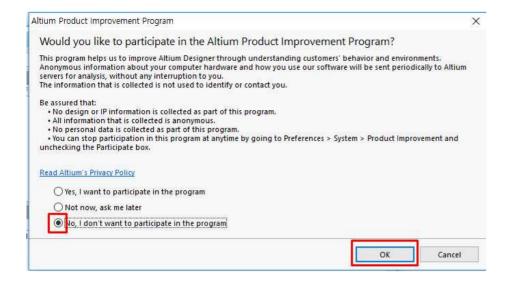




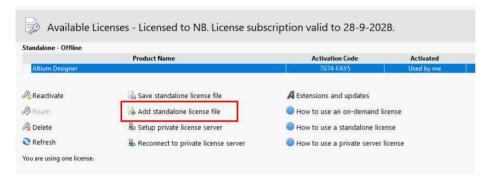




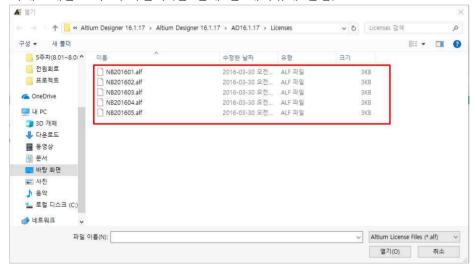




3.라이센스를 추가한다.



아래 5개를 모두 추가한다.(한 번에 한 개씩밖에 안됨)

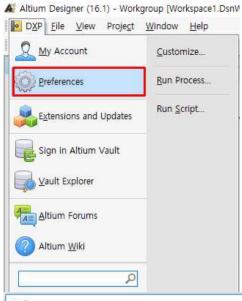


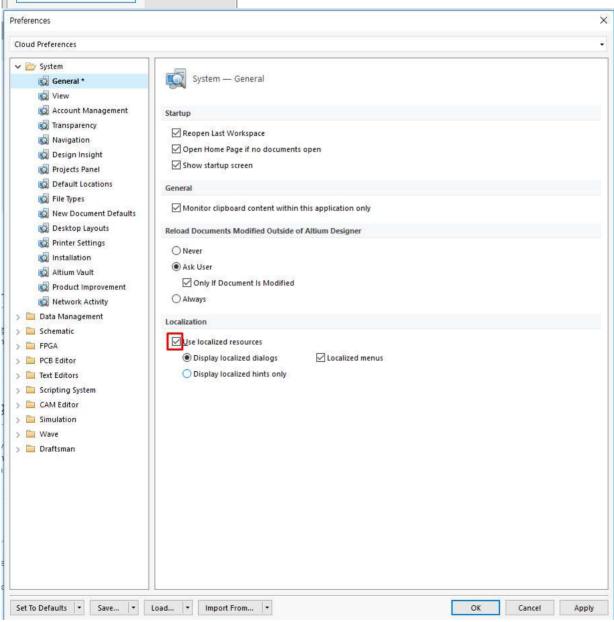
아래와 같이 라이센스가 활성화 되는 것을 볼 수 있다.



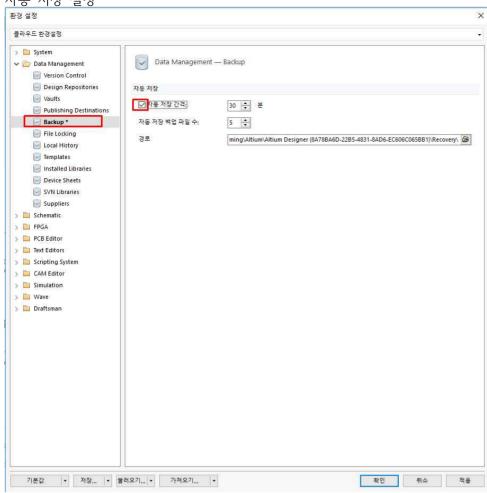
환경설정

1.메뉴창 한글로 보이기





자동 저장 설정



파일형식

