

# PSIM PID STUDY

박현우

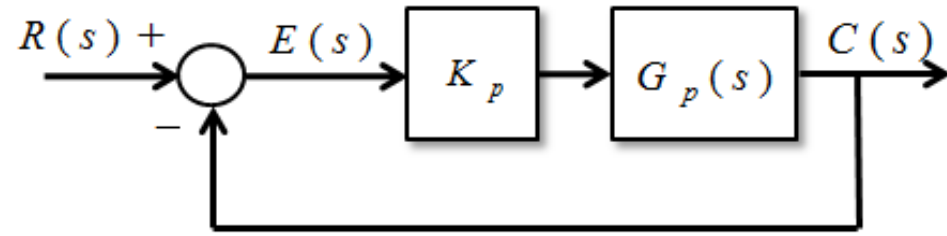
phw820@naver.com

# 목차

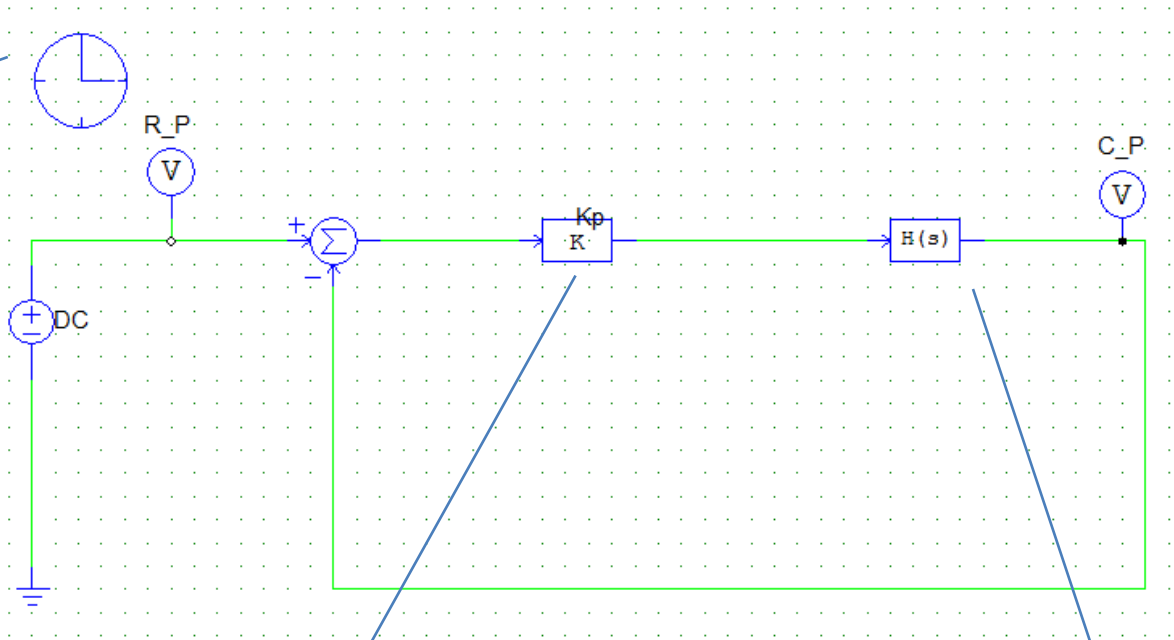
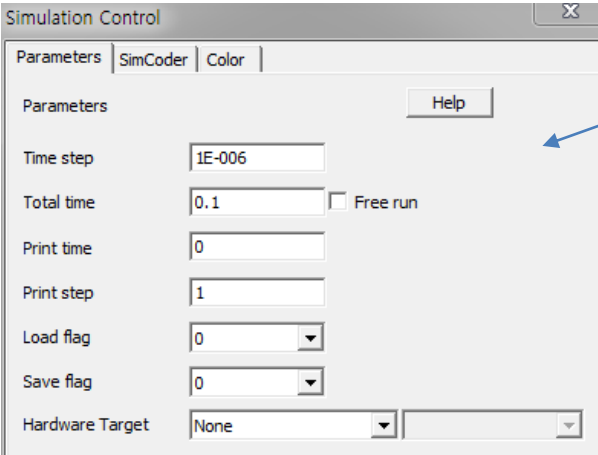
## PSIM PID 제어

- 1) P 제어 이론
- 2) P 회로 설계
- 3) P 게인(Gain)에 따른 제어 변화
- 4) I 제어 이론
- 5) I 회로 설계
- 6) I 제어에 의한 정상상태 오차 변화
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- 9) PI 제어에 따른 응답속도와 정상상태 오차 변화
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- 13) I 제어시 반드시 필요한 Anti windup

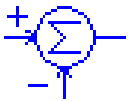
## 1) P 제어 이론



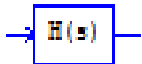
## 2) P 회로 설계



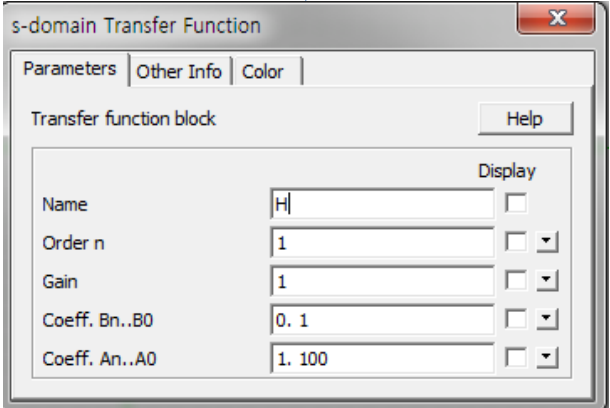
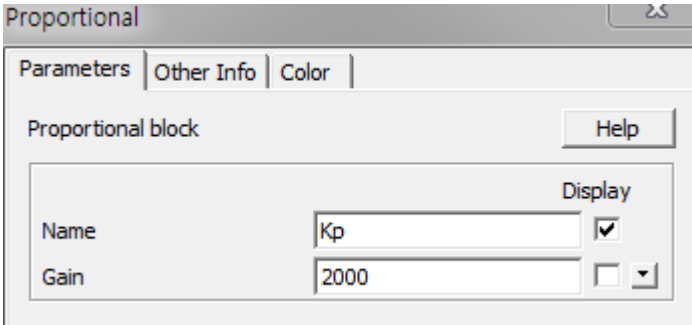
Proportional



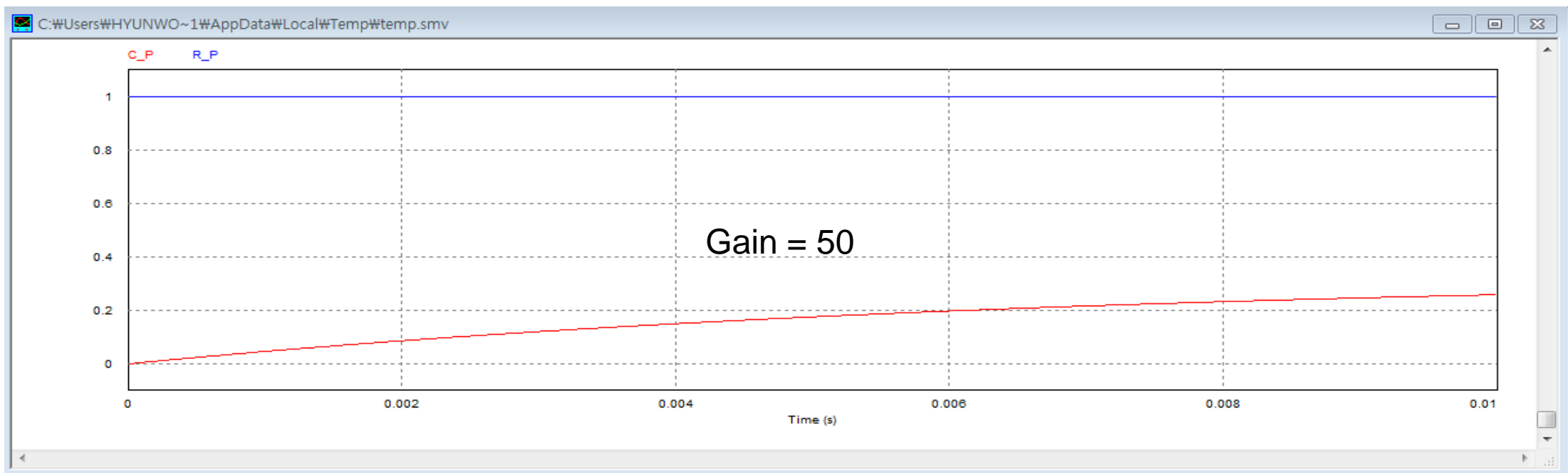
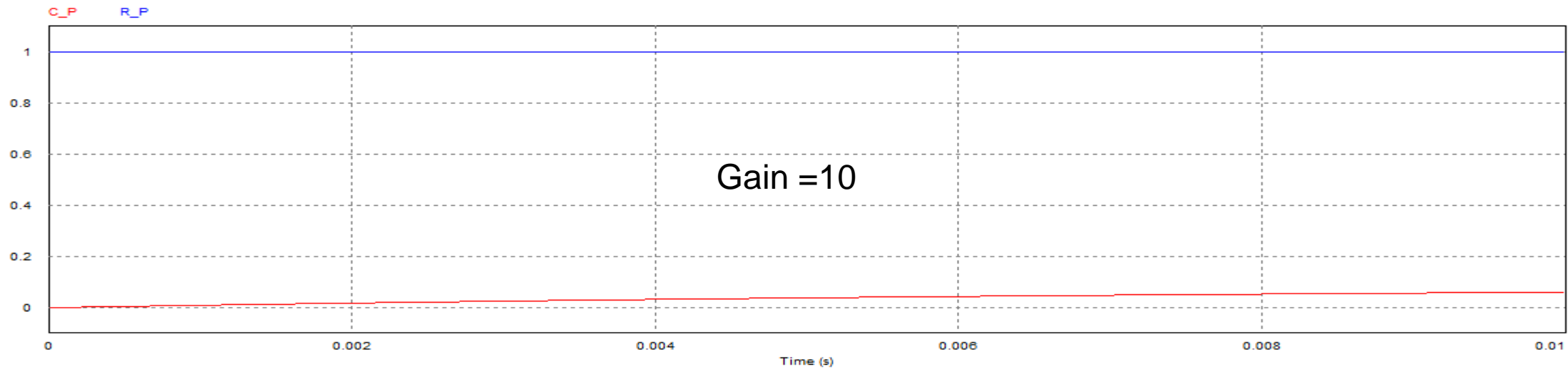
Summer (+/-)



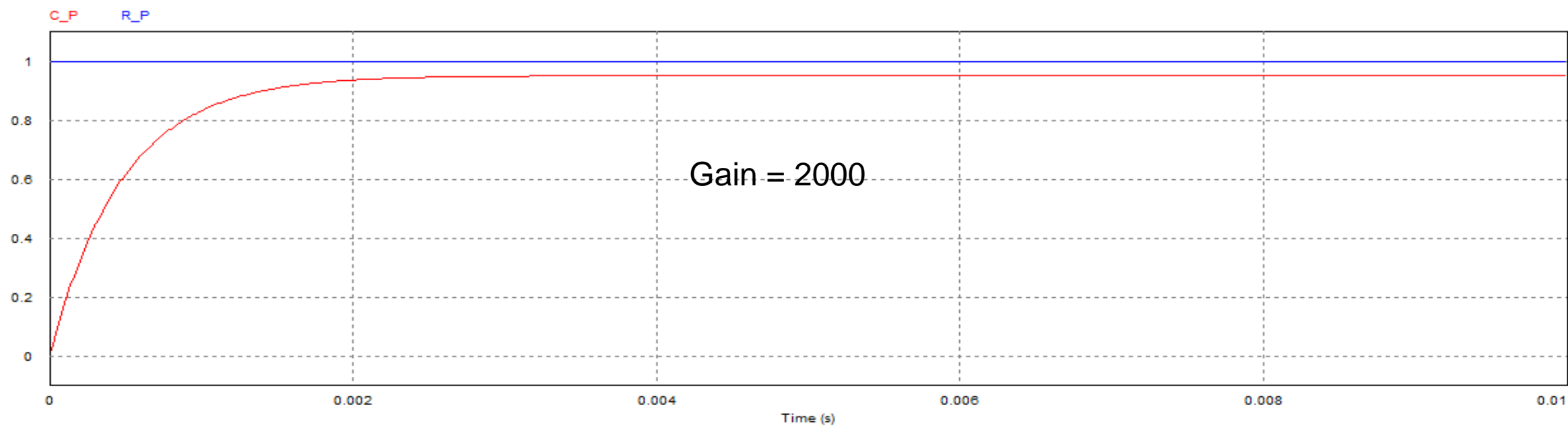
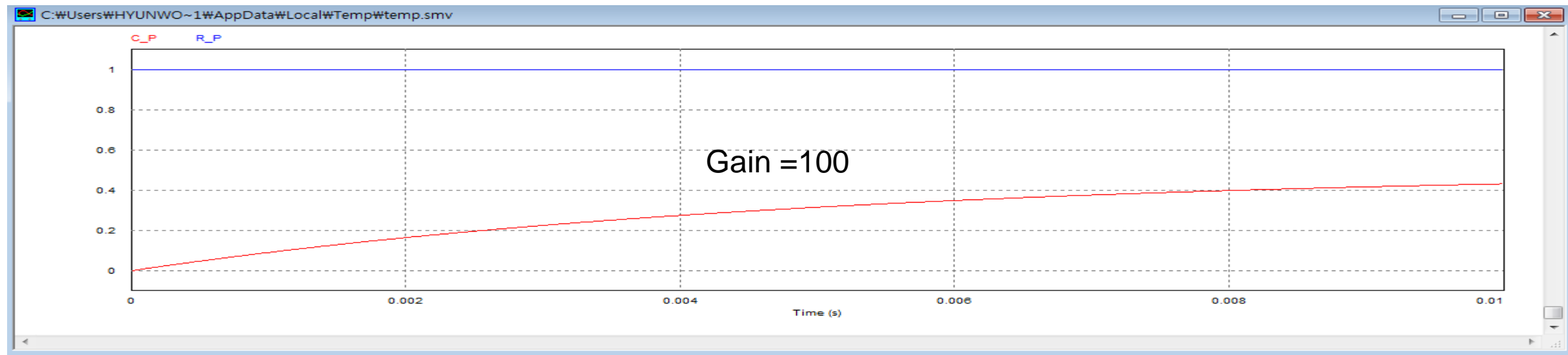
s-domain Transfer Function



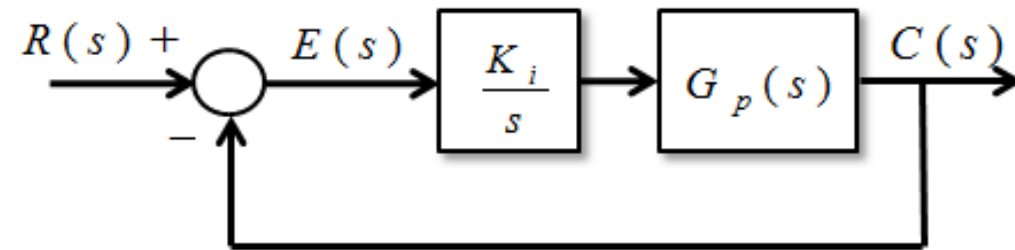
### 3) P 게인(Gain)에 따른 제어 변화



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#### 4) I 제어 이론



# 5) I 회로 설계

Simulation Control

Parameters | SimCoder | Color |

Parameters

Time step: 1E-005

Total time: 10 ☐ Free run

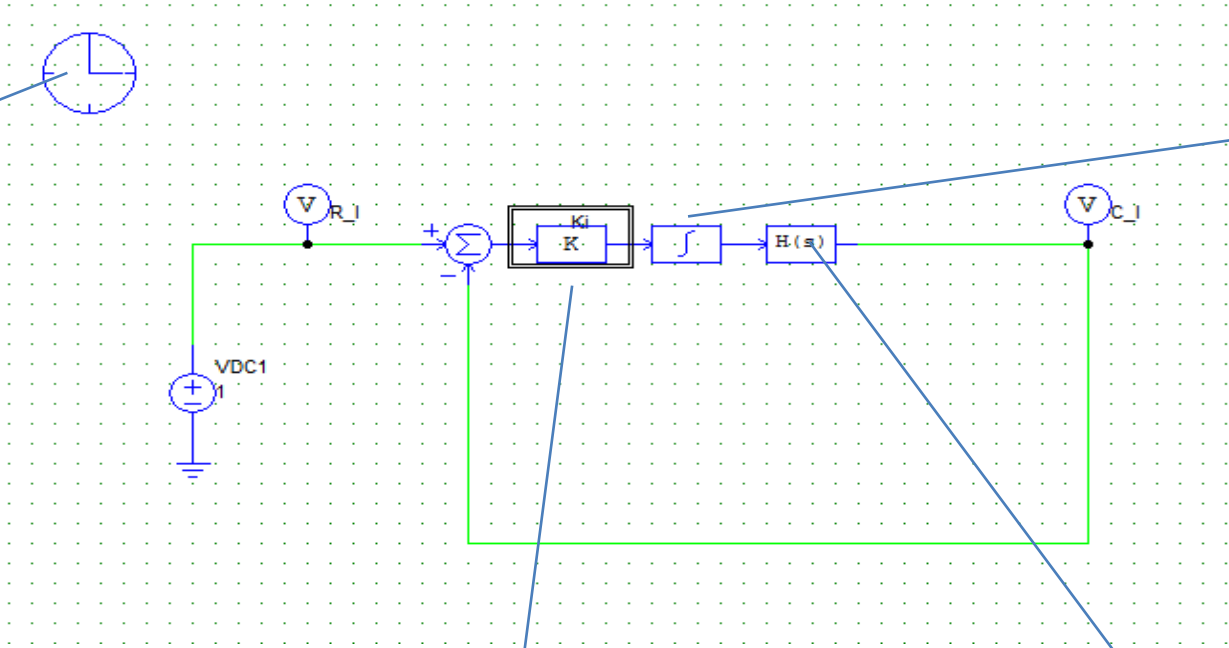
Print time: 0

Print step: 1

Load flag: 0

Save flag: 0

Hardware Target: None



Integrator

Parameters | Other Info | Color |

Integral block

Name: Integ

Time Constant: 1

Initial Output Value: 0

Integrator

Proportional

Summer (+/-)

s-domain Transfer Function

Proportional

Parameters | Other Info | Color |

Proportional block

Name: Ki

Gain: 0.5

s-domain Transfer Function

Parameters | Other Info | Color |

Transfer function block

Name: Hs

Order n: 1

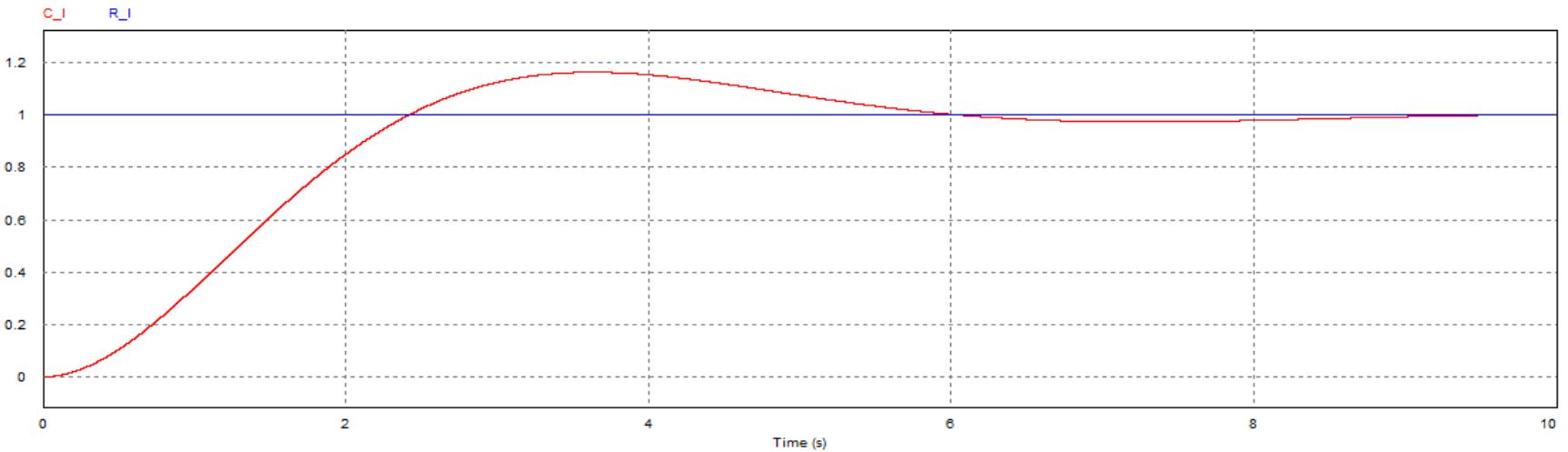
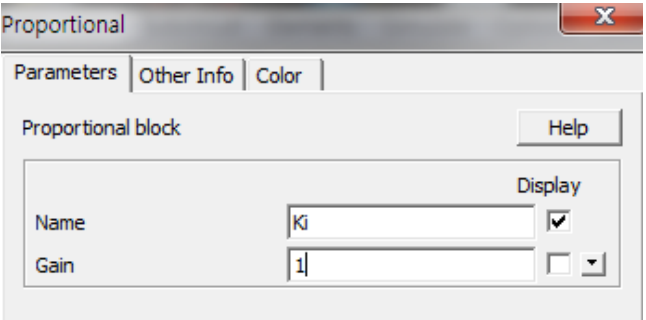
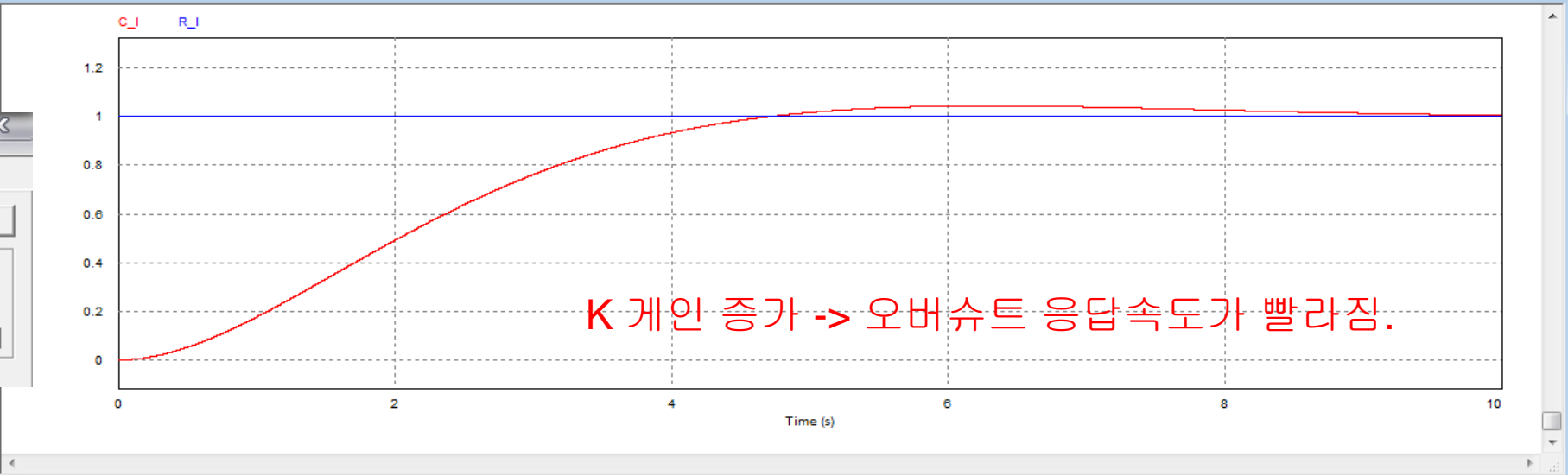
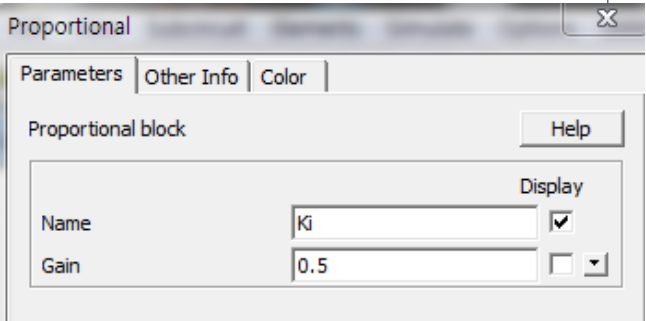
Gain: 1

Coeff. Bn..B0: 0. 1.

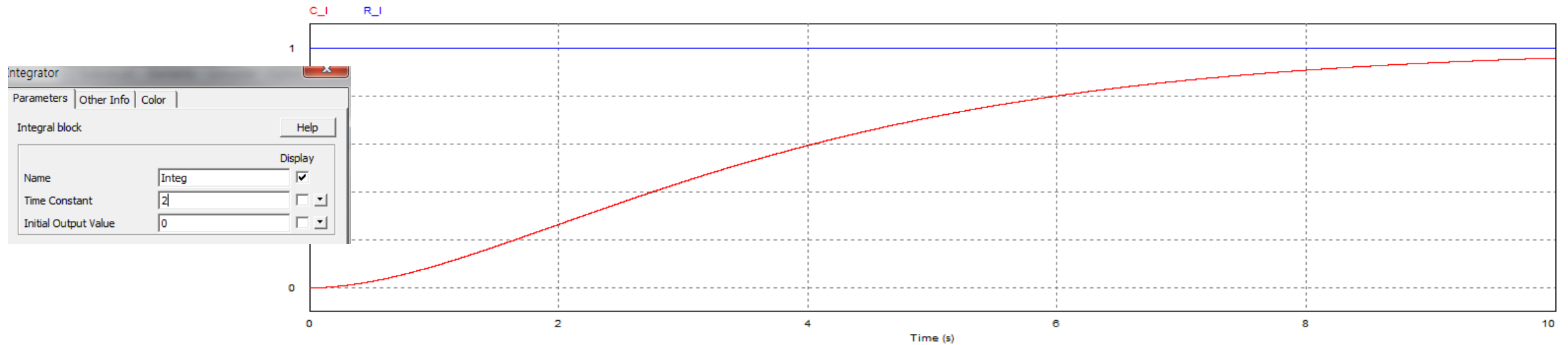
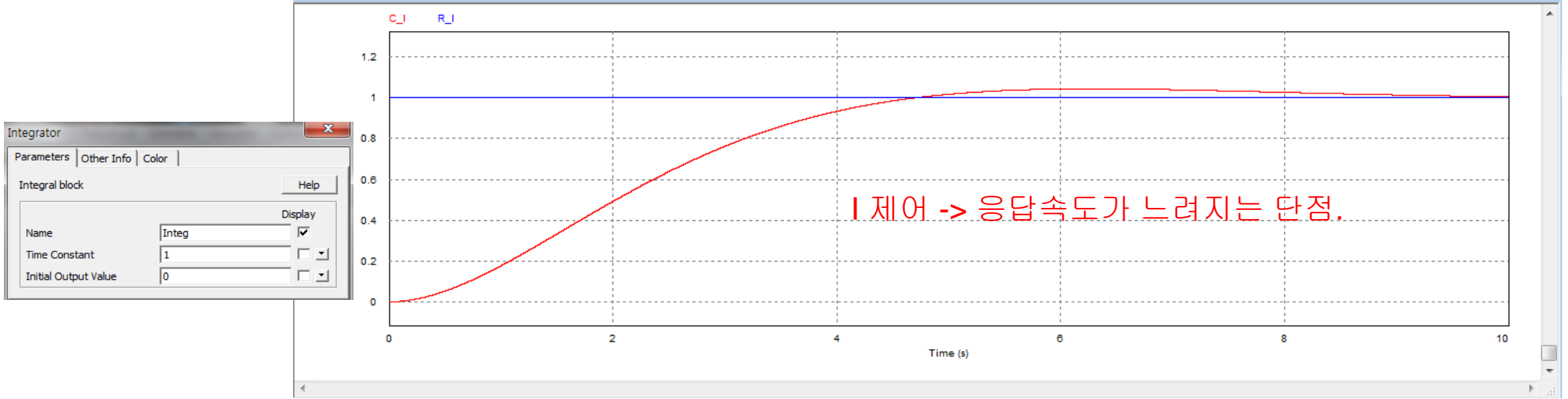
Coeff. An..A0: 1. 1.



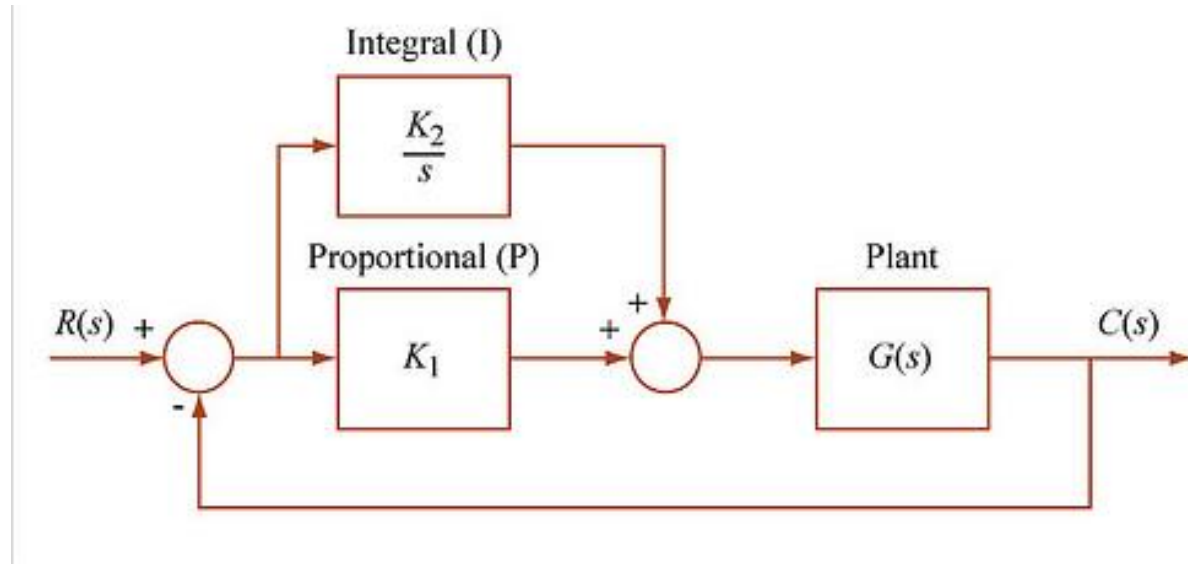
# 6) I 제어에 따른 정상상태 오차변화



## 6) I 제어에 따른 정상상태 오차변화



## 7) PI 제어 특징



# 8) PI 회로 설계

Simulation Control

Parameters | SimCoder | Color |

Parameters

Time step1E-005

Total time10

Print time0

Print step1

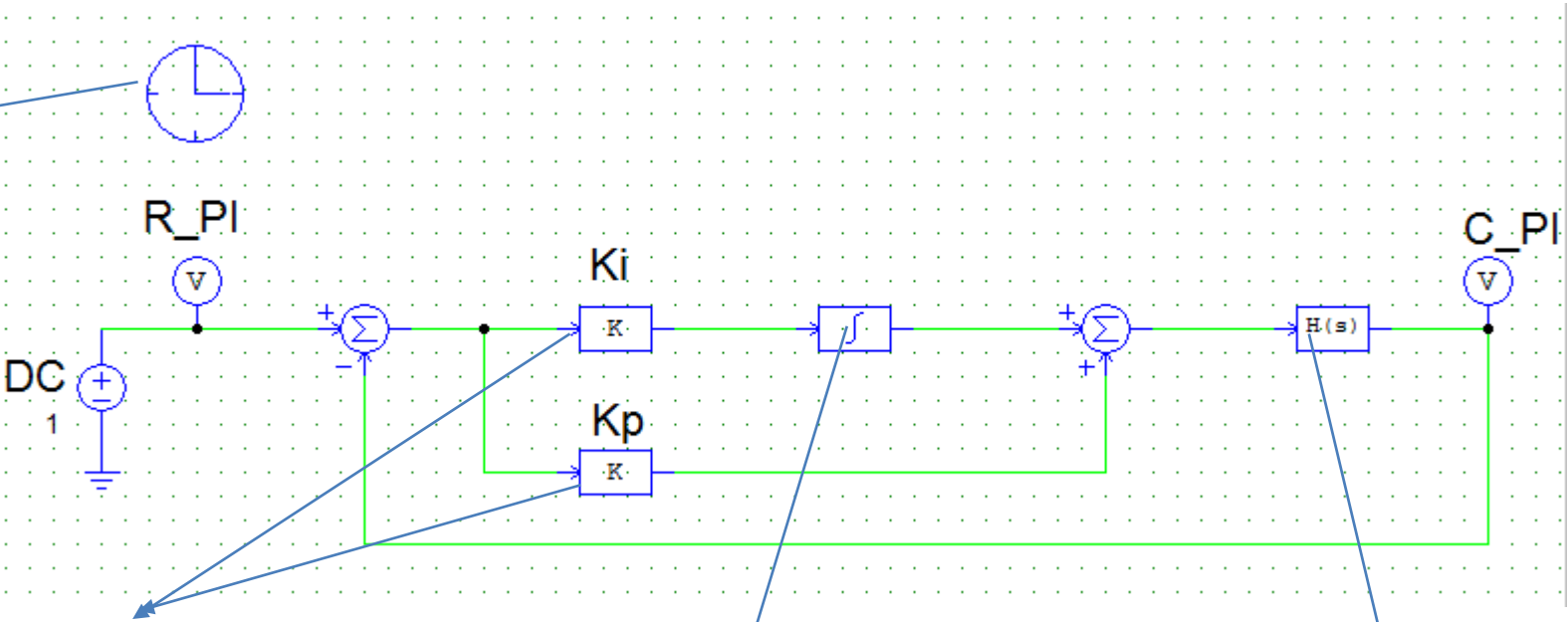
Load flag0

Save flag0

Hardware TargetNone

Free run

Help



Summer (+/+)

Integrator

Proportional

Summer (+/-)

s-domain Transfer Function

Proportional

Parameters | Other Info | Color |

Proportional block

Help

NameKi

Gain1

Display

Integrator

Parameters | Other Info | Color |

Integral block

Help

NameB1

Time Constant1

Initial Output Value0

Display

s-domain Transfer Function

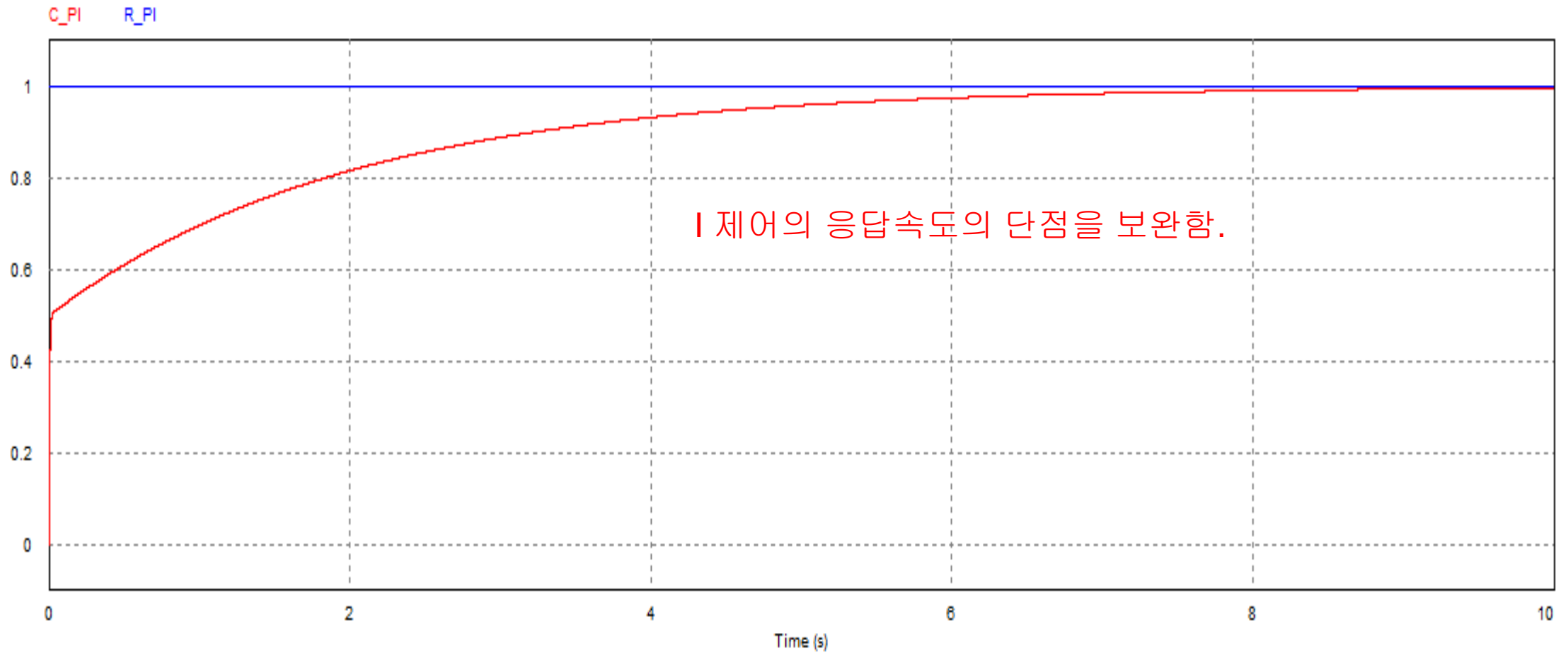
Parameters | Other Info | Color |

Transfer function block

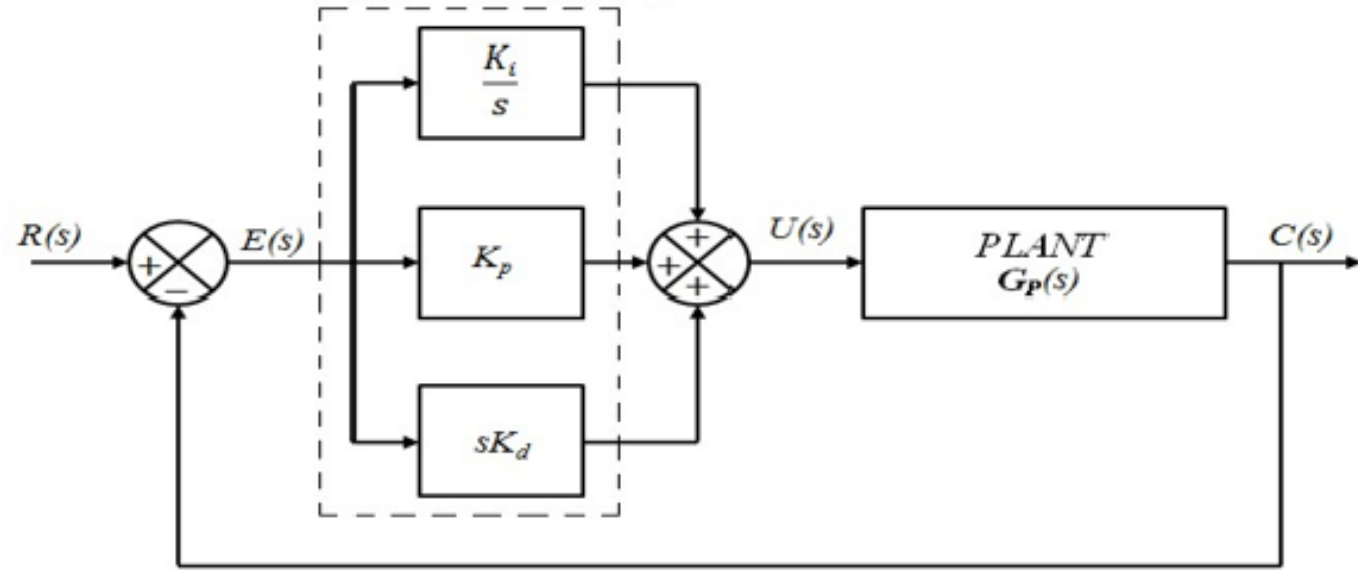
Help

Name	Display
TFCN1	
Order n	1
Gain	1
Coeff. Bn..B0	0. 1.
Coeff. An..A0	0.01 1.

## 9) PI 제어에 따른 반응속도와 정상상태 오차 변화



## 10) PID 제어 특징



# 11) PID 회로 설계

Simulation Control

Parameters | SimCoder | Color |

Parameters

Time step1E-006

Total time1☐ Free run

Print time0

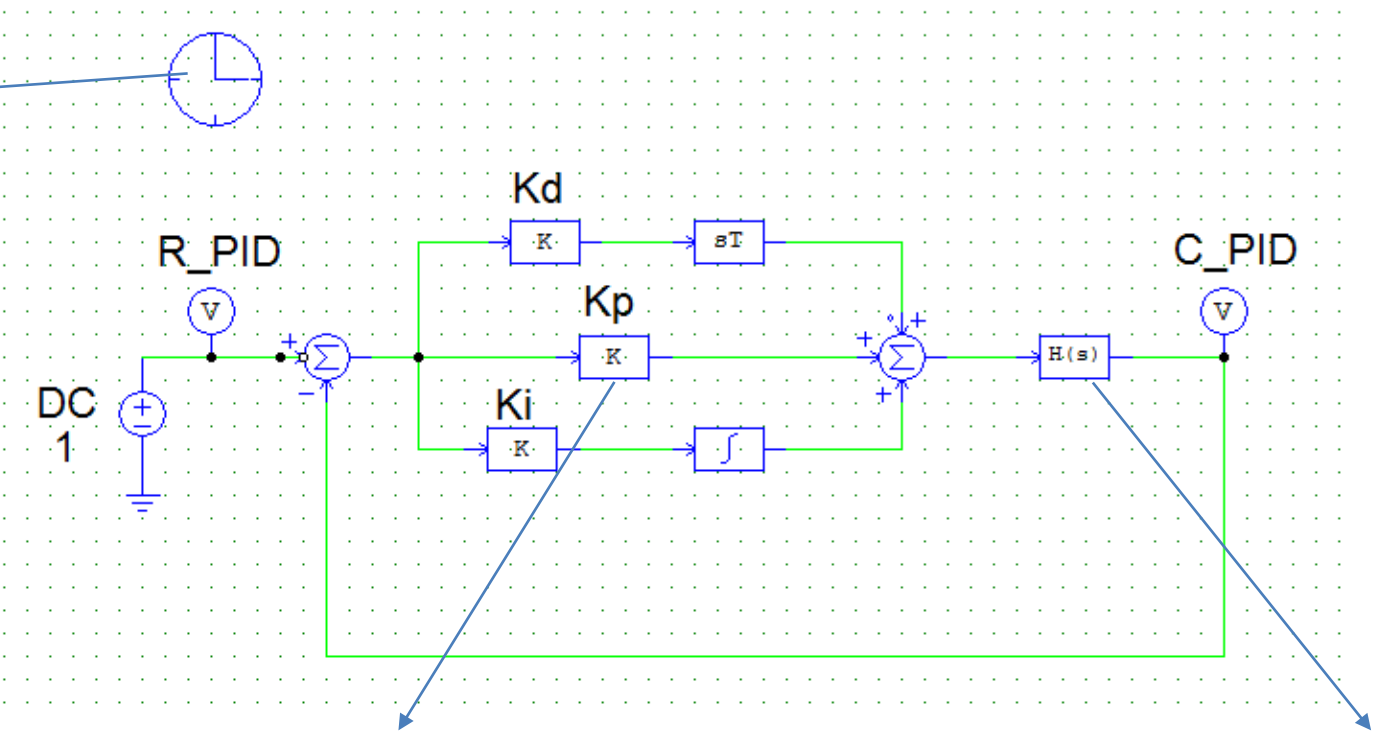
Print step1

Load flag0

Save flag0

Hardware TargetNone

Help



Proportional

Parameters | Other Info | Color |

Proportional block

Help

NameKp

Gain200

Display

Display

s-domain Transfer Function

Parameters | Other Info | Color |

Transfer function block

Help

NameTFCN1

Order n2

Gain1

Coeff. Bn..B00. 0. 50.

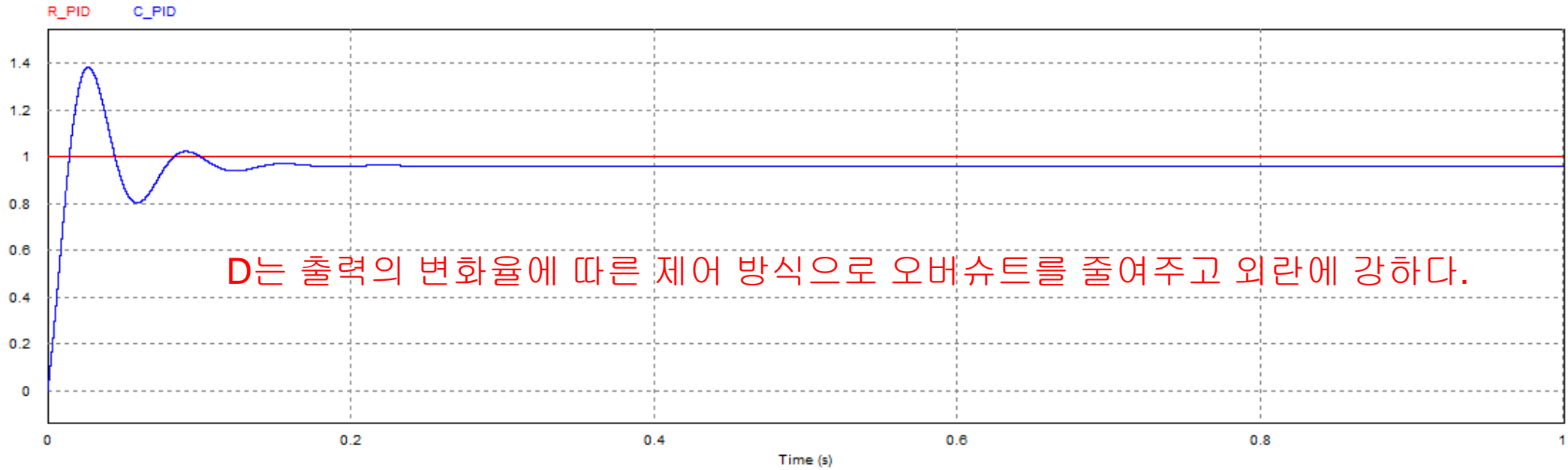
Coeff. An..A01. 10. 400

Display

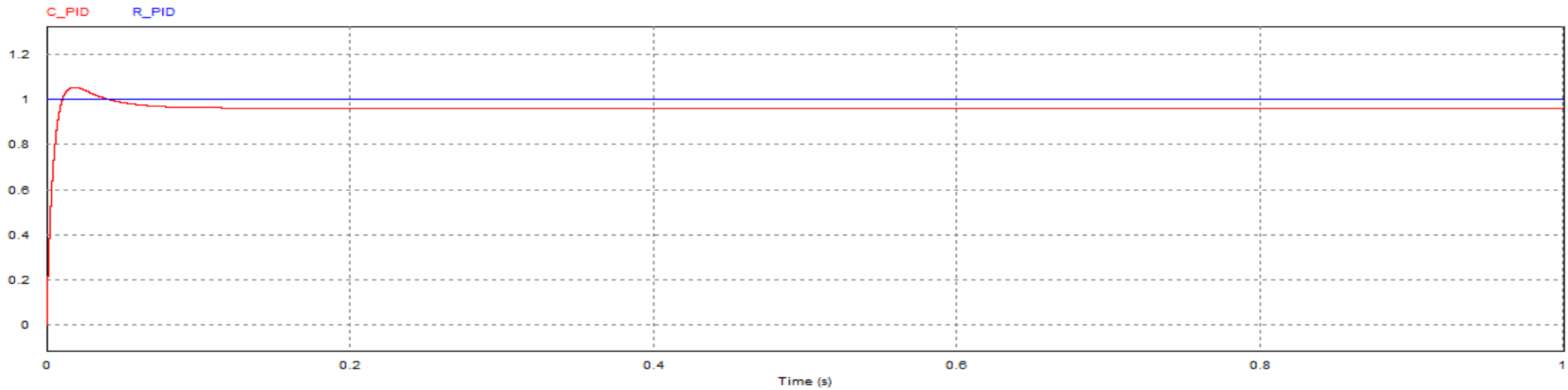
s-domain Transfer Function

## 12) PID 제어에 따른 오버슈트, 반응속도와 정상상태 오차 변화

Name	Kd
Gain	1

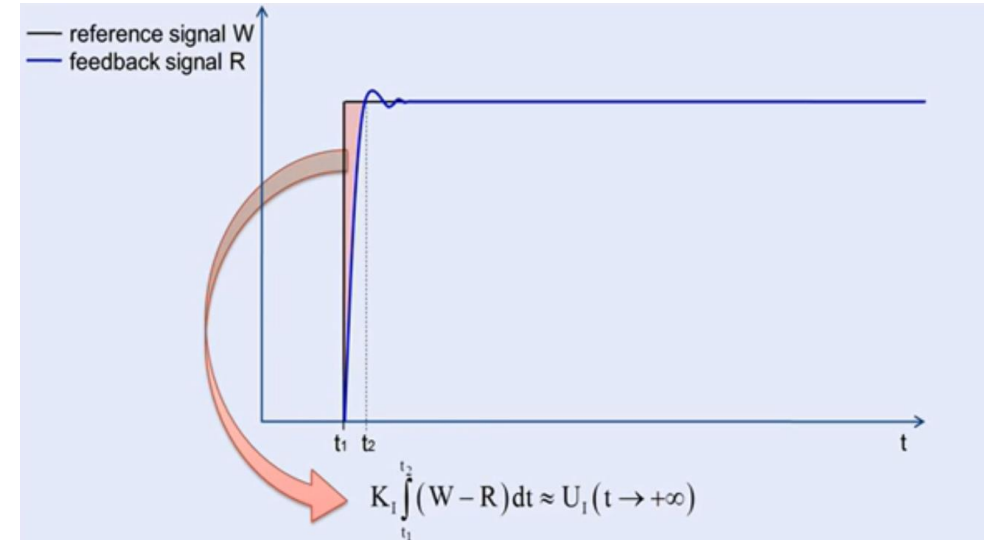
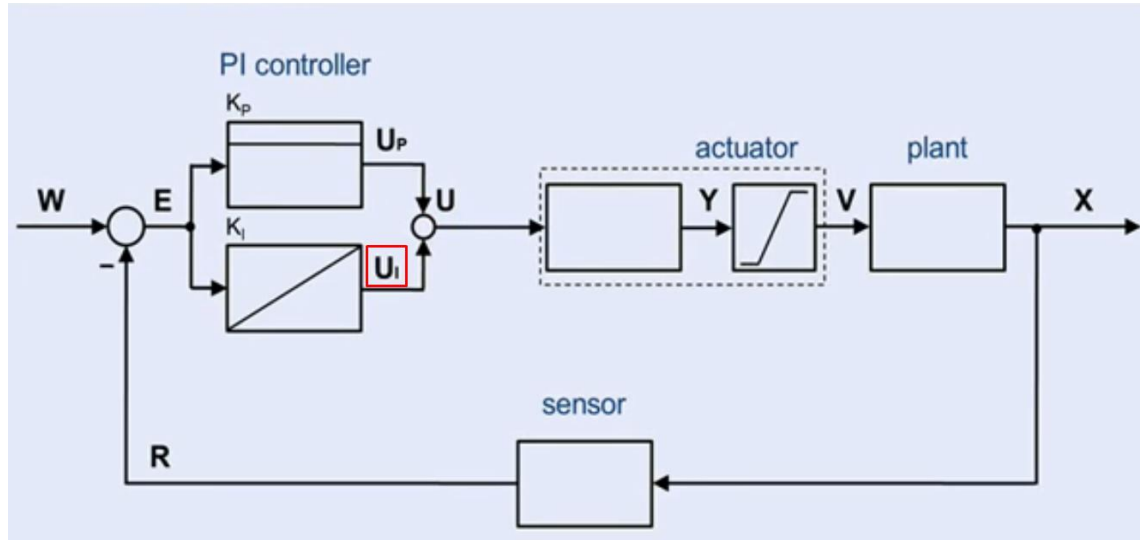


Name	Kd
Gain	5





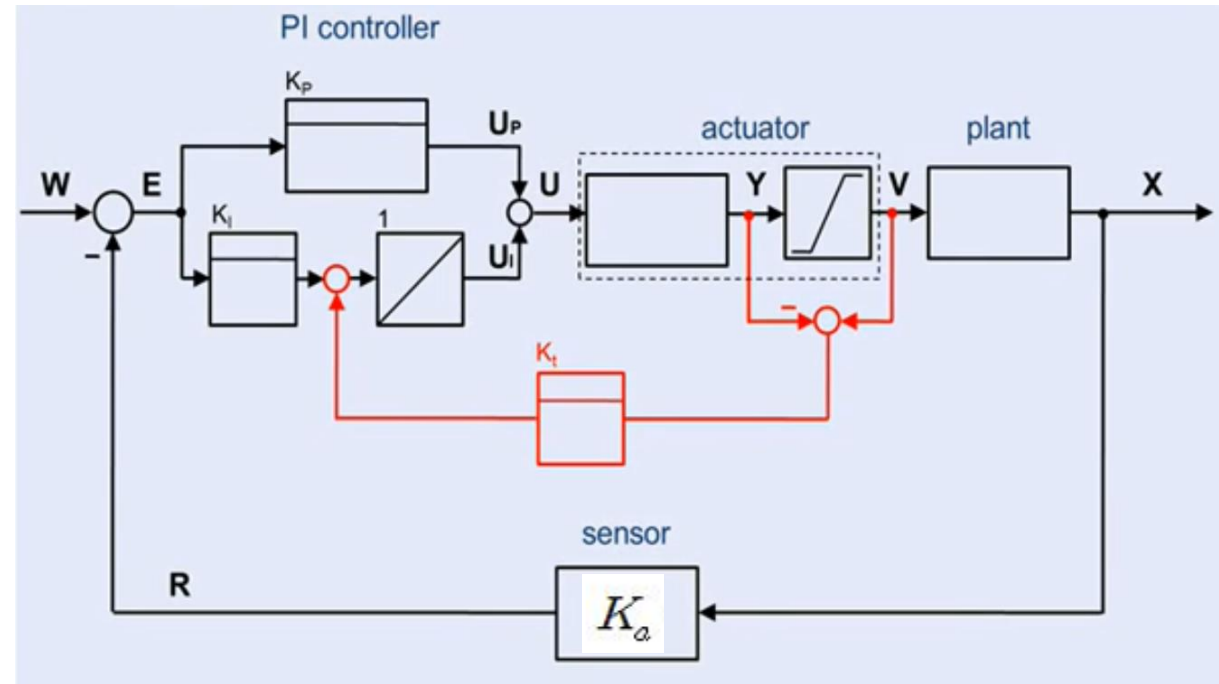
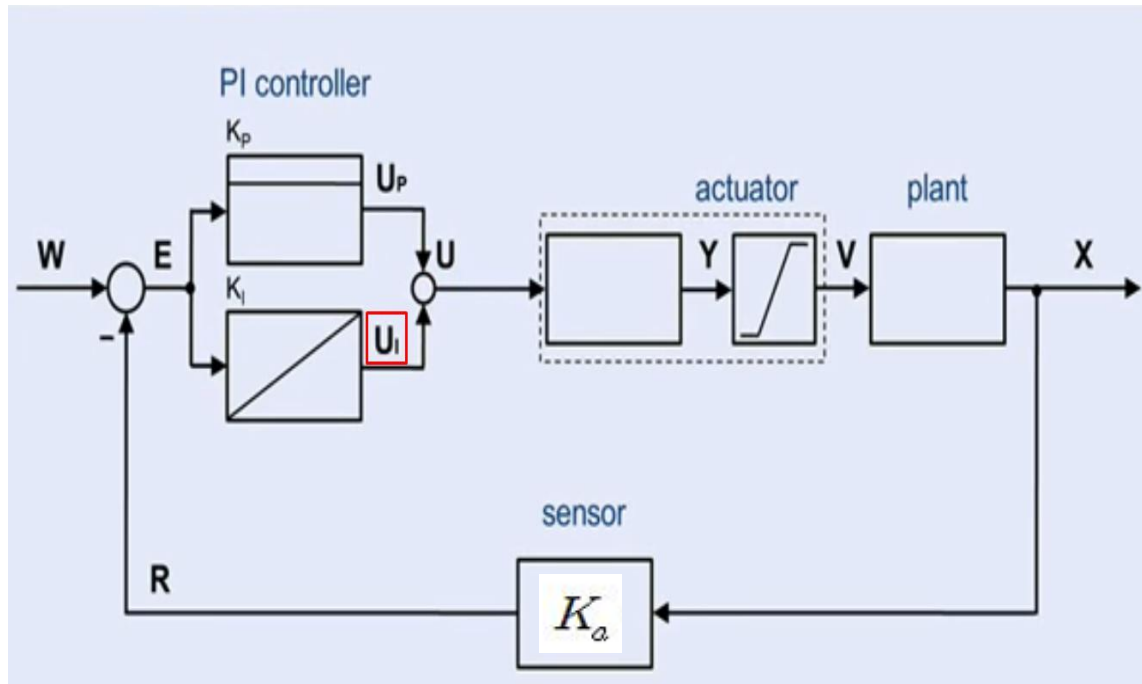
## 12) I 제어시 반드시 필요한 Anti-Windup 1



in steady state:  $E(t \rightarrow +\infty) = 0$

$\Rightarrow U(t \rightarrow +\infty) = U_i(t \rightarrow +\infty)$

## 12) I 제어시 반드시 필요한 Anti-Windup 2

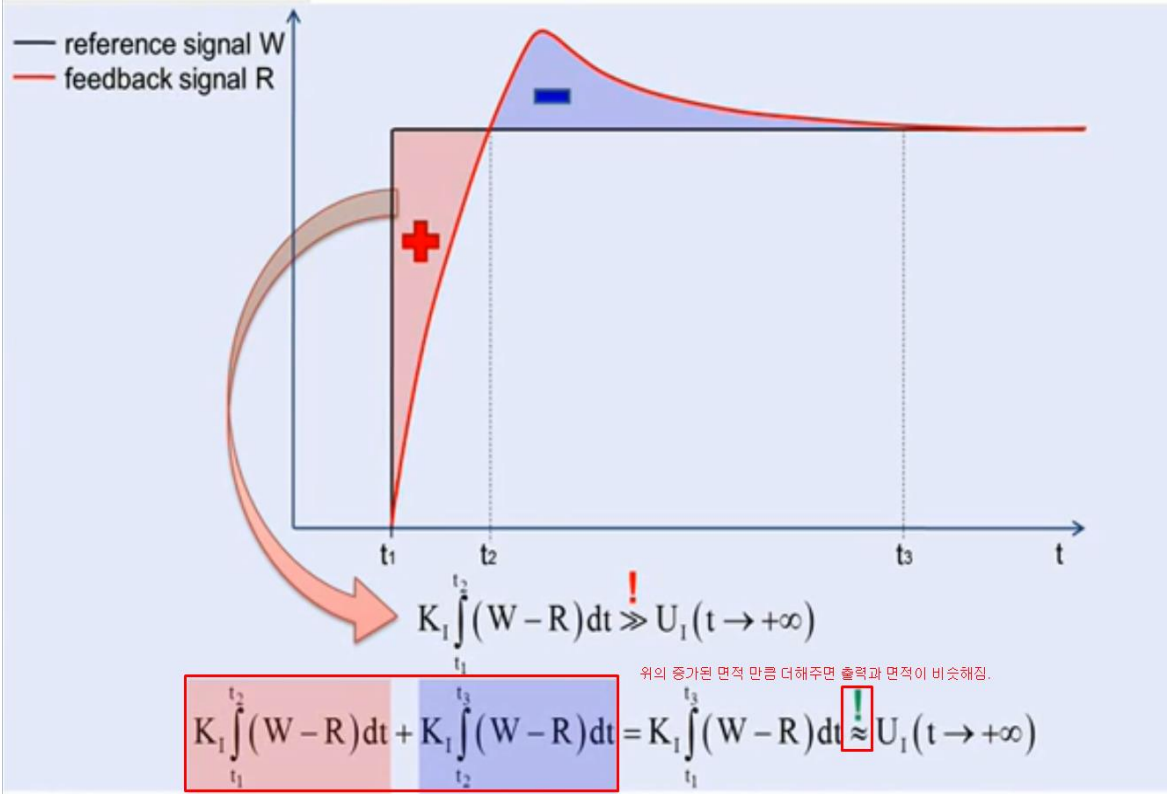
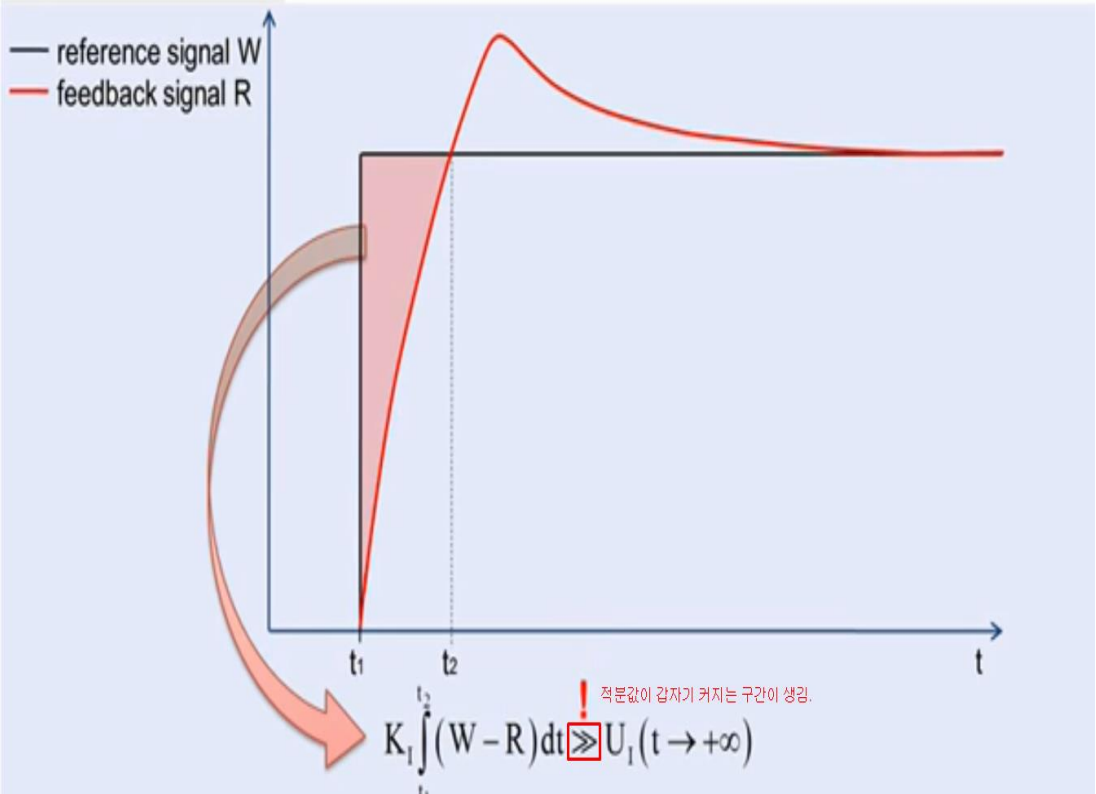


대략적인 적정범위

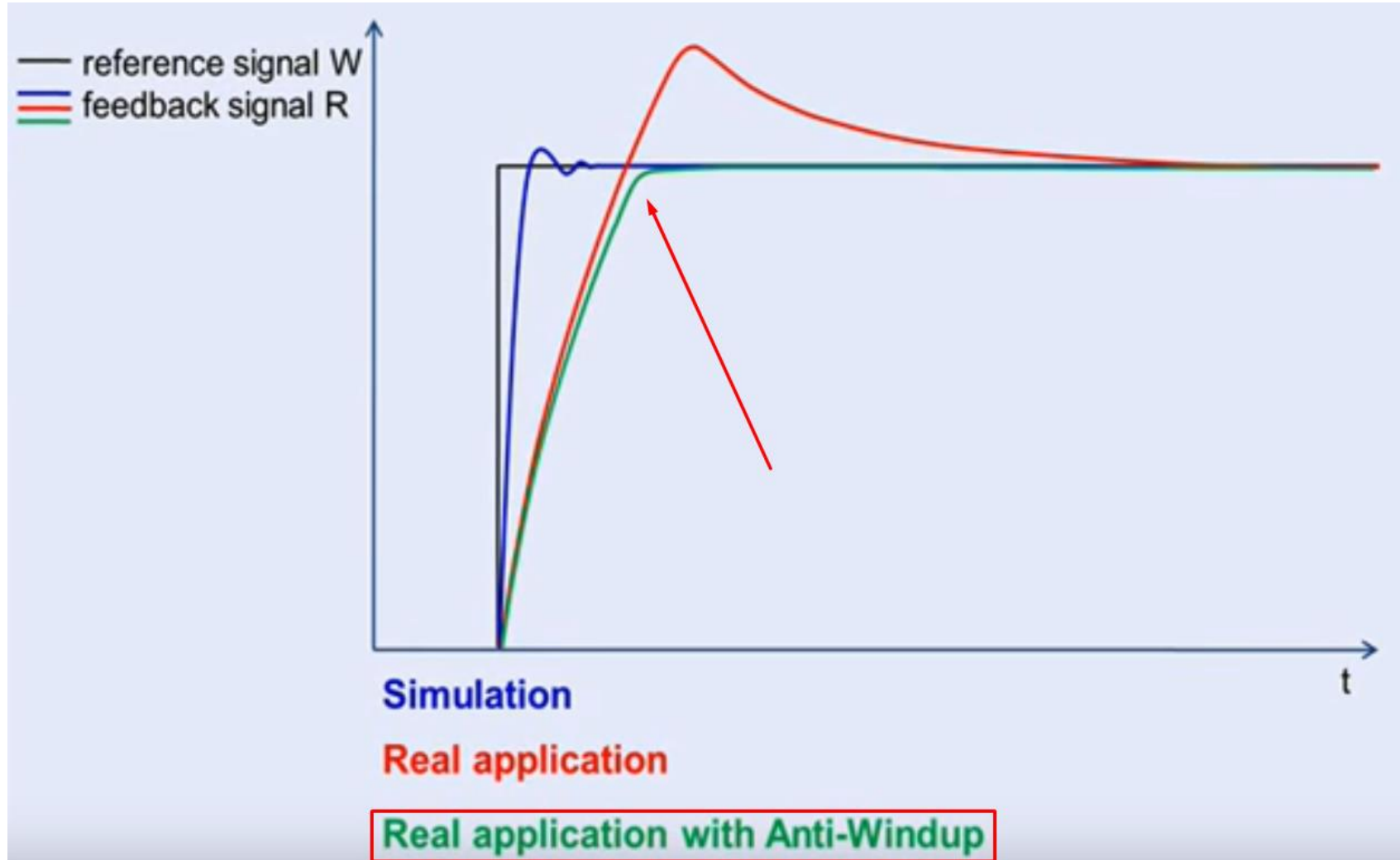
$$K_a = \frac{1}{K_p}$$

$$\frac{1}{3} \frac{1}{K_p} < K_a < \frac{3}{K_p}$$

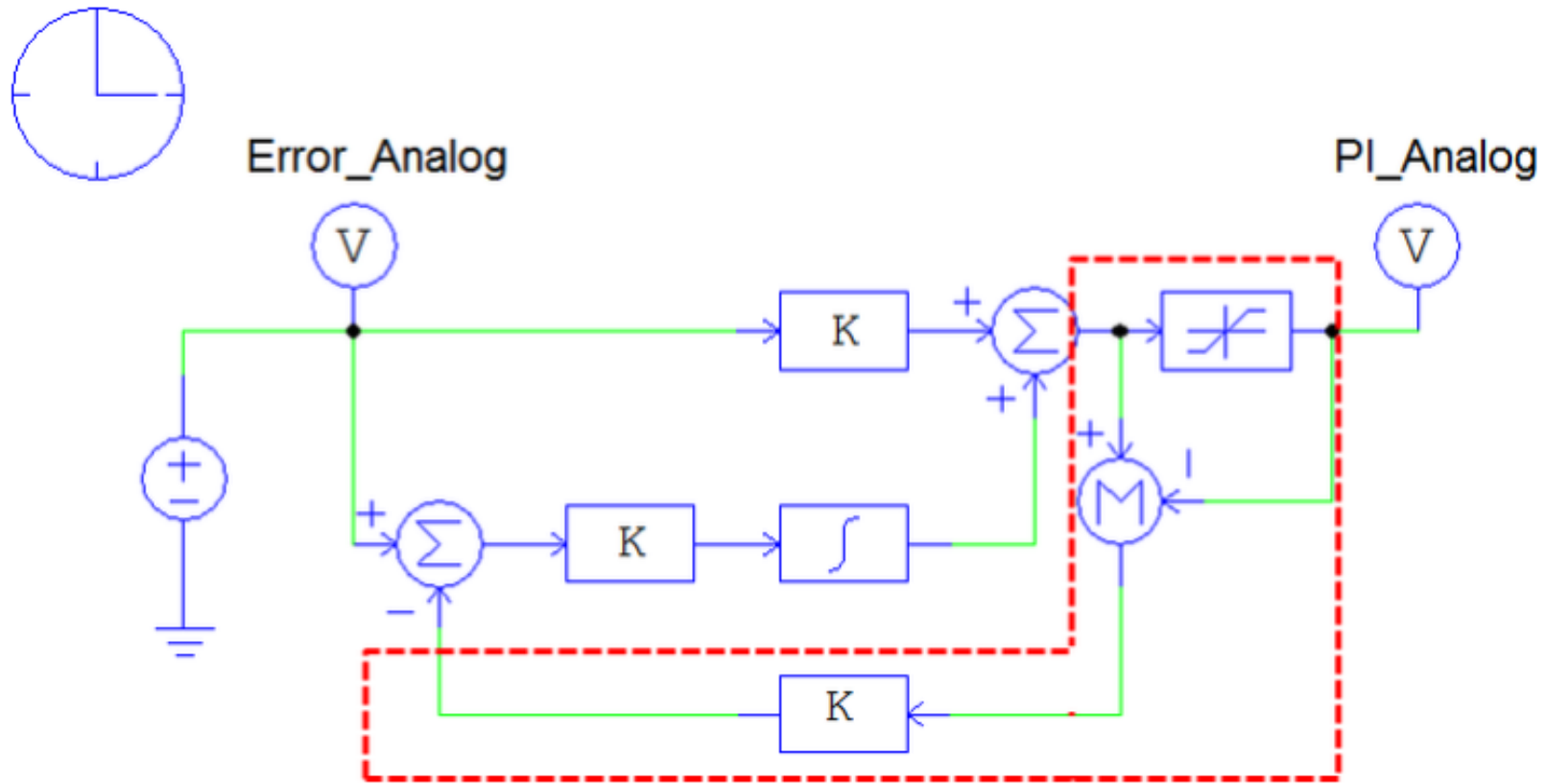
12) I 제어시 반드시 필요한 Anti-Windup 3



## 12) I 제어시 반드시 필요한 Anti-Windup 4



## 12) I 제어시 반드시 필요한 Anti-Windup 5



$$K_a = \frac{1}{K_p}$$

$$\frac{1}{3} \frac{1}{K_p} < K_a < \frac{3}{K_p}$$