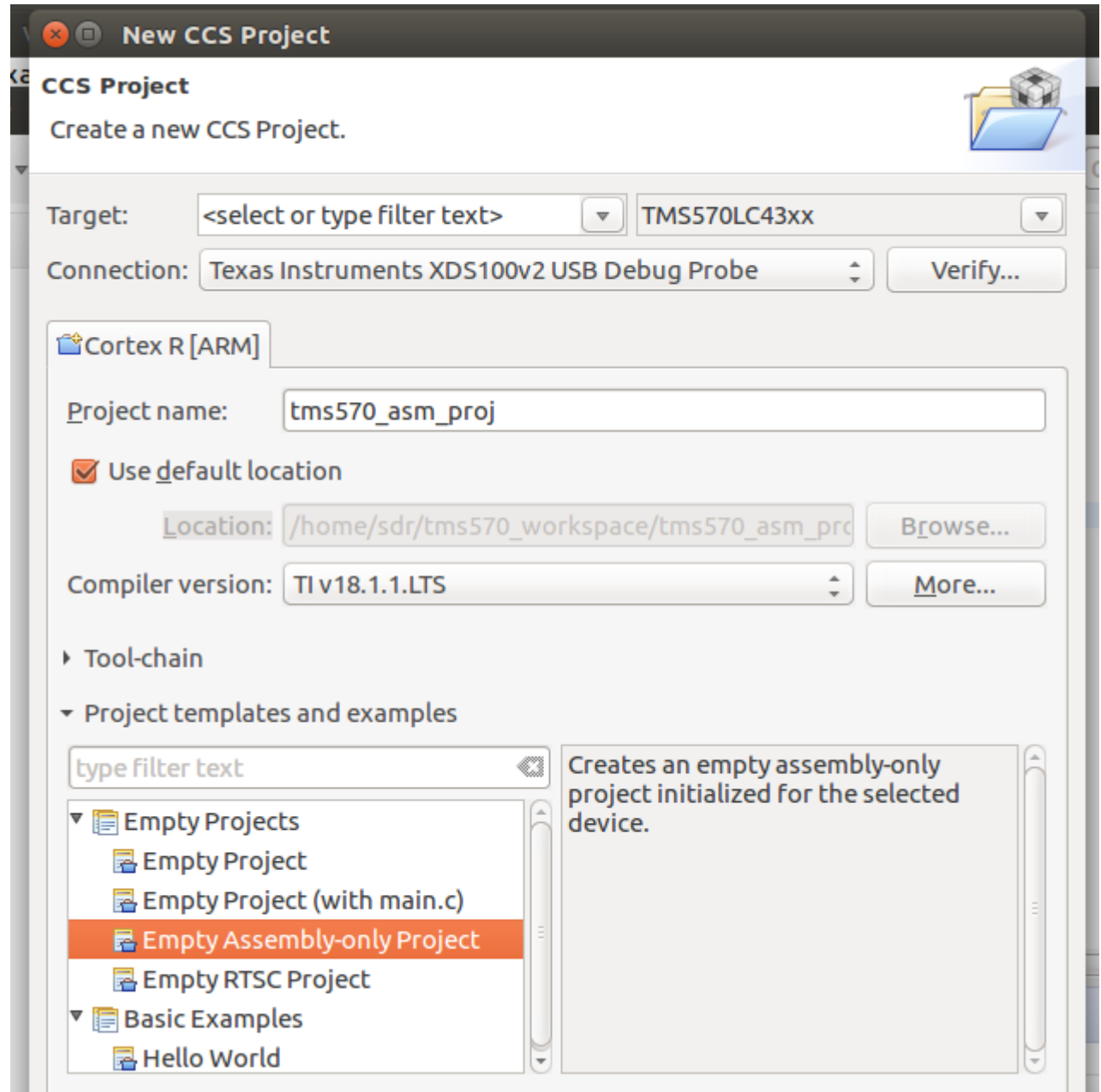


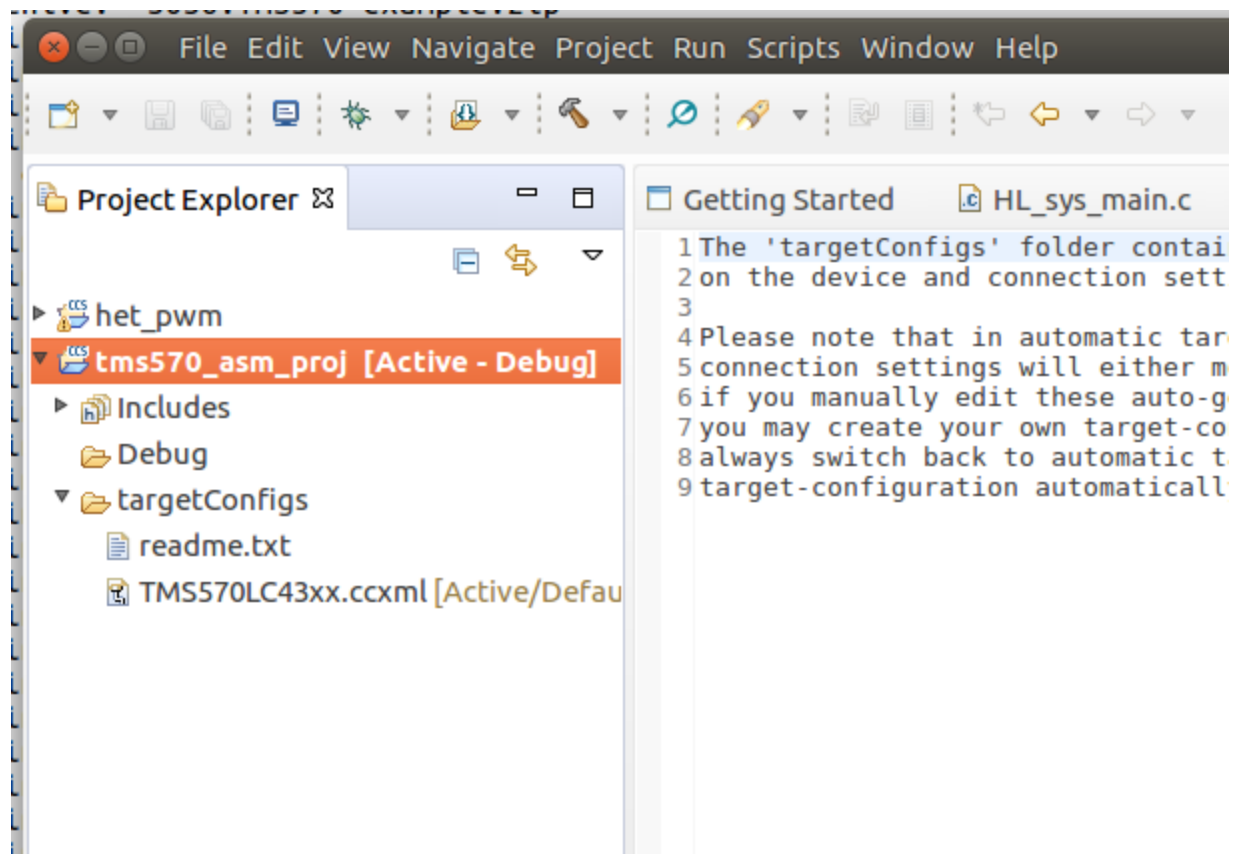
Xilinx Zynq FPGA, TI DSP, MCU 기반의 회로 설계 및 임베디드 전문가 과정

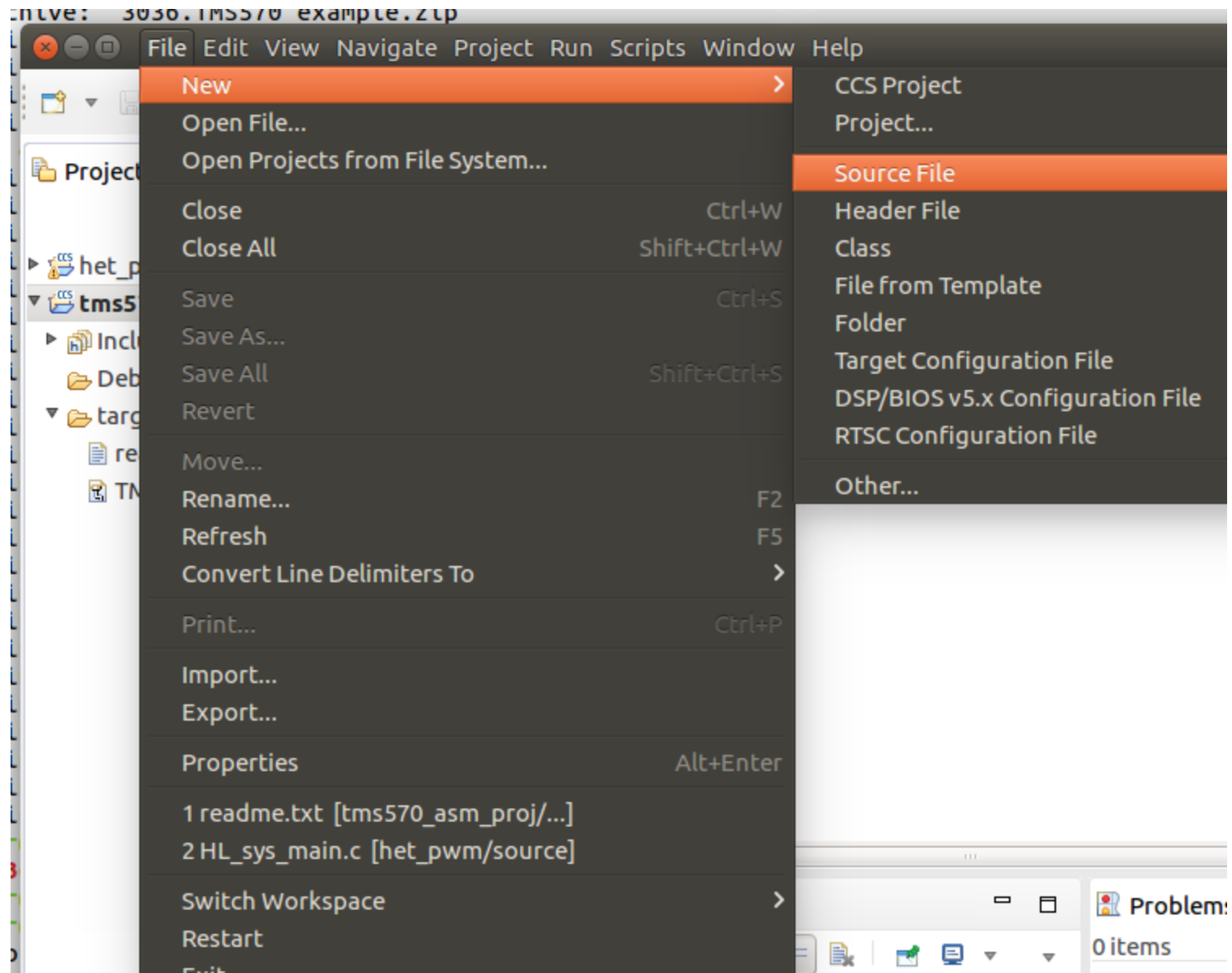
강사 – Innova Lee(이상훈)
gcccompil3r@gmail.com

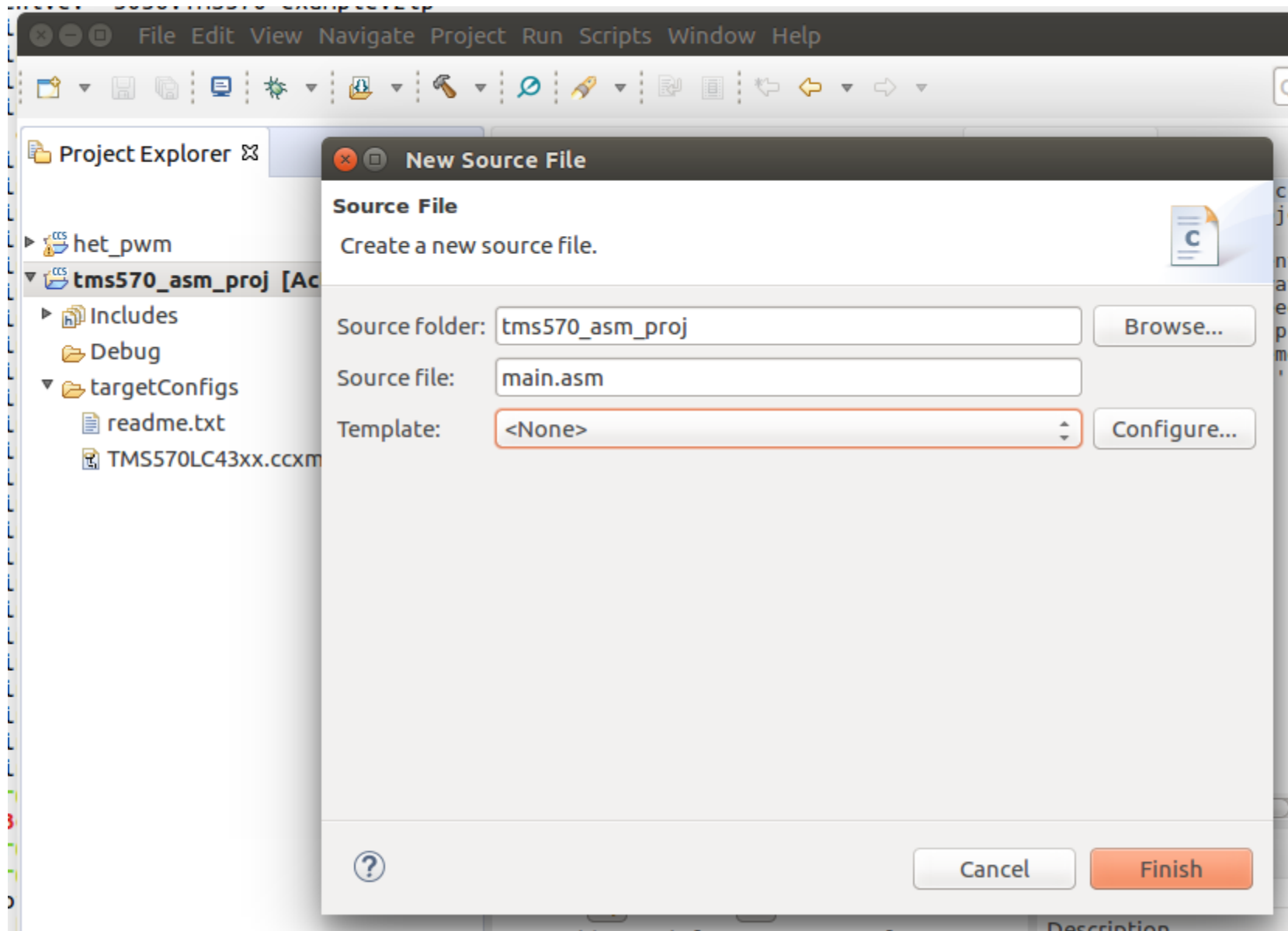
How to make TMS570LC4357 Based Assembly Project

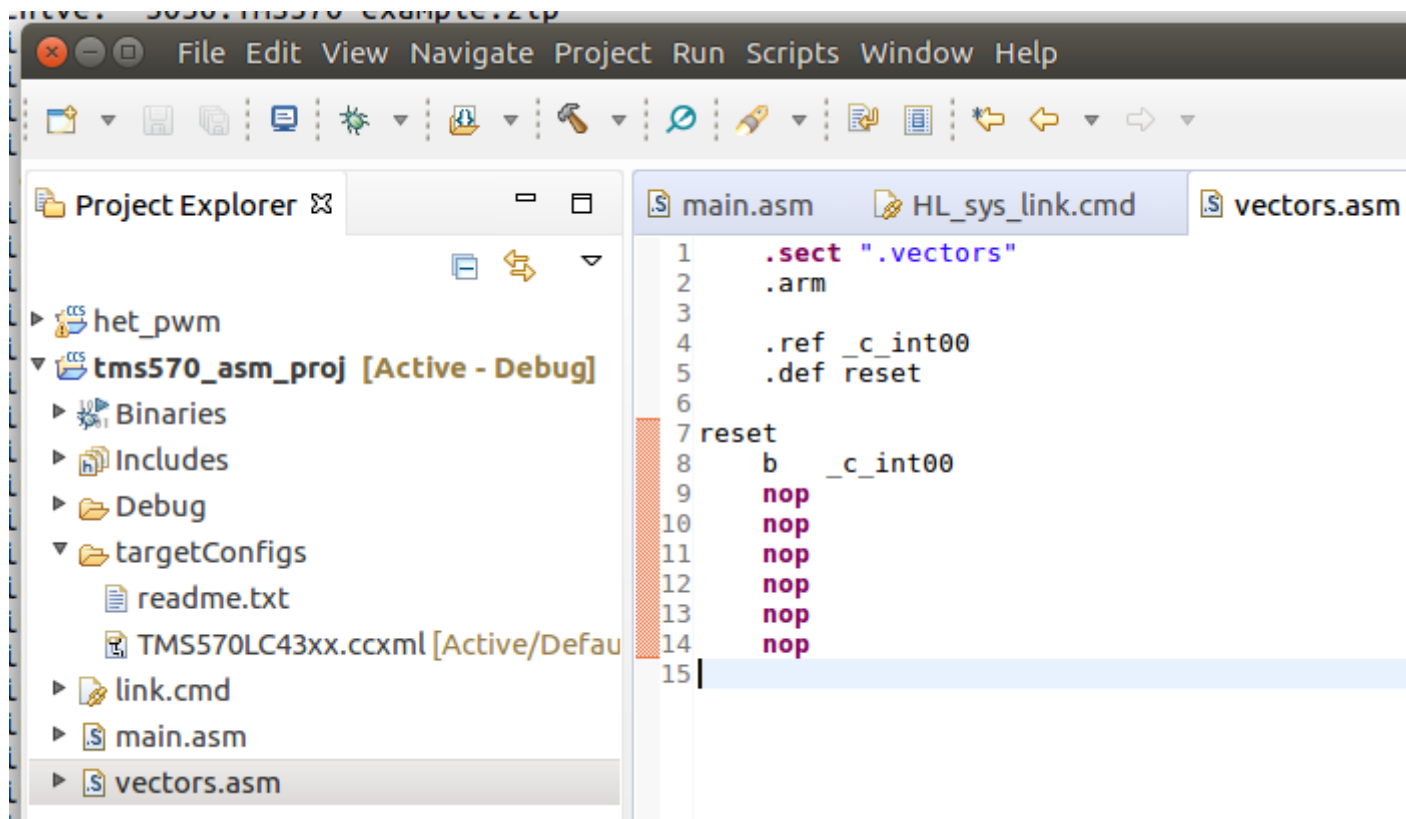
1. File – New – CCS Project
2. Empty Assembly-only Project

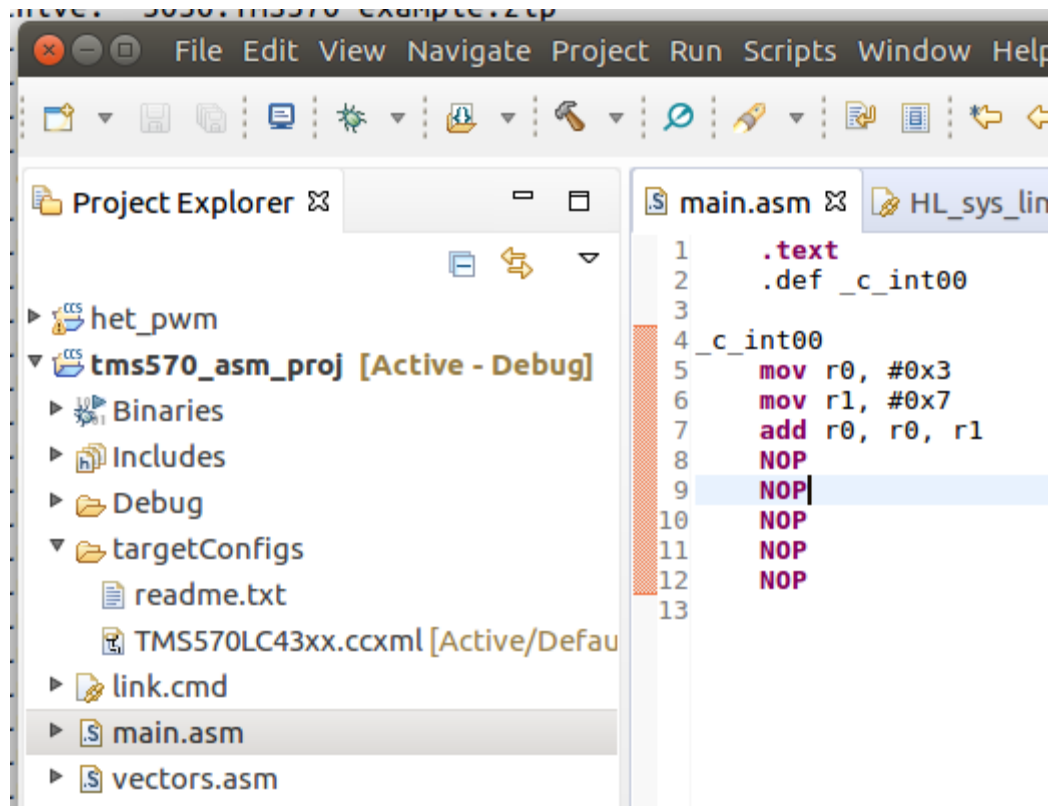












Some background info on this is available here: http://processors.wiki.ti.com/index.php/C6000_EABI_Migration#Conditional_Linking_Feature This page was written for the C6000 tools but works is the same for other devices as well.

Remedy

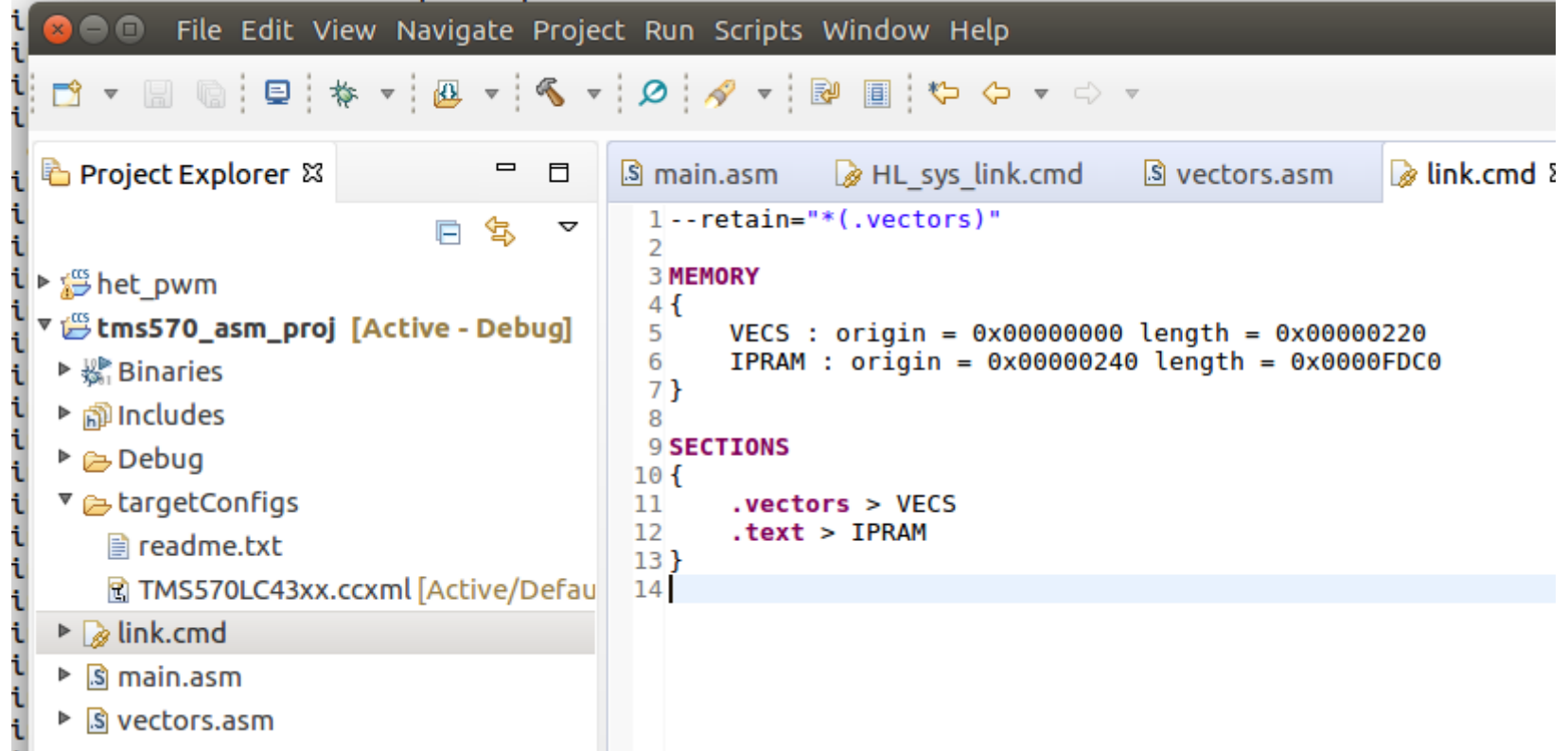
To work around this bug, add **below two directives** to the assembly source file.

- .retain tells the linker to keep this section (.text)
- .retainrefs additionally tells the linker to retain sections that reference this.

```
.cdecls C,LIST, "device.h"
;-----
        .text                                ; Program Start
        .retain
        .retainrefs
;-----
```

Additionally, if you are seeing diagnostic 10202 for missing entry point, [see here](#) for more details.

entive: 3036.TMS570 example.zip



- Binaries
- Includes
- Debug
- targetConfigs
- readme.txt
- TMS570LC43xx.ccxml [Active/Default]
- link.cmd
- main.asm
- vectors.asm

```
7 }  
8  
9 SECTIONS  
10 {  
11     .vectors > VECS  
12     .text > IPRAM  
13 }  
14
```

Console

CDT Build Console [tms570_asm_proj]

<Linking>
Finished building target:
"tms570_asm_proj.out"

**** Build Finished ****