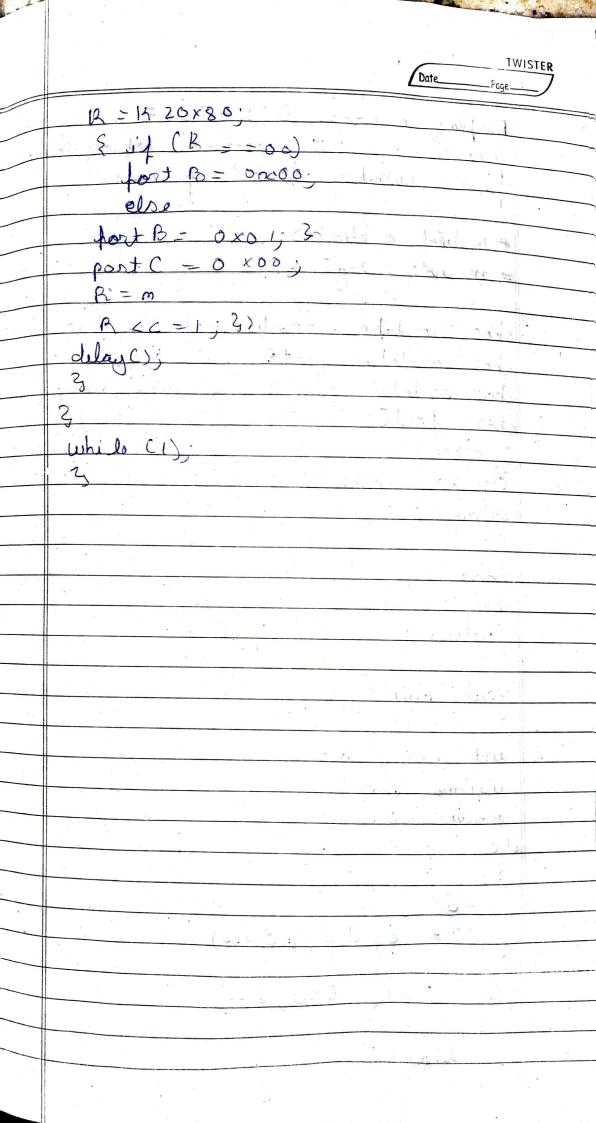
TWISTER Date\_ Ways drive # include < reg 52.h) # include estations vaid delay (int); () rion biov do 12 = 0 x 01 11 0001 delay (1000):  $i2 = 0 \times 02$ , (100)6 delay (1000); 22 0 X04; 110100 delay (1000); 512 - 0x0-8; 1/1000 delay (1000) 3 while (1) void delay Cast h

Full adrine # include < state home Void delag (int) word main (); do f P > 0 x 03 110 011 P, 0x0.6; 1/0100 delay (1000), delay (1000), P2 = 0×09; 11 1001 delay (1000) vad delag (int R)

Date\_\_\_\_ Half driver and one of Hindude 2 nog 82 h) 4 include ostdions vaid delay (int); void mais () do 2 = 0 x0 | 11 600 1 delay (1000) P2 = 0x03; 110011 delay (1000); p, 0002 /// 0010 delay (1000). delay (1000); P2 = 0x04; 1101000 dolay (1000); Pz J 0x06; 1/1100 delaij (100) P2 - 0x02; 1/1000 delay (100); P) = 0×09; 11,100 mis lar delay (100); while (1) void dolog (inte) and inj fron (i=0, i < K; i+t)

Display merroges Fire and help alternatively display interface for a suitable pried of time Ensure afterhing roads that makes it early to read both the # include cstdio h) # include 2 reg 81 h) chan redula communation 2003; dras adata farts at exessi; chor radala part ( -at - 0x080) char part [20] - [0xxe ox/9,0xdo OXHOXH, OXH 0 x86, 0x (7, 0x à c3, delry () long u for Cu=6: u 28000; U++); & 3 Voi d main () S int d.bij, m; unsigned chan b; commu = 0 mas; for (d=0, d c 3, d+1) & for ( 6=0 , 6 24; 6+4) & K = hard [i++] for (j=0; j'c8; j't+) m= K.



Brogram to dono the clurator interface # include < stdiah) unsigned char redat a command word at -Occe803; unriqued char occluse Port A at Doce 800; unriqued char occluse a proventation, Doguntal unigned long order Count : Relay C) for (count =0; count == 4500; Count + Reset () Step = Step 2 oncof; Port A = Stehj Step = Step 1 onlo; Port + = Step; zuturn o Crown () Switch ( Requested Floor) are oxod: while (step comps) Stoop to to

Date\_\_\_\_\_Page. Port A = Step; Delay (); Robert C); break; care 0006 : while (Step 20066) Step + +; Part A = Step; Dolay (1) Reset co: break; Care onc 07 : while Cstep conc. Step + + Port A - Step; Relay (); Resat (); broak; returno, Golows () Switch (Raguested Floor) Care and od while (Stop) oaf 3 Part + - Step; Delay ();

herot (), break; Care ox ob while (Step > Onc/6) Stop -- ; Port A = Step; Delay () Reget (); break; care onco o while (Step > or fo) Step -- ) Part A = Step; Delay (); Reset () returno, word main (). Commandword = 00082; Part t = oxfo; Orehent Floor - oncoe; While (1) 5 Requested Floor = Post B; haguested Floor - Requested Florer & On Of

	TWISTER  DatePage
	if Chequested floor ! = Oxof & Chequested floor !=  Prepart Flor )
	of Chequested Floor 2 Brownt Floor)
	Group C)
	GroDain ();
	Present Floor = Requested Floor;
	Requested Floor - Port B;
	3
-	
	43 - 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
<del> </del>	

Display mersonges BANG ALORE in rolling fushion on a 7 - segment display interface for a suitable period of time. # in durlo astdio.h.s # indude c rog 51.h > char ocalates comme = at - on . 203 char a data parts -al-0x.80; char acdata part Cation 302; char part [20] = { on f, onef, coeff, 3 xconco33, 00c38, 00c0,02c32,0ACCT On 66, 6 XAF, 0×363,1; delay () fort u=0; u (4000; b++); void main () unt a, 6, j, m; unsigned char B; coloure = 0x80 for (d=0; d<j,d+t) for ( 6=13, 6>0; 6--) delay ();

Date\_\_\_\_ K = port [i++]; for (j=0; j=0; j++) meh; 13 - p. RGD(80; af (12-60) port B=corco elso! port B = 0001; part (-axol; port = 00000; R = 19 while (i)

	Stepper Hotor: (Clock-wire)
	# include & Stdio b > w
	H 10 3 1 1 2 300 3 1 1 1
	chas acdarda hort -at - ONE OUD
-	chap ordata post at oxe sou
	char xdata acc at 0x30;
-	
	delay () {
	Jox ( i = 0 ; i + 300 ; i + +)
	5)
	\$
	void mais ()
	2
	port = 0 x 80; B
<u> </u>	while (1) & coil & Motos Supply
	acc = 0 oc 82;
+-	port a =acc; c
	delay (); (sholl) &
	acc = 0 x 44)
	delay ();
	are goez; Motor 0
	port a = acc; Supply.
	delay C,
1.50 1.50 1.40	ace souls
	parta = acc;
	delay ();
	3
	15

Drive a stepher notor interface to rotate
the motor in anti-dochuiro by Determen Successive Staps # Include (Stdio.h) # inlude (reg 5) b) char ocdata part - at - 0 ac 800; char reduta part a -at-once 800; char ordala acc at ox 330; int ( ) = 0 ; j 2800 ; j + +) part = 0 nc80; while (i) arc = oall; porta-acci delay (); acc=once; part a = arr \_\_delay (); acc = 0914 G pont a = acc; delay ();

