

Ref. no: EICT/2021-22/JFDP/MLSPCE/143

Electronics & ICT Academy
Supported by Ministry of Electronics and Information Technology (MeitY), Govt. of India
Indian Institute of Technology Guwahati

Participation Certificate



This is to certify that

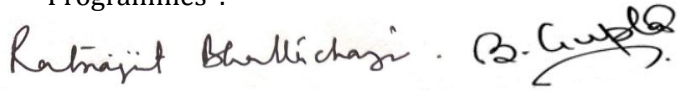
Mr./Ms./Dr. _____

Ms. Mukta Mukesh Nivelkar

of _____

Veermata Jijabai Technological Institute, Mumbai

has participated in a One - week Online Joint Faculty Development Programme on “**Machine Learning Application in Signal Processing and Communication Engineering**” jointly organised by Electronics and ICT Academies held from **03 - 08 January, 2022** under the “**Scheme of financial assistance for setting up of Electronics and ICT Academies**” of the **Ministry of Electronics and Information Technology (MeitY), Government of India**. He/she has also taken part in the hands on sessions during the program. We wish him/her a very good luck for his/her future endeavour. This Faculty Development Programme is at par with other Quality Improvement Programmes*.



Prof. Ratnajit Bhattacharjee

Principal Coordinator and Co-PI, E&ICT Academy
IIT Guwahati
Assam



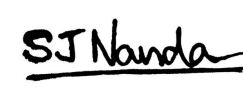
Dr. Bharat Gupta

Joint Principal Coordinator
NIT Patna
Bihar



Prof. Sanjeev Manhas

Joint Principal Coordinator
IIT Roorkee
Uttarakhand



Dr. Satyasai J. Nanda

Joint Principal Coordinator
MNIT Jaipur
Rajasthan



Dr. Anil Kumar

Joint Principal Coordinator
IIITDM Jabalpur
Madhya Pradesh



Prof. RBV Subramanyam

Joint Principal Coordinator
NIT Warangal
Telangana

* As per minutes of the 3rd PRSG.