

# **Some Closure Results for Polynomial Factorization and Applications**

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**Joint work with Chi-Ning Chou (Harvard) and Noam Solomon (MIT)**

# Multivariate Polynomials

$$P = \sum_{\mathbf{e}} \alpha_{\mathbf{e}} \mathbf{X}^{\mathbf{e}}$$

$$\mathbf{e} = (e_1, e_2, \dots, e_n), \sum_i e_i \leq d \quad \mathbf{X}^{\mathbf{e}} = X_1^{e_1} X_2^{e_2} \cdots X_n^{e_n}$$

$\alpha_{\mathbf{e}}$ - field elements

Polynomial on n variables, of degree d.

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- Polynomial Method in Combinatorics (Kakeya sets, Distinct distances, Joints problem, Cap sets)

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**But wait, how is  $P$  given ?**

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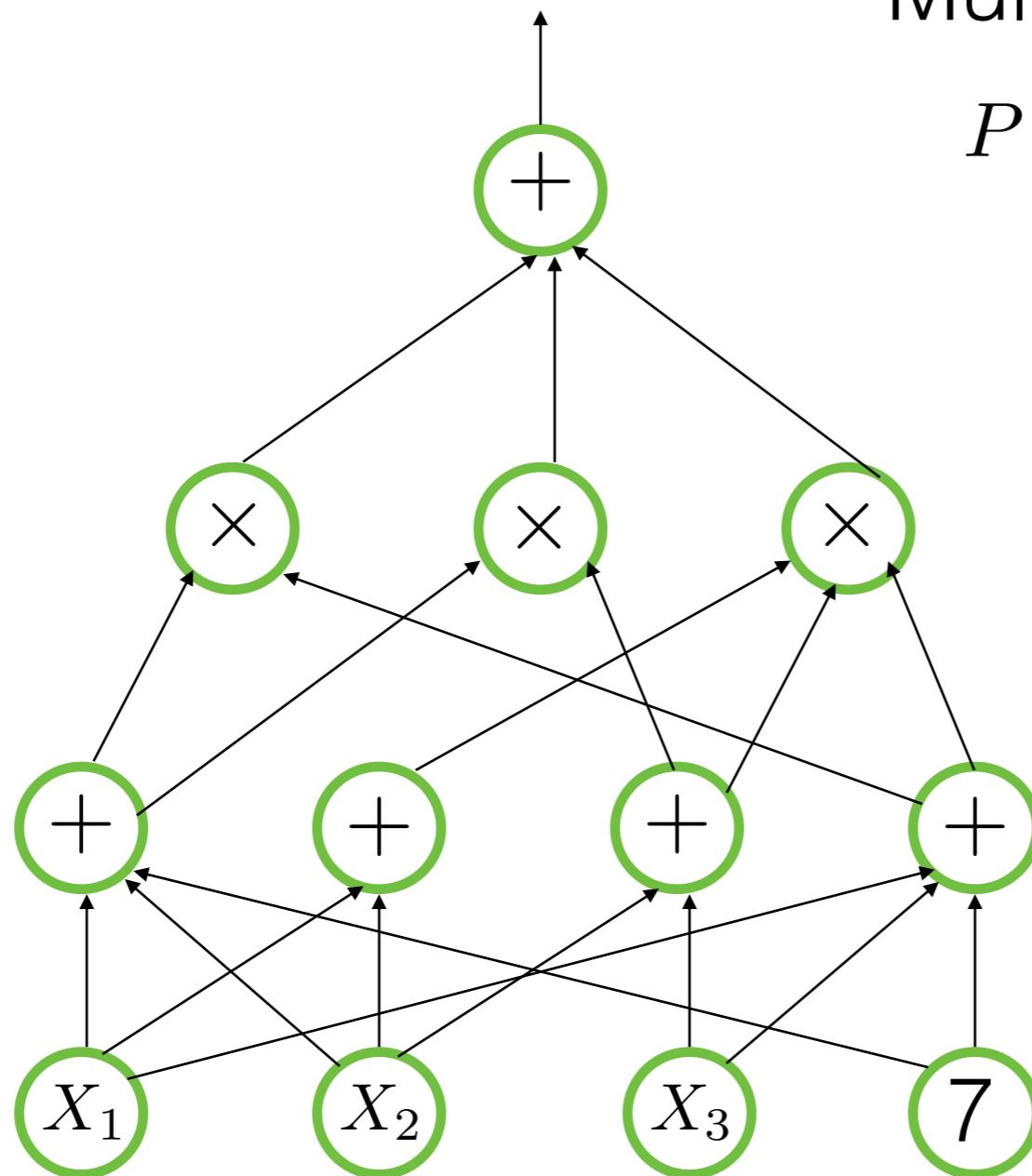
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Is there a representation which is more succinct than sum of monomials?

# Arithmetic circuits

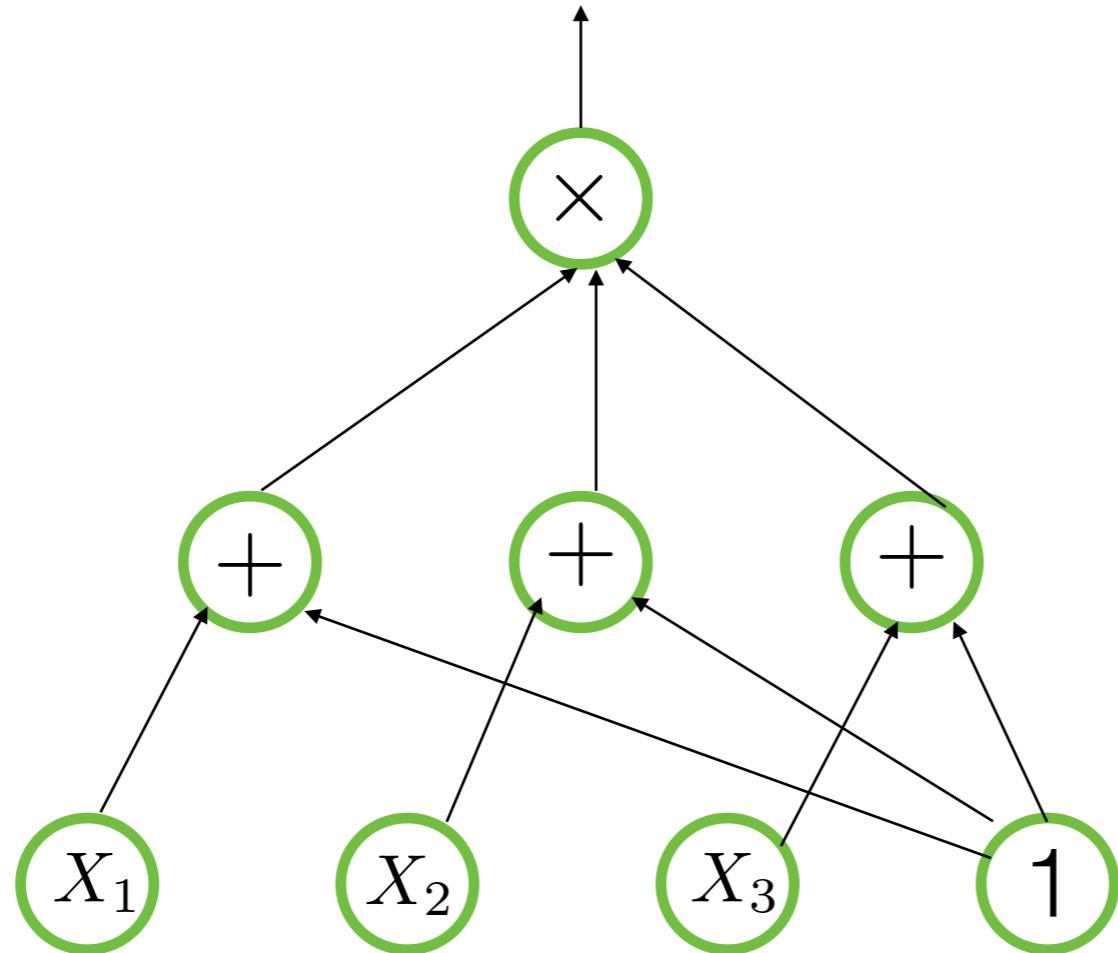
Multivariate polynomial

$$P \in \mathbb{F}[X_1, X_2, \dots, X_n]$$

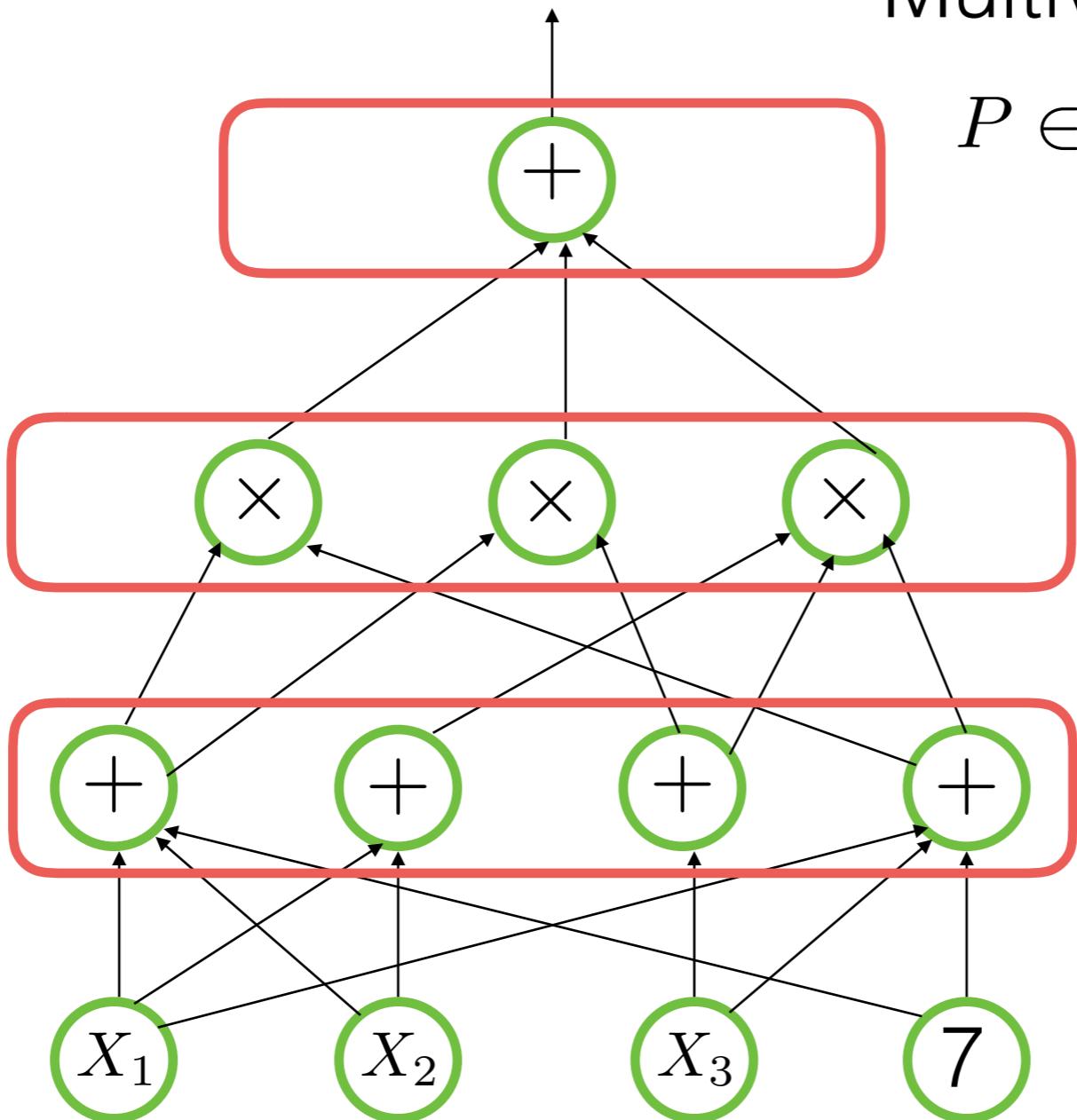


# Arithmetic circuits

$$(X_1 + 1)(X_2 + 1)(X_3 + 1)$$



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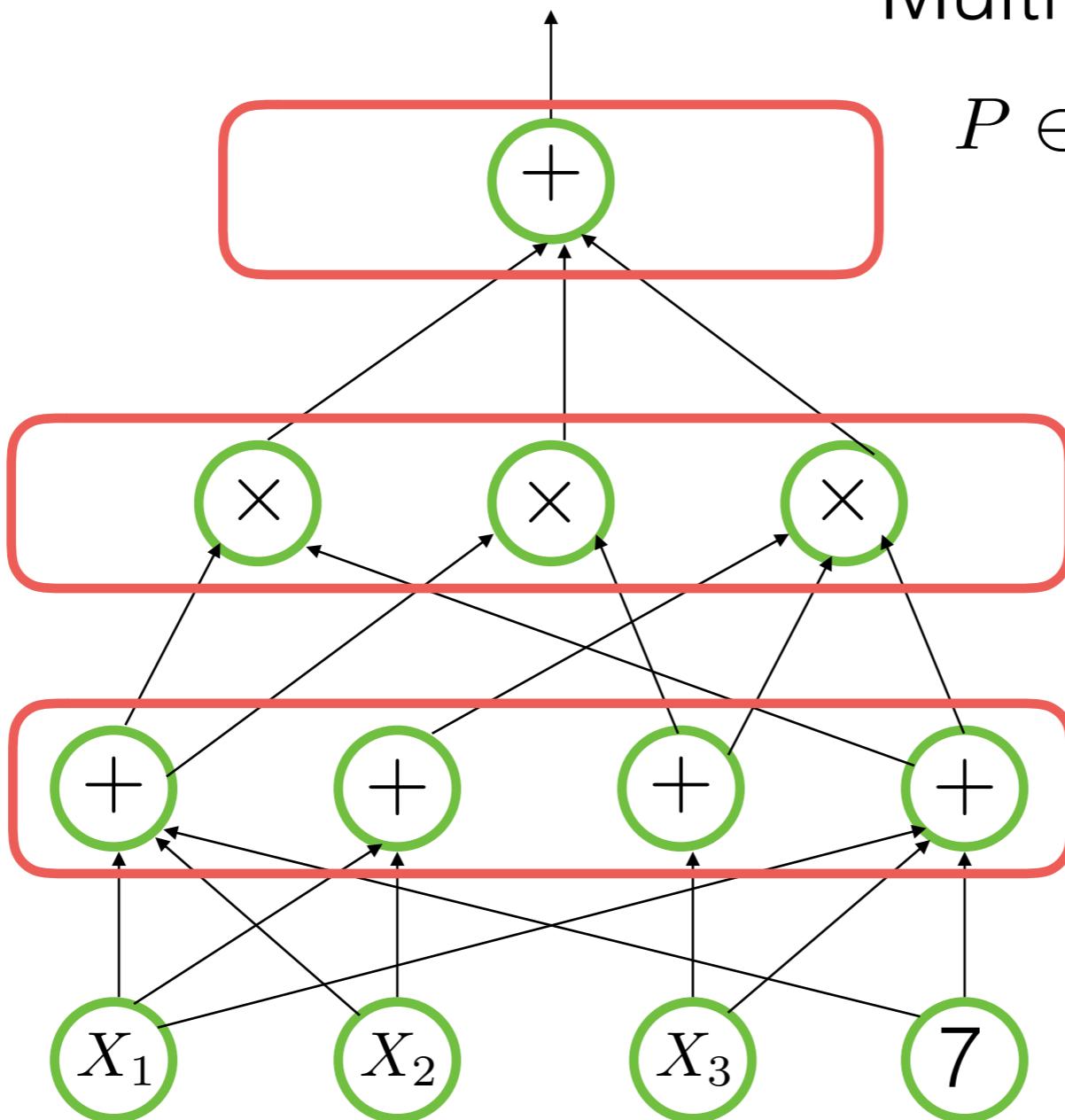


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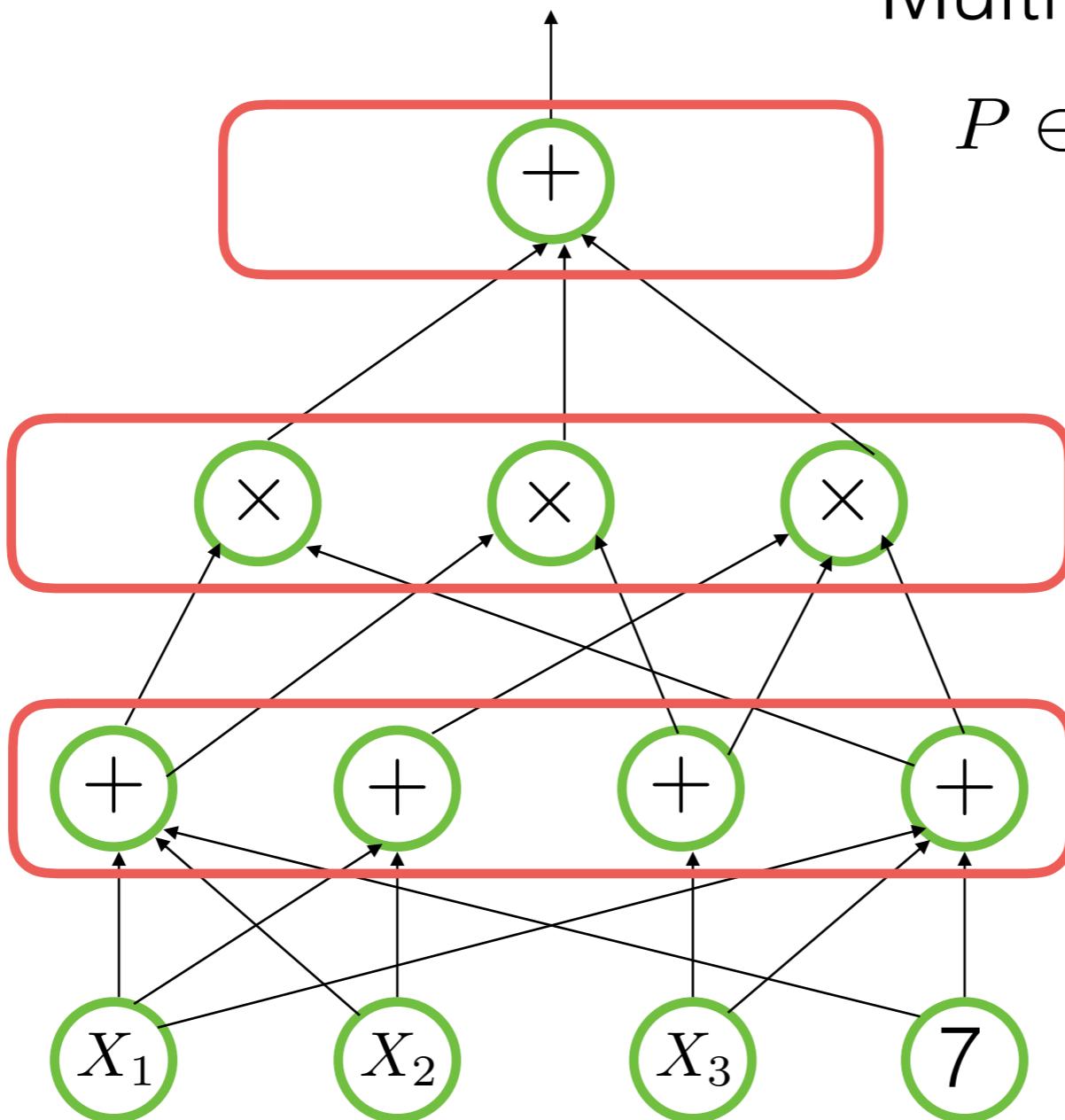
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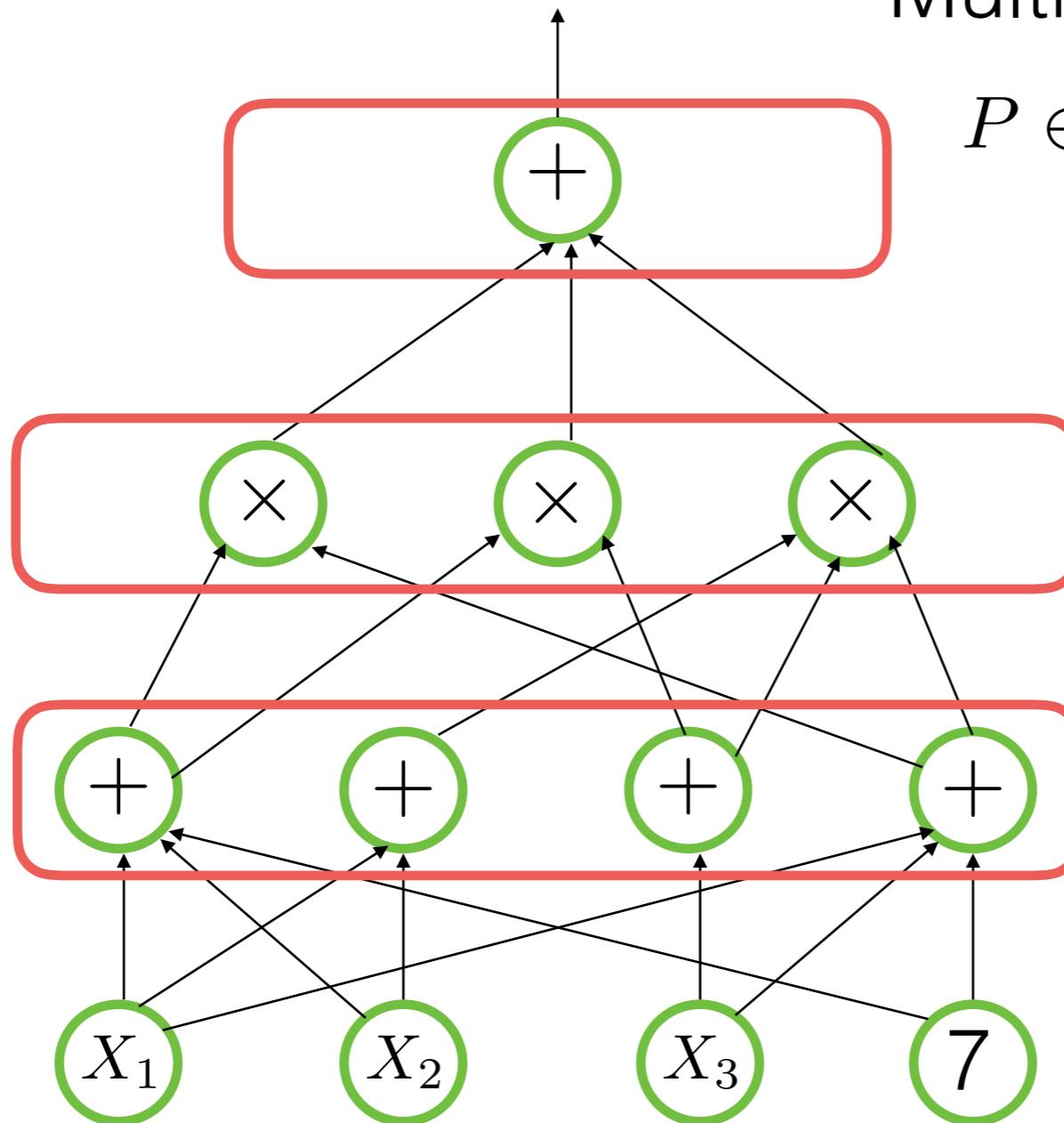
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A circuit is called a formula if the underlying graph is a tree.

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**But, can we compute their factors efficiently ?**

# Polynomial Factorization

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Not true for sparse representation!

# Factors of sparse polynomials

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Another reason why this representation is not so nice...

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**The complexity class VP is uniformly closed under taking factors.**

# Detour : Algebraic P and NP

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[Folklore, Ben-Or] Esym(m,d) is in VP.

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[Valiant] Permanent is complete for VNP.

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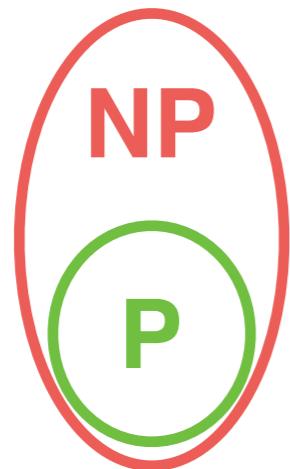
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- In particular, he conjectured that Permanent does not have  $\text{poly}(m)$  sized arithmetic circuits.
- Algebraic analogue of the P vs NP question.

# Cook's vs Valiant's hypothesis

P vs NP

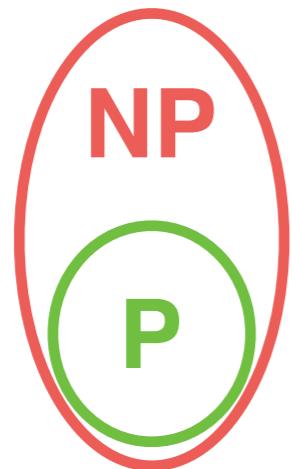


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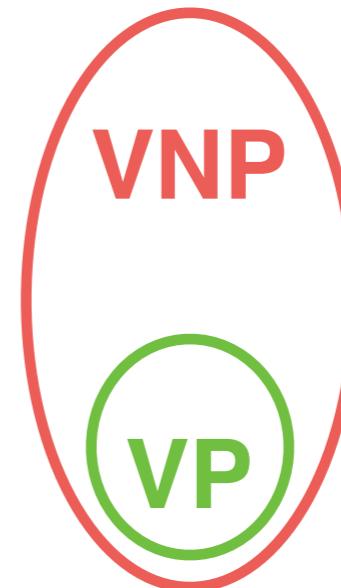


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[Burgisser] Under GRH,  $VP = VNP$  implies non-uniform  $P =$  non-uniform  $NP$ .

# Algebraic P vs Algebraic NP

Are there explicit polynomial families which cannot be computed by polynomial sized arithmetic circuits ?

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- Applications to derandomization.

# Closure under Factorization

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**The complexity class VP is uniformly closed under taking factors!**

# What about closure of other classes ?

- If a polynomial is in VNP, are the factors in VNP ?
- If a polynomial has small formulas, do its factors have small formulas ?
- If a polynomial has small constant depth circuits, do the factors have small constant depth circuits ?

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**Conjecture [Burgisser]**

**The complexity class VNP is closed under taking factors.**

# Results I : Closure results

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**Improves a quasi-polynomial upper bound of Dutta-Saxena-Sinhababu.**

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**Theorem [Chou-K-Solomon, Dutta-Saxena-Sinhababu]**

Let  $P$  be an  $n$ -variate degree  $D$  polynomial computable by a formula of size  $s$ , and let  $f$  be a factor of degree  $d$  of  $P$ .

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**For low, but growing degree factors, this is still  $\text{poly}(n)$ .**

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Let  $P$  be an  $n$ -variate degree  $D$  polynomial computable by a depth  $k$  circuit of size  $s$ , and let  $f$  be a factor of degree  $d$  of  $P$ . Then,  $f$  can be computed by depth  $k + O(1)$  circuits of size  $d^{O(d^\epsilon)} \cdot \text{poly}(n, s, D)$

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A bound of  $n^{O(d^\epsilon)} \cdot \text{poly}(n, s, D)$  follows from Kaltofen's result and standard structure theorems, but this is not  $\text{poly}(n, s)$  as long as  $d$  is growing.

# Results II : Applications to Hardness vs Randomness

# Hardness vs Randomness

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A natural question on its own, but some unexpected and remarkable connections to lower bounds and algorithm design.

# A simple randomized algorithm

**Lemma [Ore, Schwartz, Zippel, DeMillo, Lipton]**

Let  $S$  be a subset of the field. Then,

$$\Pr_{a \in S^n} [C(a) = 0] \leq \frac{d}{|S|}$$

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And, we didn't even have to look inside the circuit.

Also, gives an  $\exp(n \log d)$  time deterministic algorithm. We are interested in doing anything better than this!

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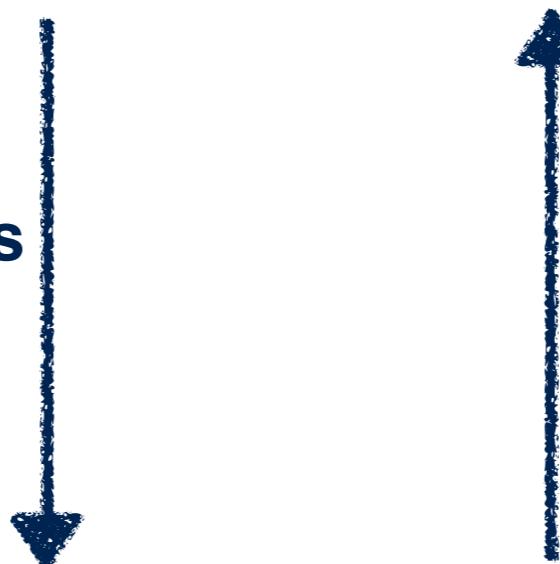
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## **Theorem [Kabanets-Impagliazzo]**

**Super-polynomial lower bounds for arithmetic circuits imply non-trivial deterministic PIT for polynomial size arithmetic circuits.**

**Crucially, this proof uses Kaltofen's result about closure of VP under factorization.**

**And thus, does not extend to formulas or low depth circuits, where we do not know closure results.**

**Scaled down versions of this result ?**

# Randomness from hardness at low depth

## Question [Shpilka-Yehudayoff]

**Do lower bounds for low depth circuits imply deterministic PIT for them ?**

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**Do lower bounds for low depth circuits imply deterministic PIT for them ?**

## Theorem [Dvir-Shpilka-Yehudayoff]

**Lower bounds for low depth circuits imply deterministic PIT for low depth circuits with bounded individual degree.**

# Randomness from hardness at low depth

## Theorem [Chou-K-Solomon]

**Super-polynomial lower bounds for low depth arithmetic circuits for  $\text{poly}(\log n)$  degree polynomials imply non-trivial deterministic PIT for them.**

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**Thus, we get rid of the low individual degree assumption of Dvir et al. at the cost of asking for lower bounds for low degree polynomials.**

**For depth  $k$  PIT, we need lower bounds for depth  $k+5$  circuits, which as of now, renders this result unusable.**

# Randomness from hardness for formulas

## Theorem [Chou-K-Solomon]

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So, (seemingly) small improvement in the state of lower bounds for formulas has extremely interesting consequences for the PIT question.

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- VNP (the algebraic analog of NP) is closed under taking factors.
- Low (but growing) degree factors of small formulas, low depth circuits have small formulas, low depth circuits respectively.
- Even somewhat non-trivial lower bounds for formulas, low depth circuits imply sub exponential time deterministic Identity Testing algorithms for them.

# Snippets of the proof

# Key lemma : structure of factors

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## Lemma (informal)

**Let  $P$  be an  $n$ -variate polynomial of degree  $D$ , which can be computed by a size  $s$  circuit. Let  $f$  be a factor of  $P$  of degree  $d$ .**

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‘Normal form’ for factors.

Would be helpful in arguing about their structure.

# Structure of factors

**P, n-variate, degree D  
size s circuit.**

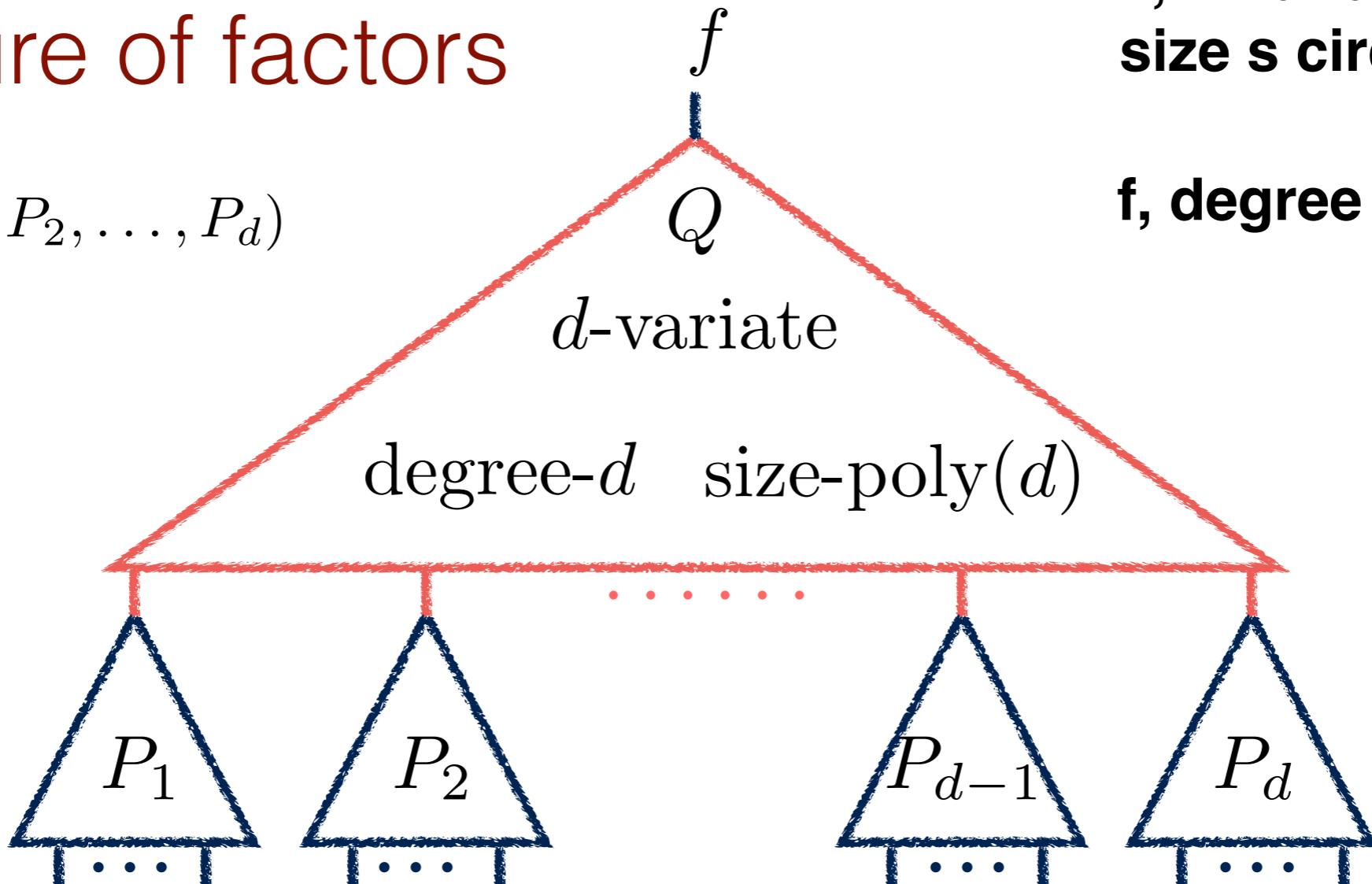
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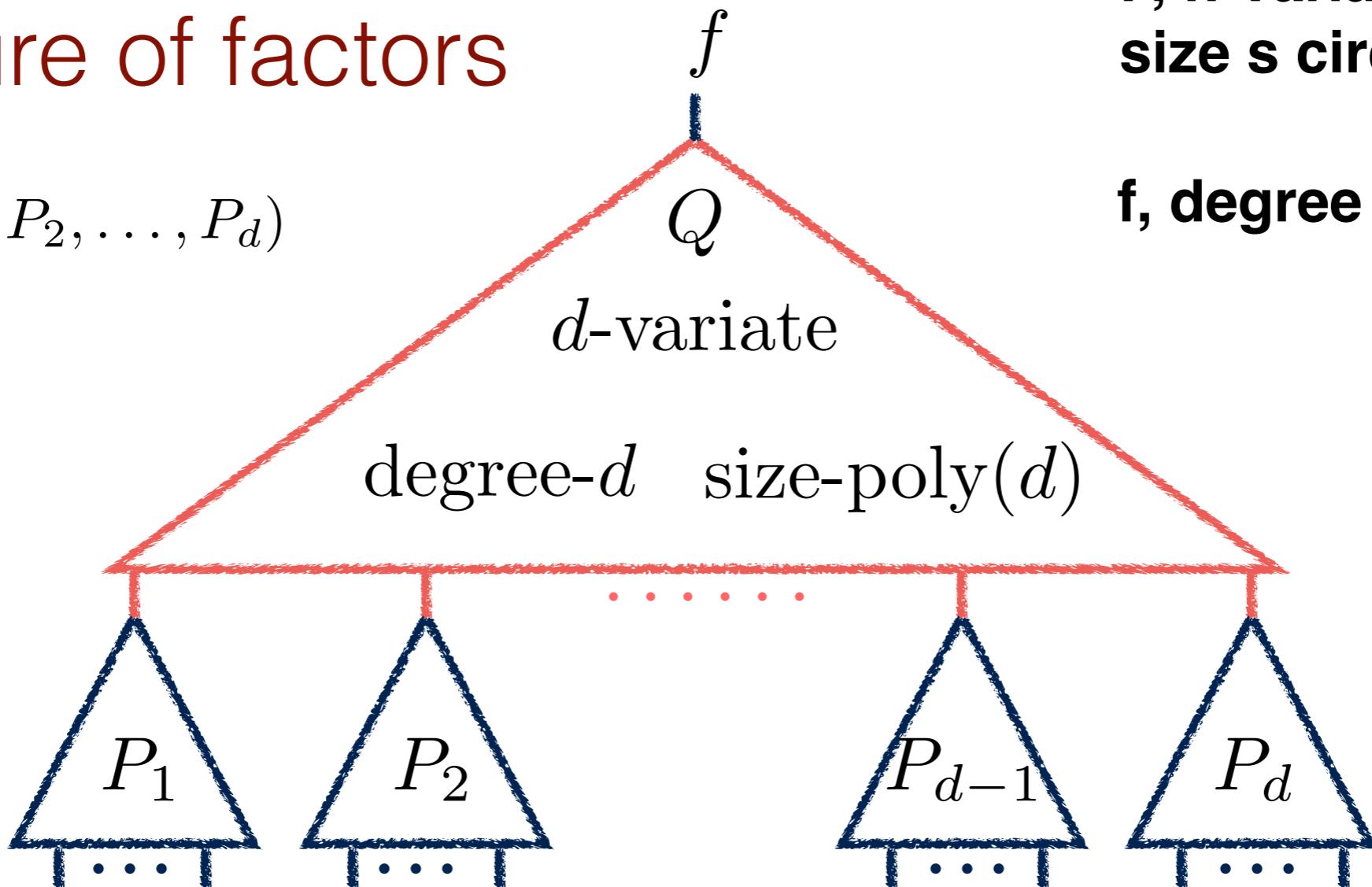


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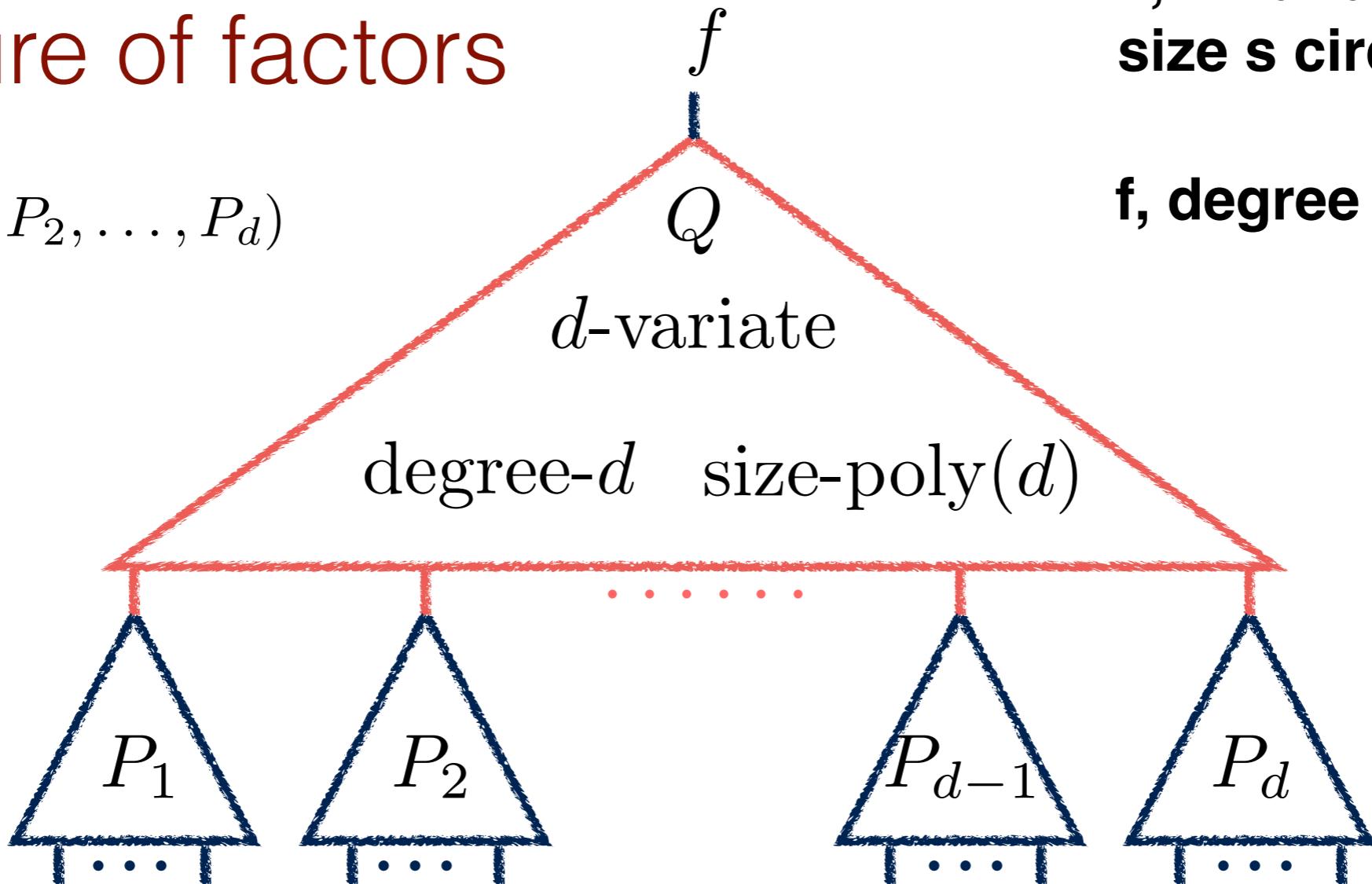


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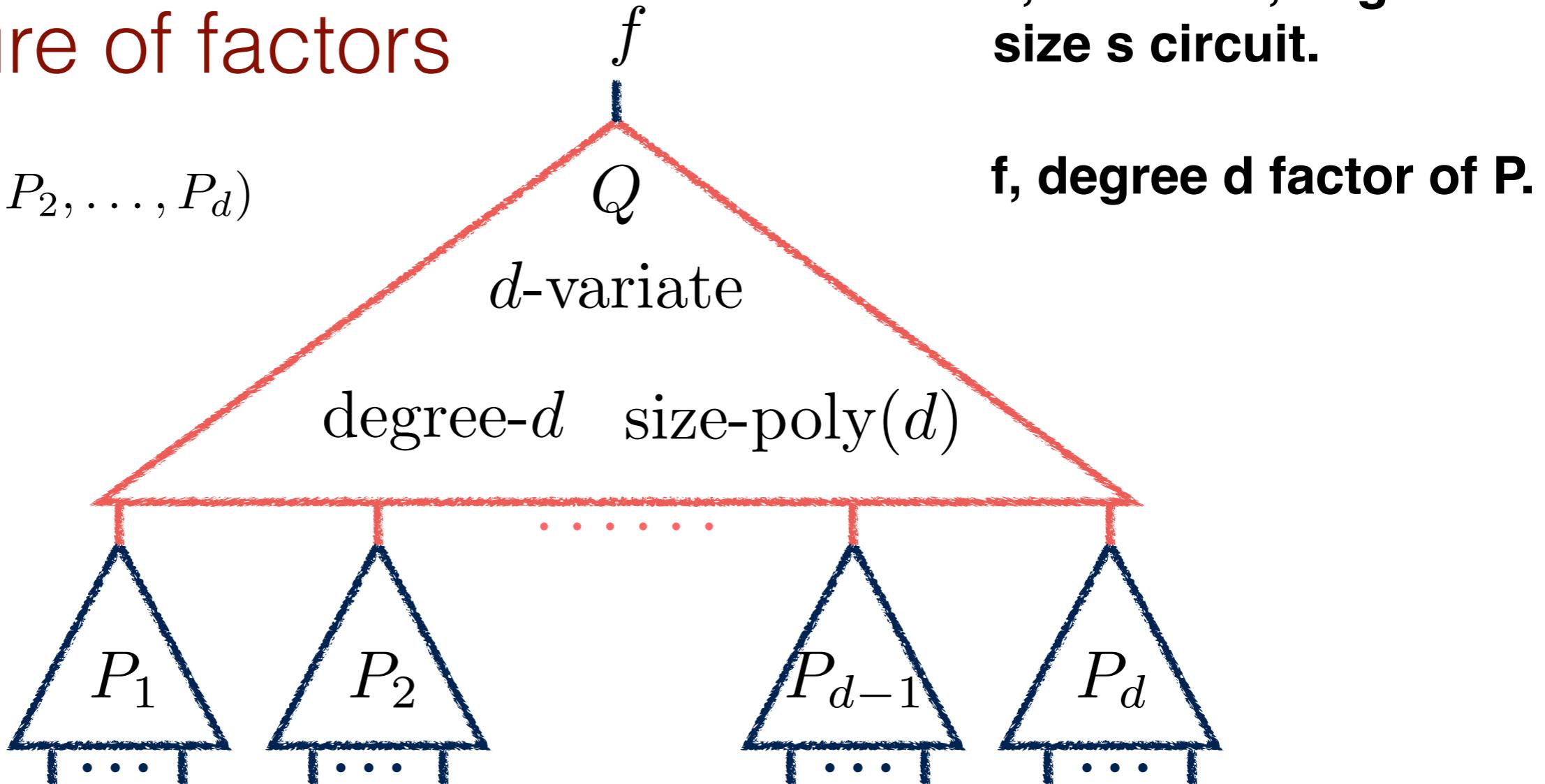


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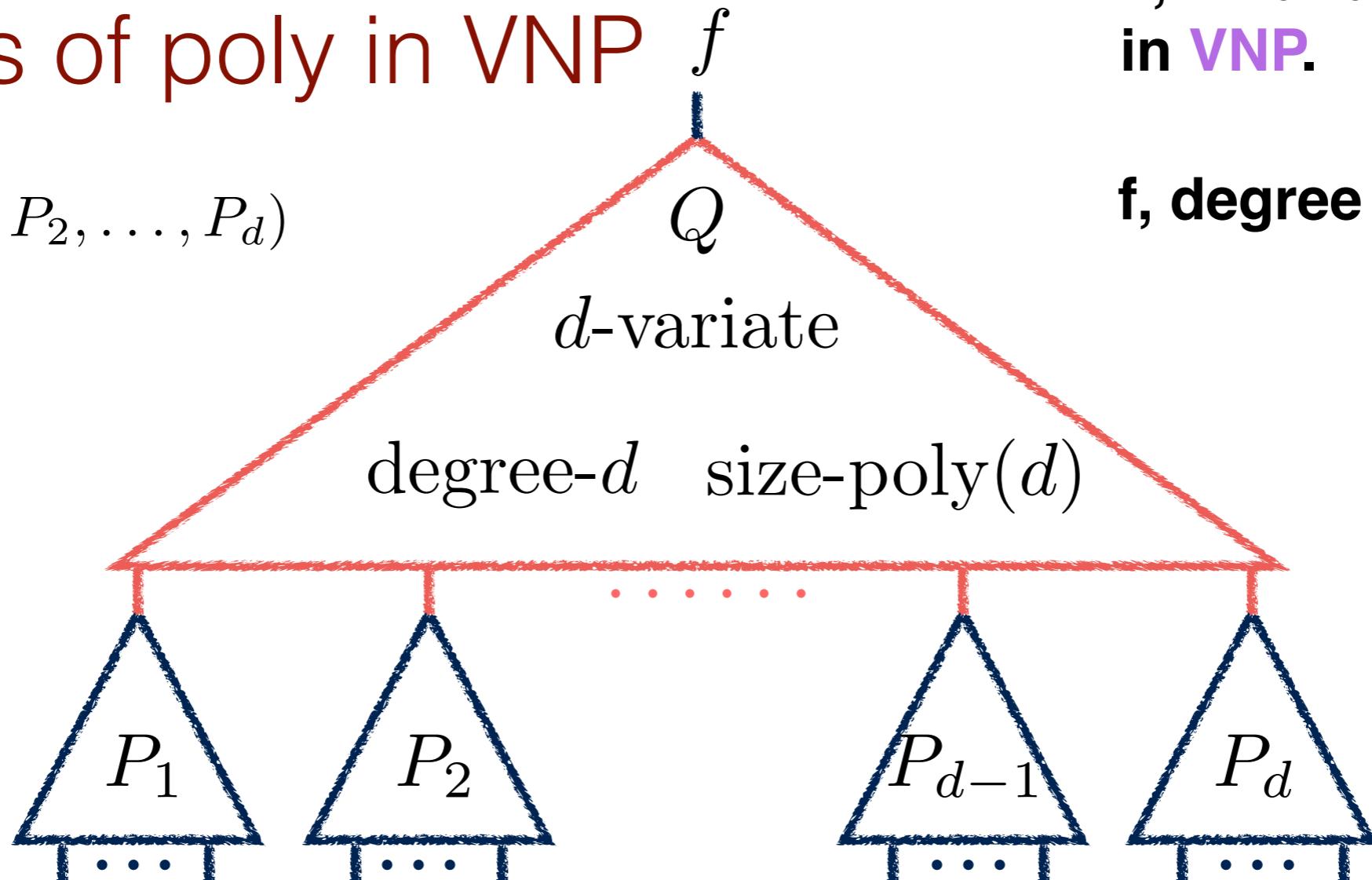
low depth  $\rightarrow$  low depth  
formula  $\rightarrow$  formula  
VNP  $\rightarrow$  VNP

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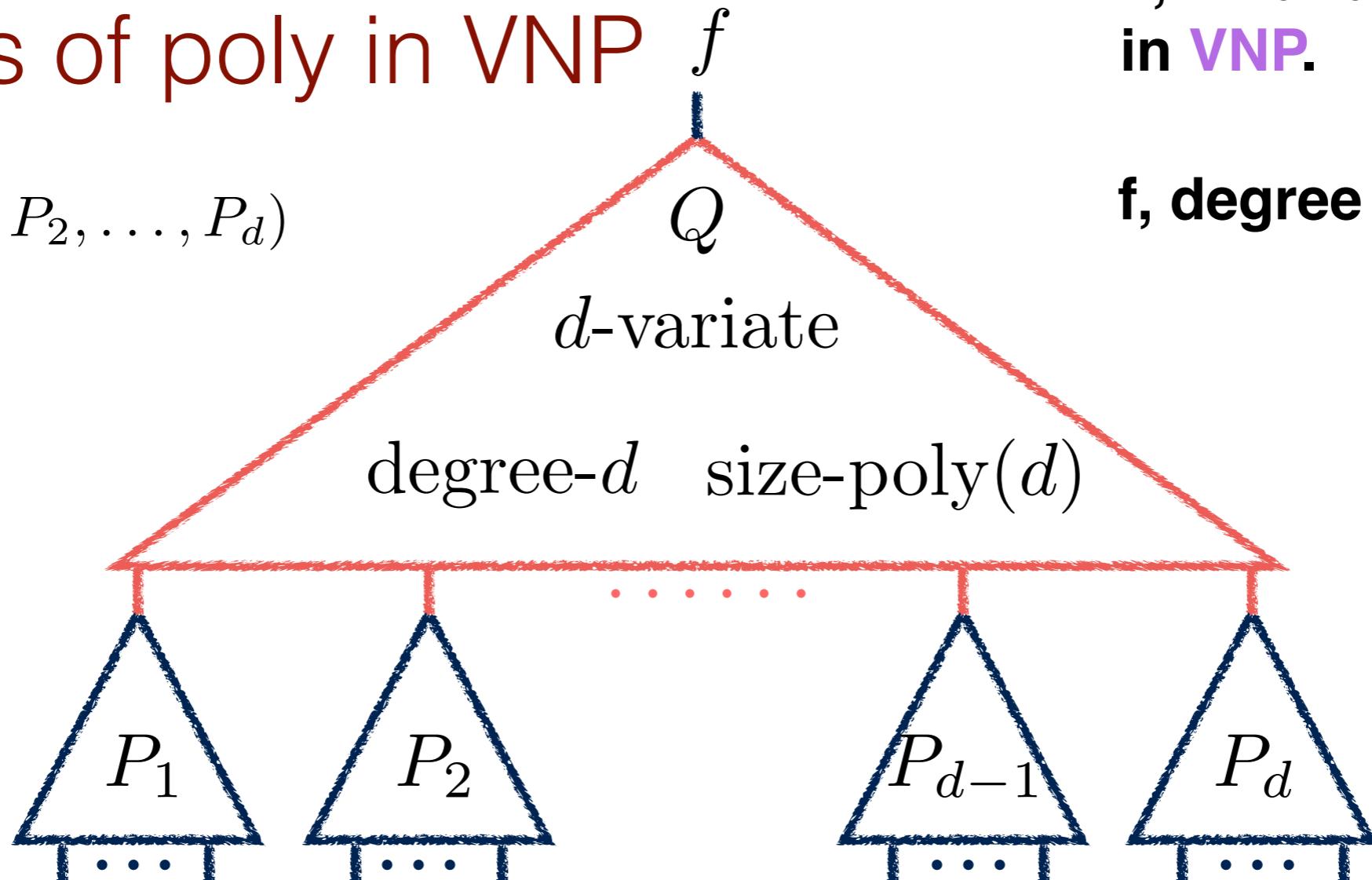
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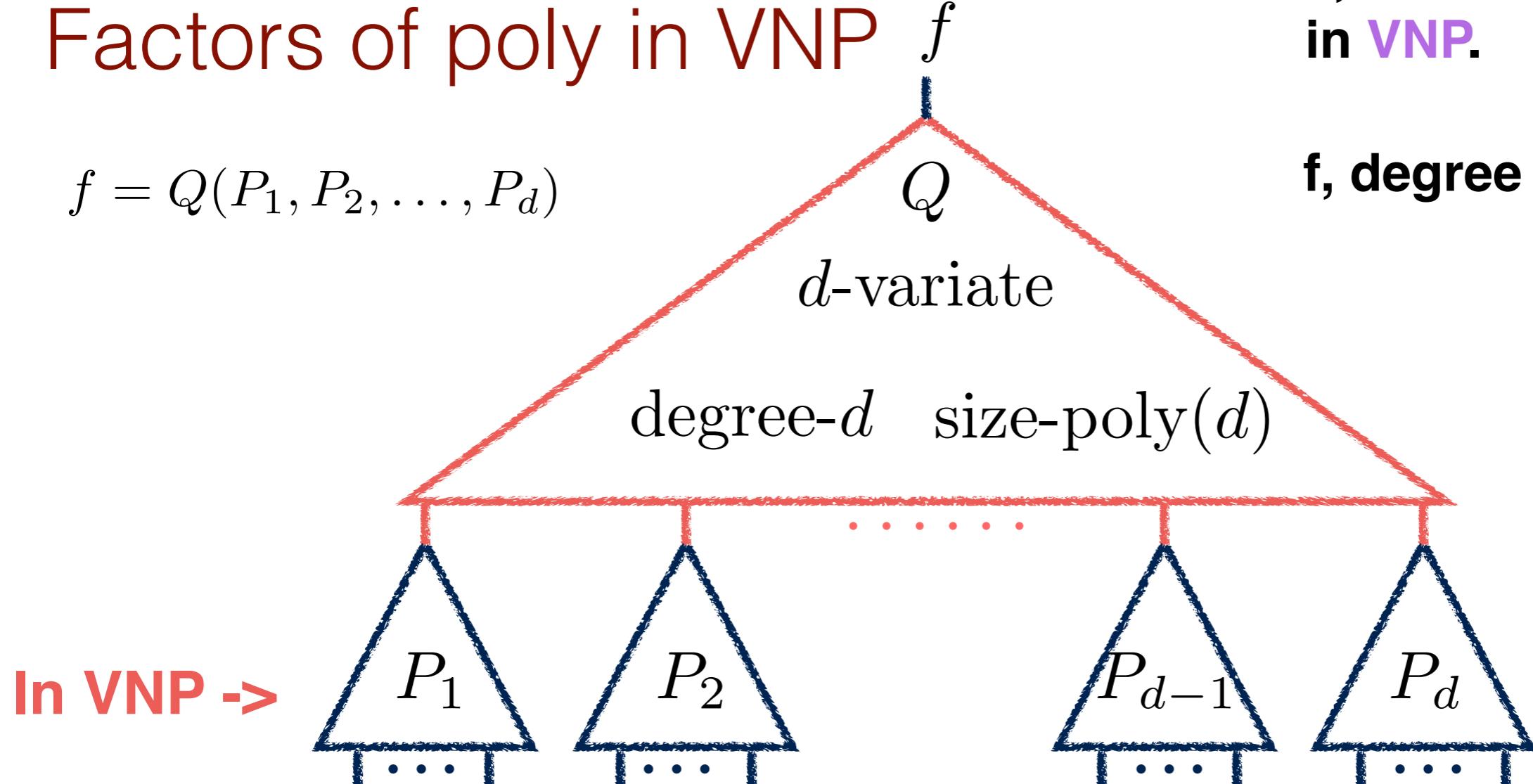


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## Theorem [Valiant]

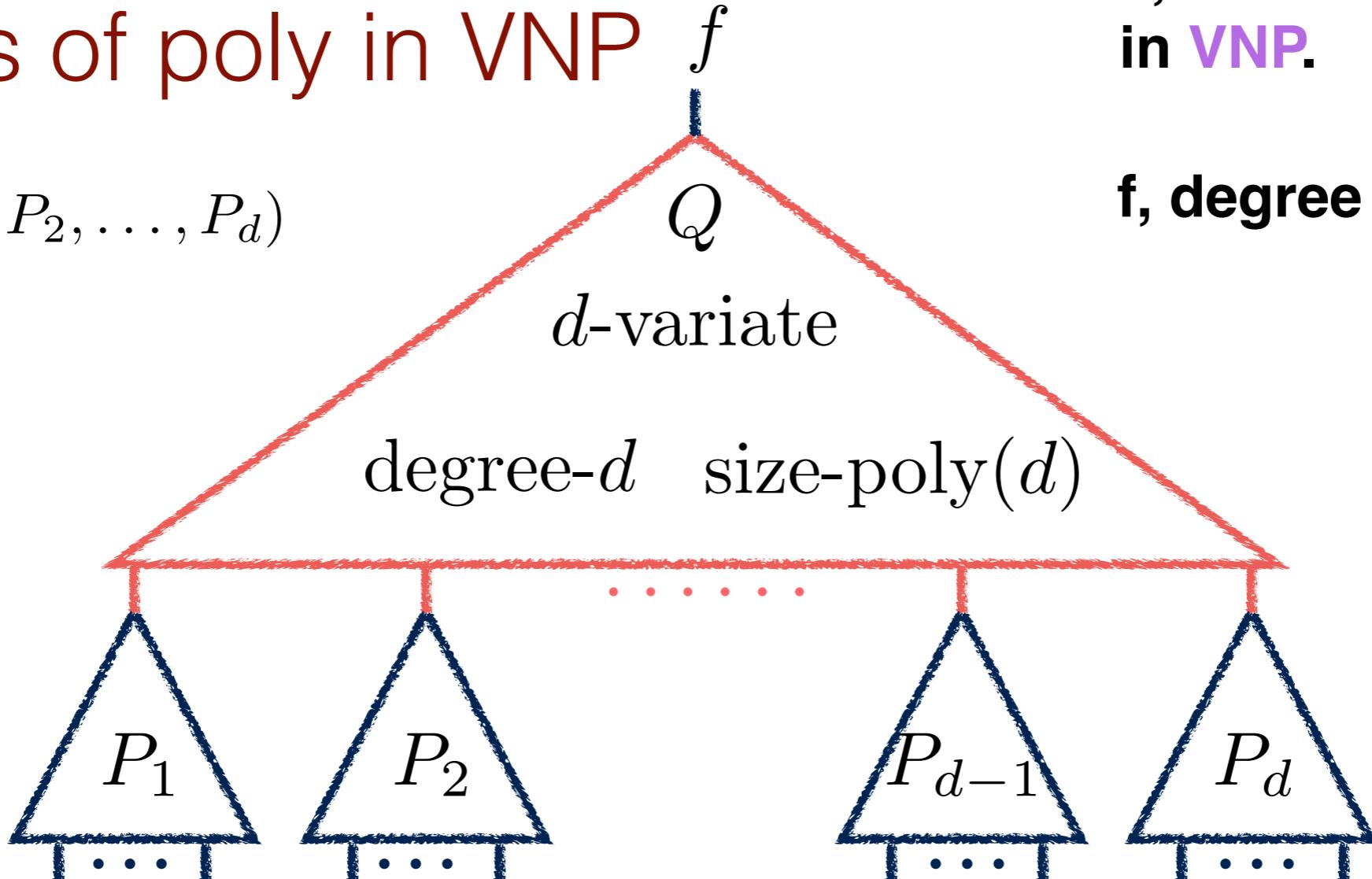
If each  $P_i$  is in VNP, then  $Q(P_1, P_2, \dots, P_d)$  is in VNP.

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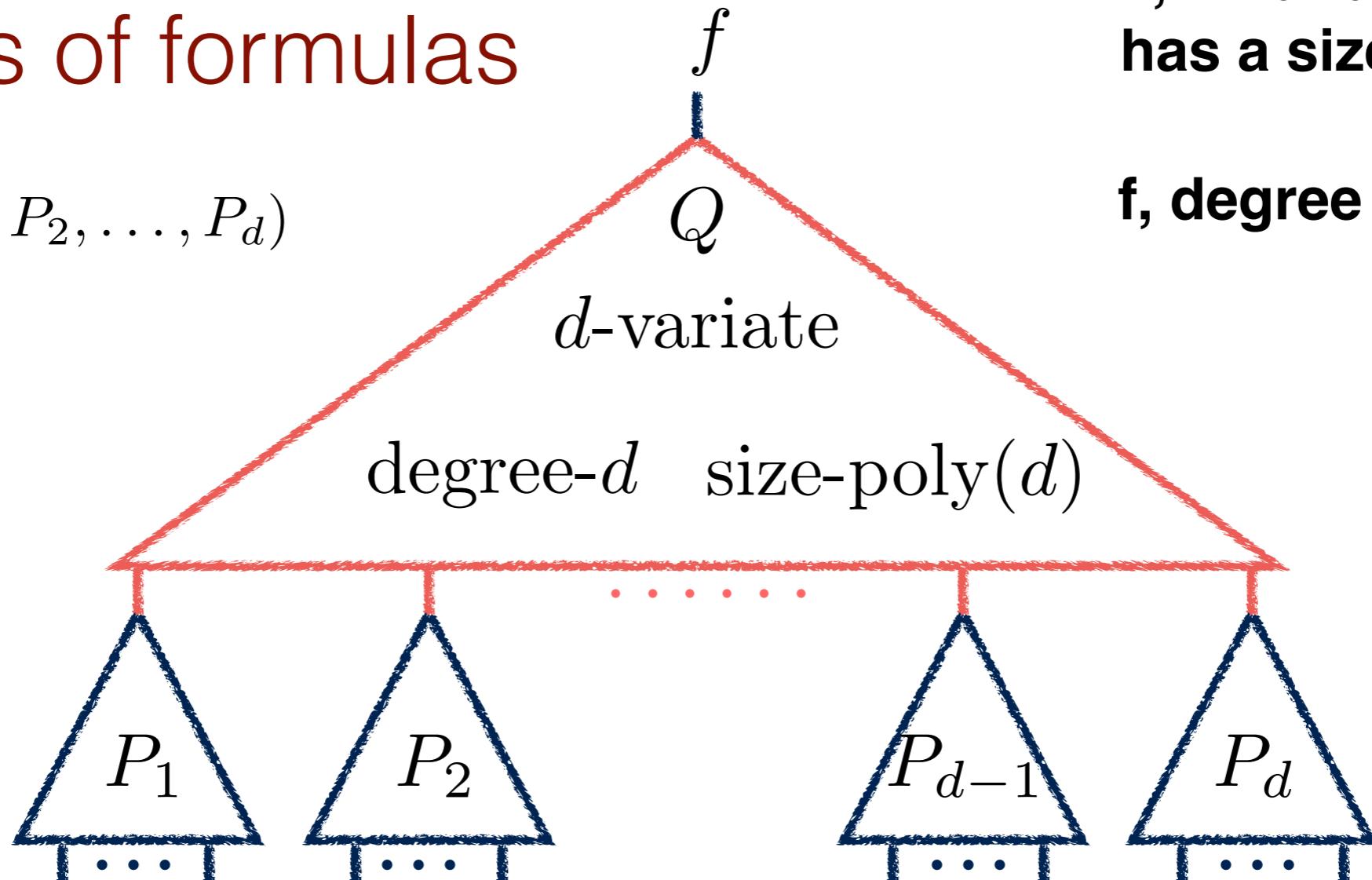
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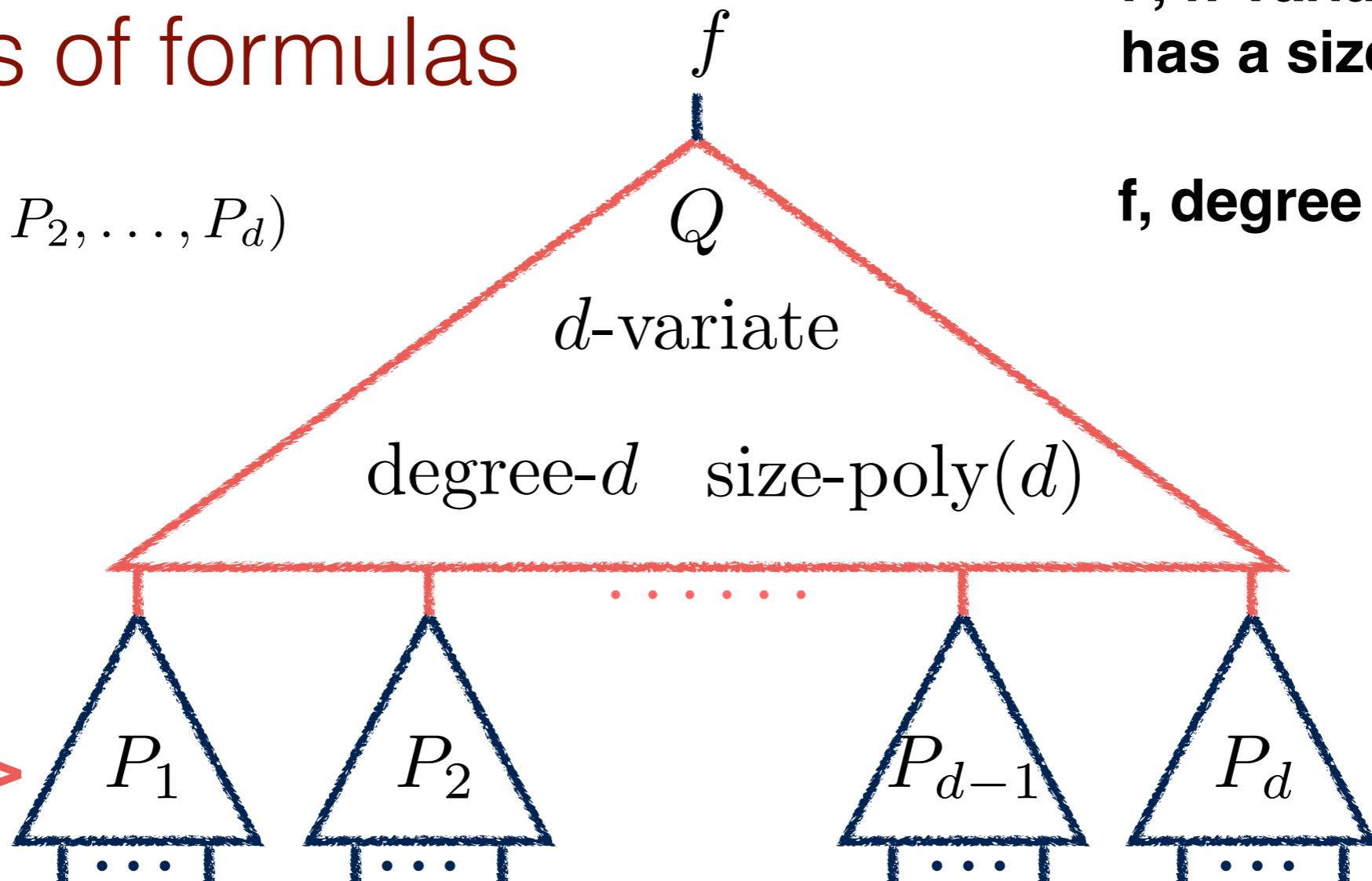


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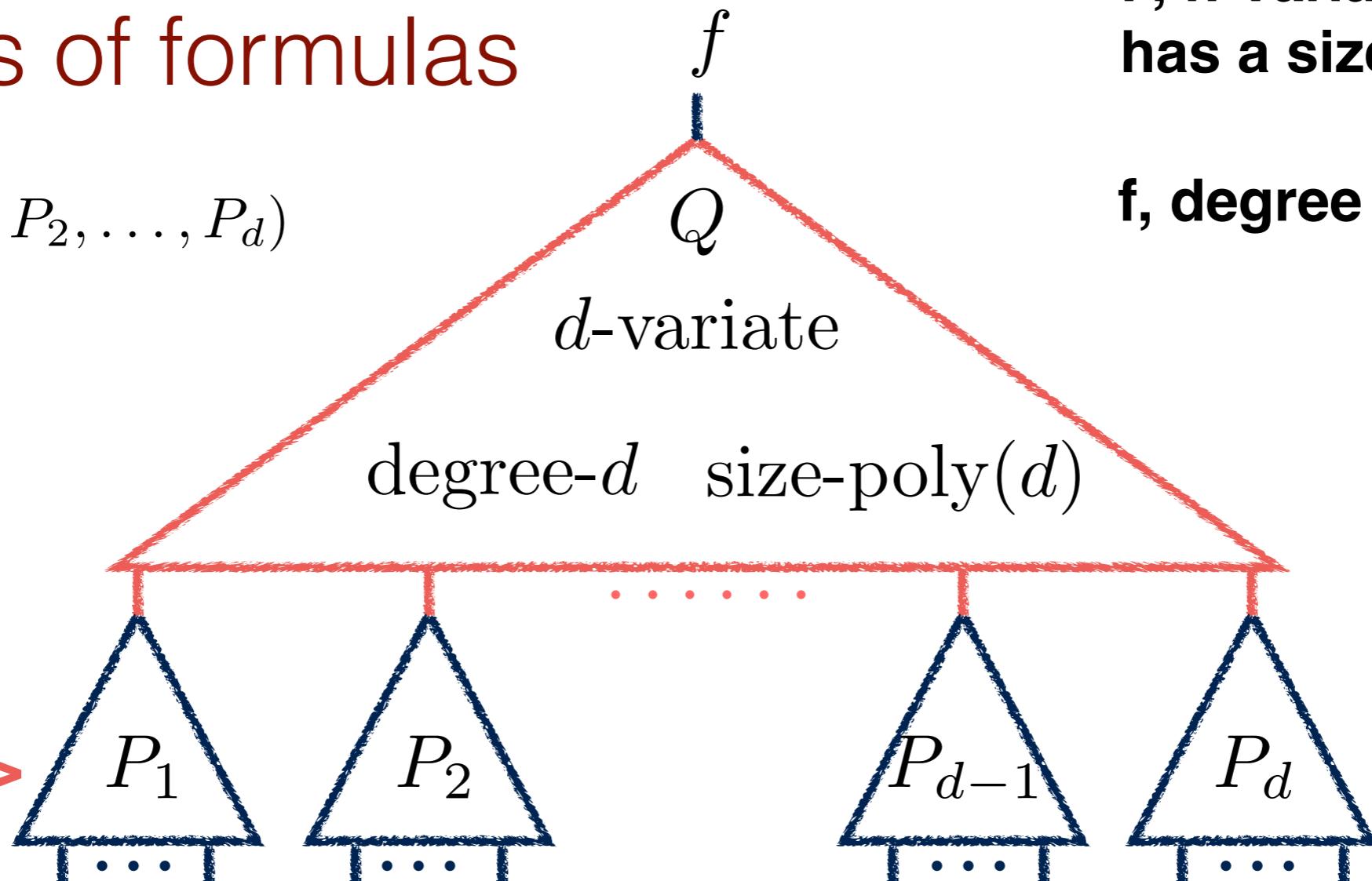


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Formulas ->

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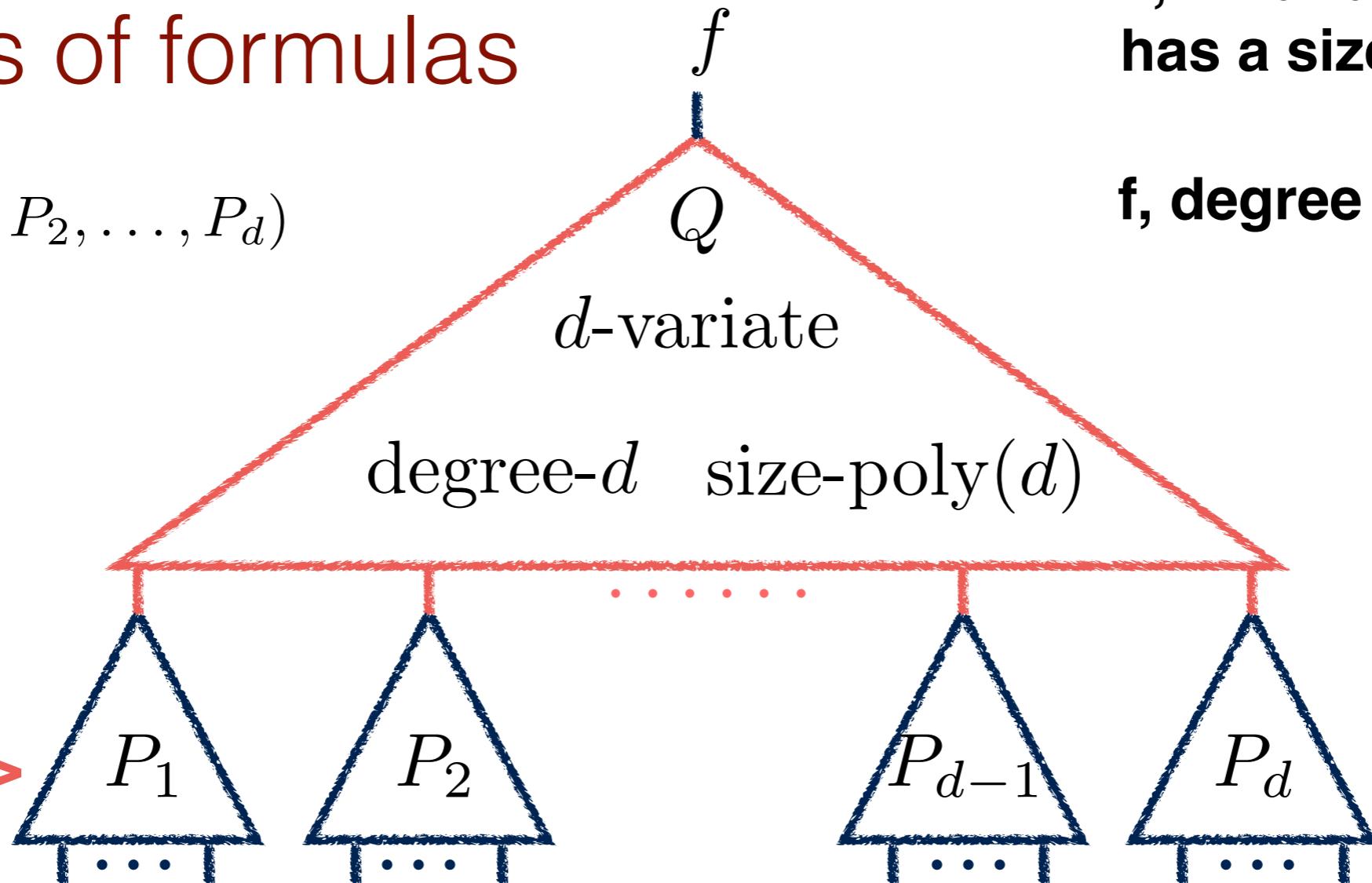
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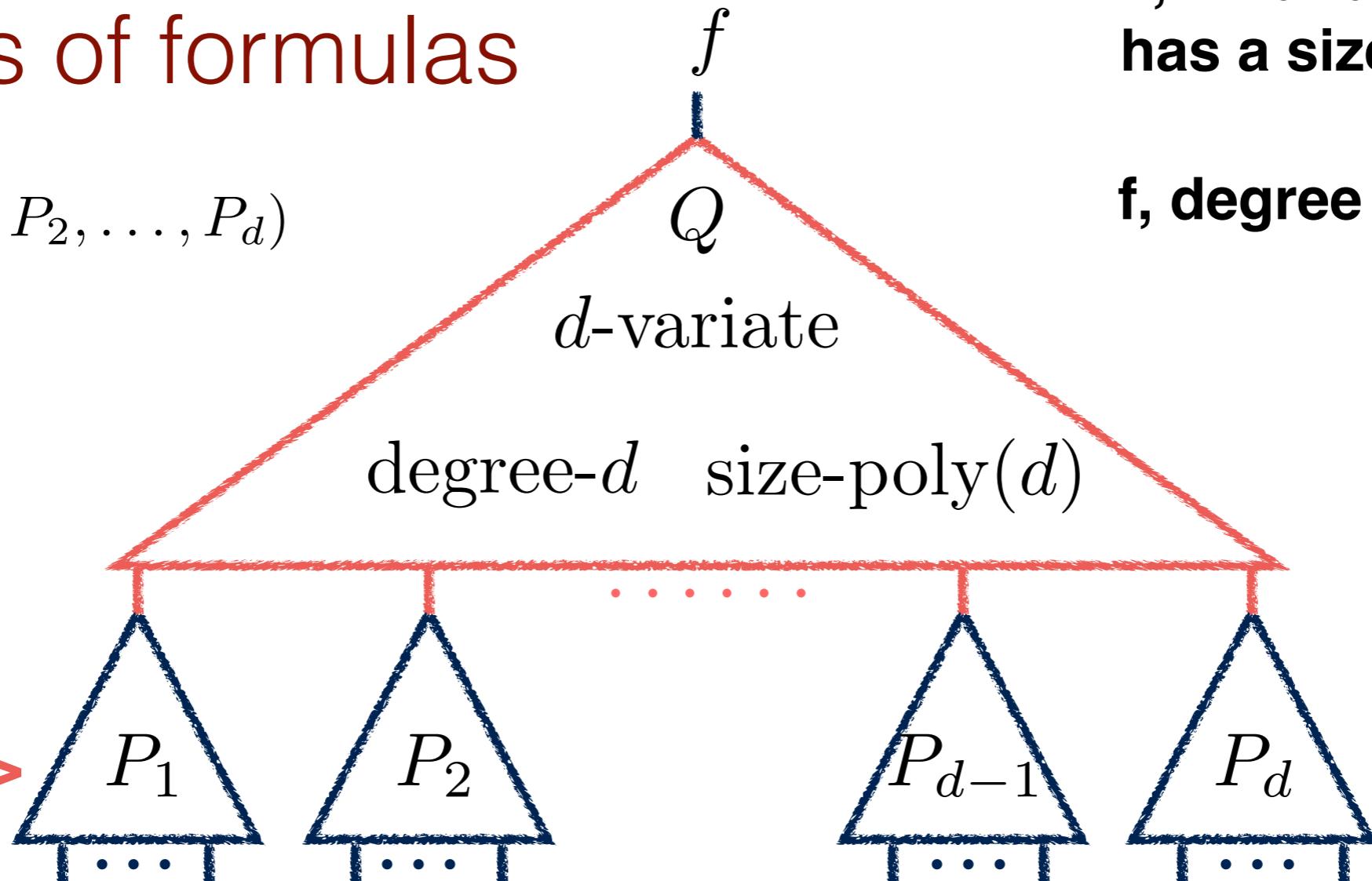
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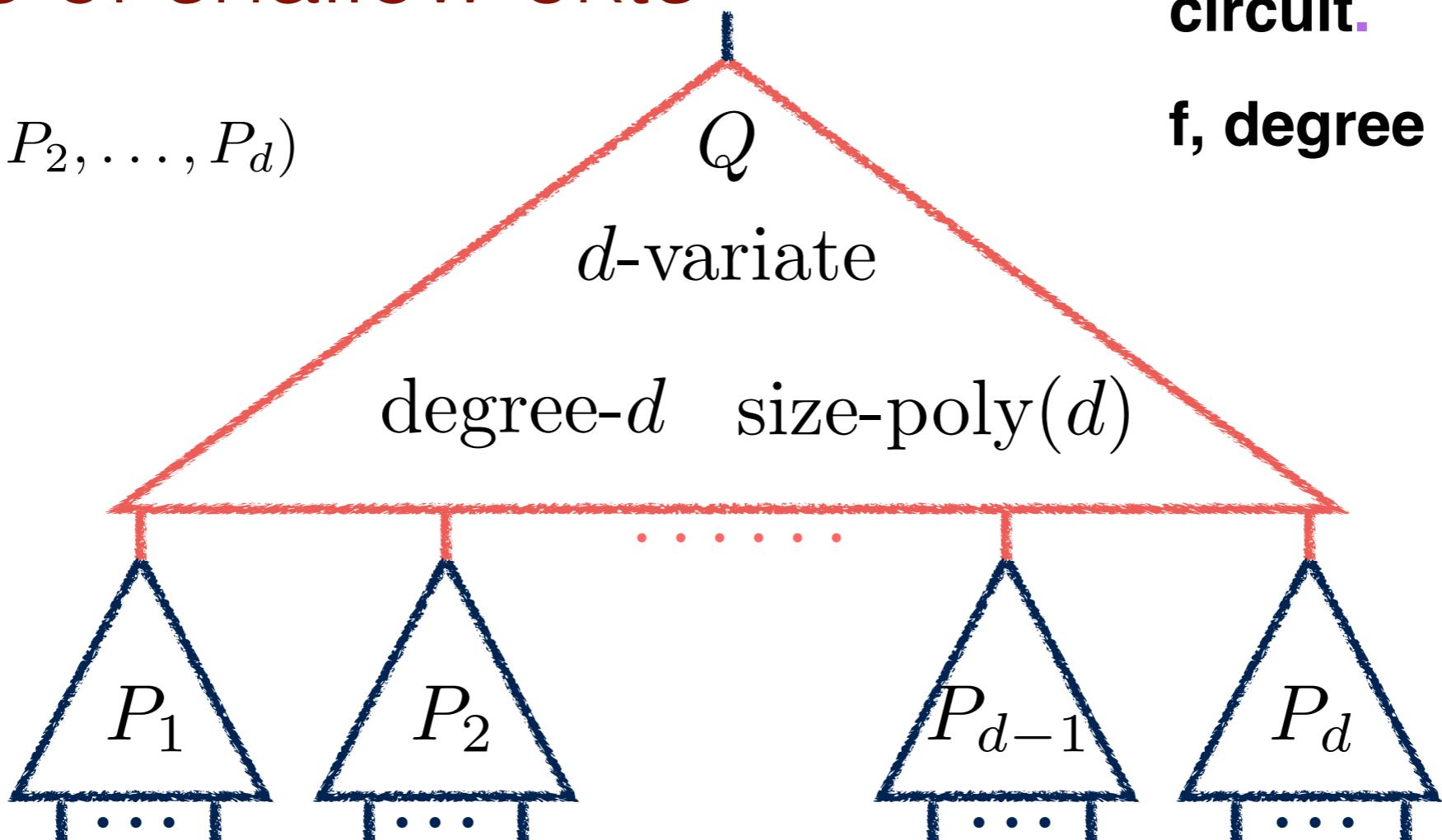
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Take the formula for Q , and paste a formula for each  $P_i$  at the leaves.

We get a formula for  $f$  of size  $d^{O(\log d)} \cdot \text{poly}(n, s, D)$ .  
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# Factors of shallow ckts $f$

$$f = Q(P_1, P_2, \dots, P_d)$$



$P$ , n-variate, degree  $D$   
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circuit.

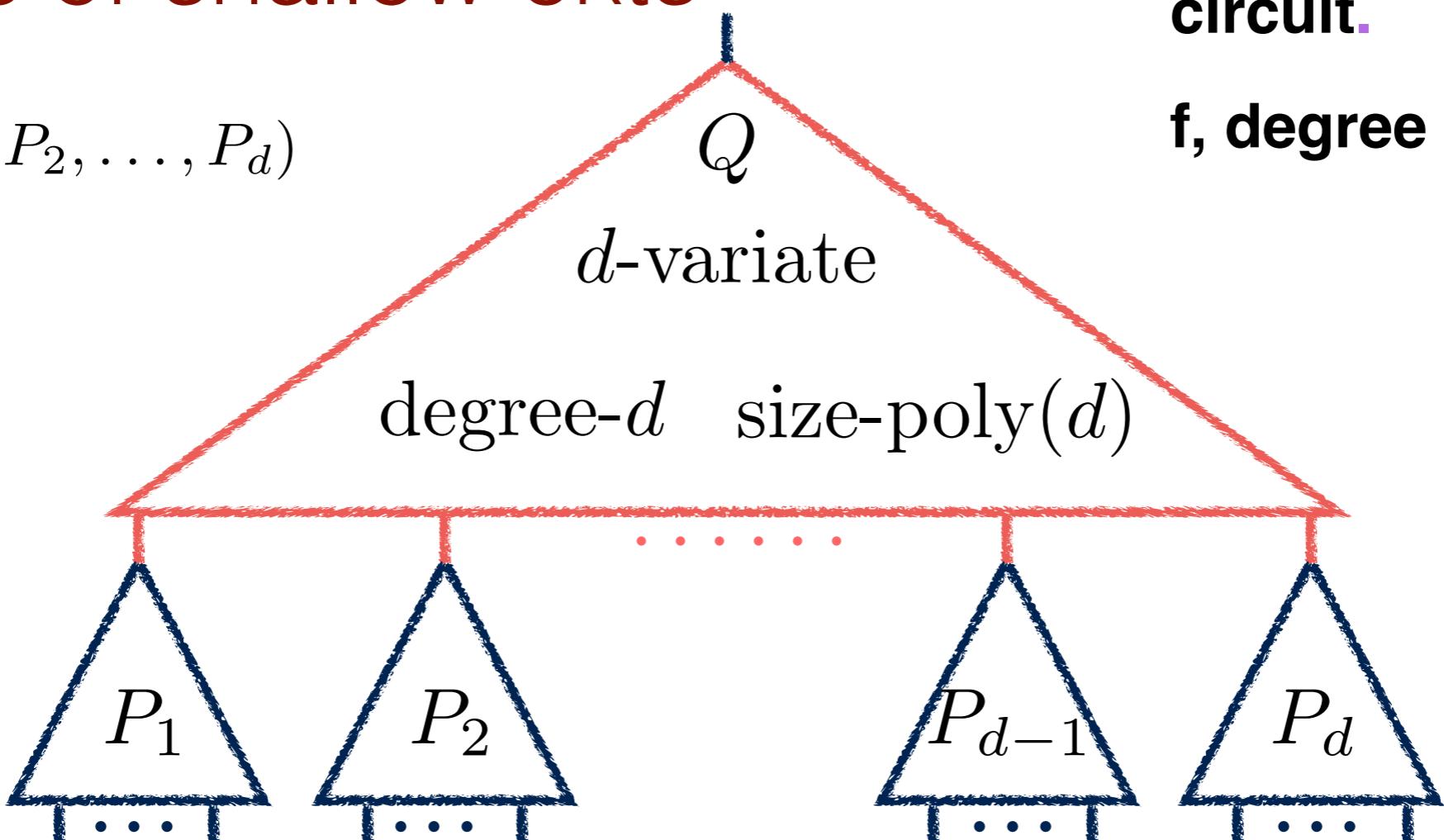
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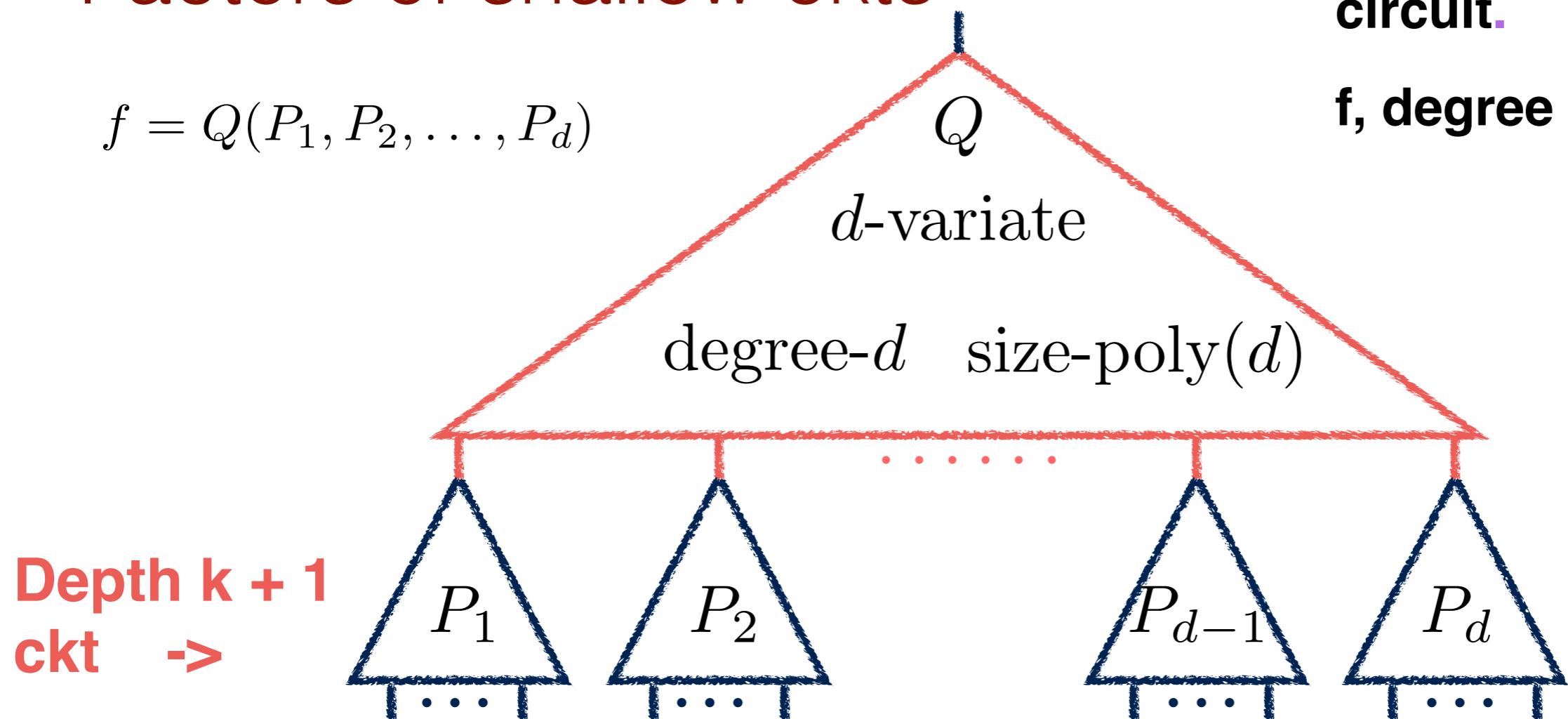


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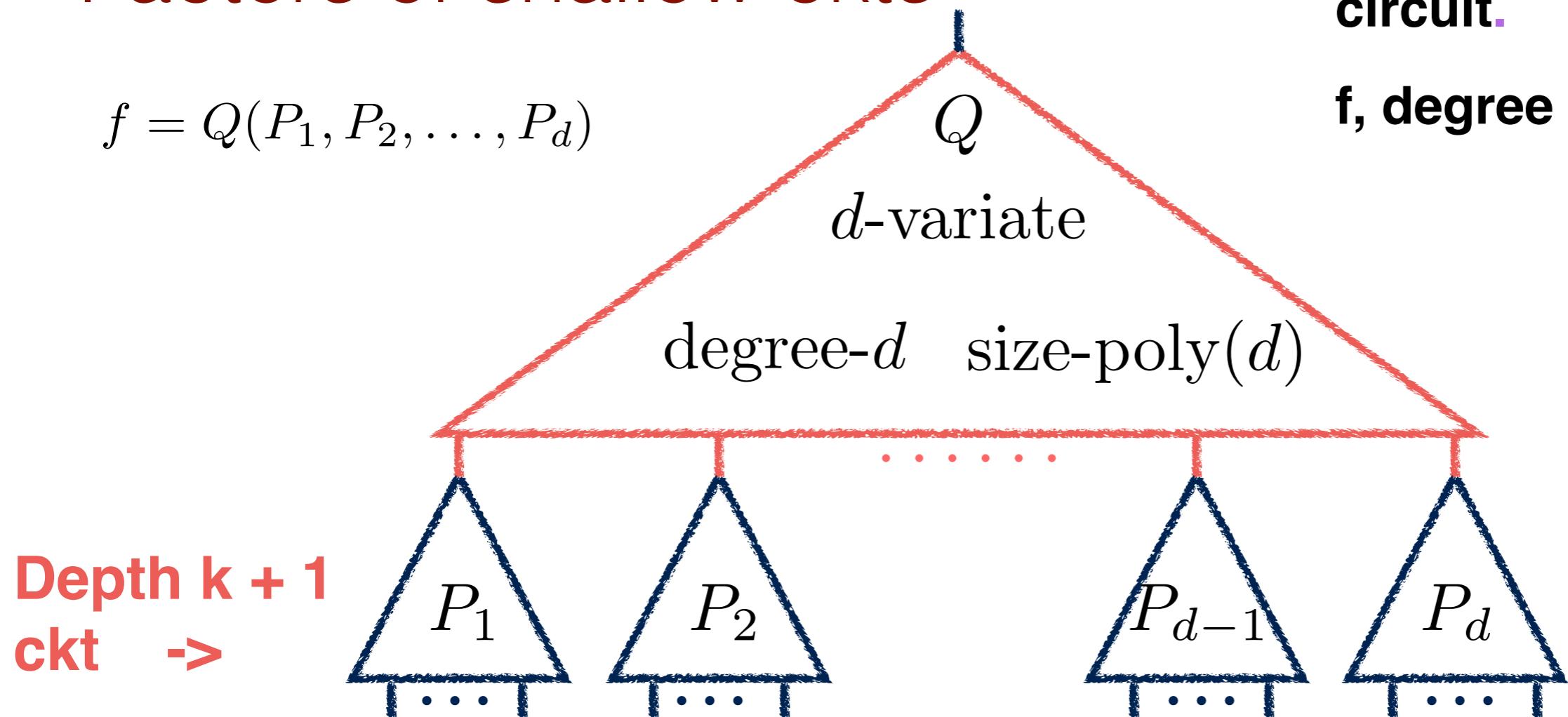
Q has a depth  $2c$  circuit of size  $d^{O(d^{1/c})}$ .

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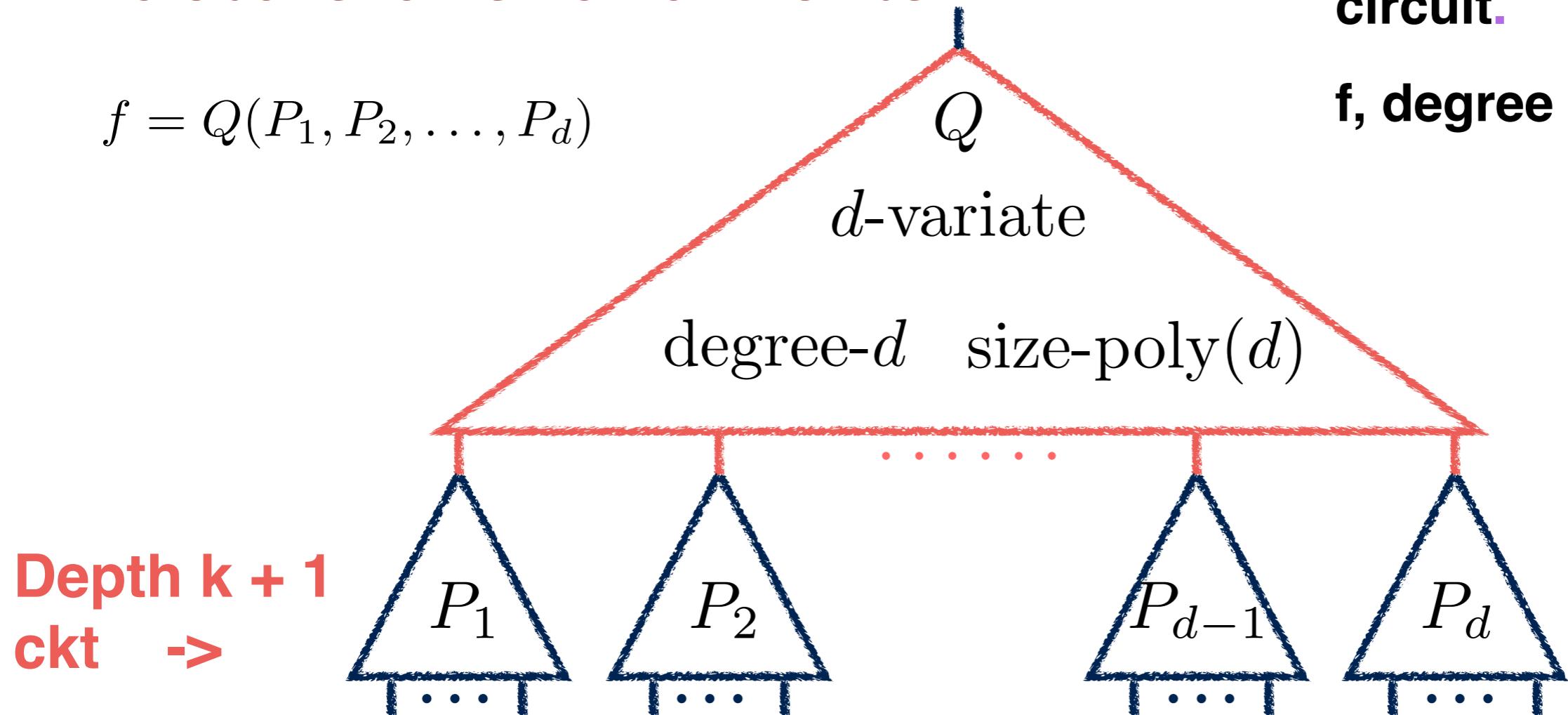
Take the shallow circuit for Q, and paste the shallow circuit for each  $P_i$  at the leaves.

# Factors of shallow ckts $f$

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$P$ ,  $n$ -variate, degree  $r$   
has a size  $s$ , depth- $k$  circuit.

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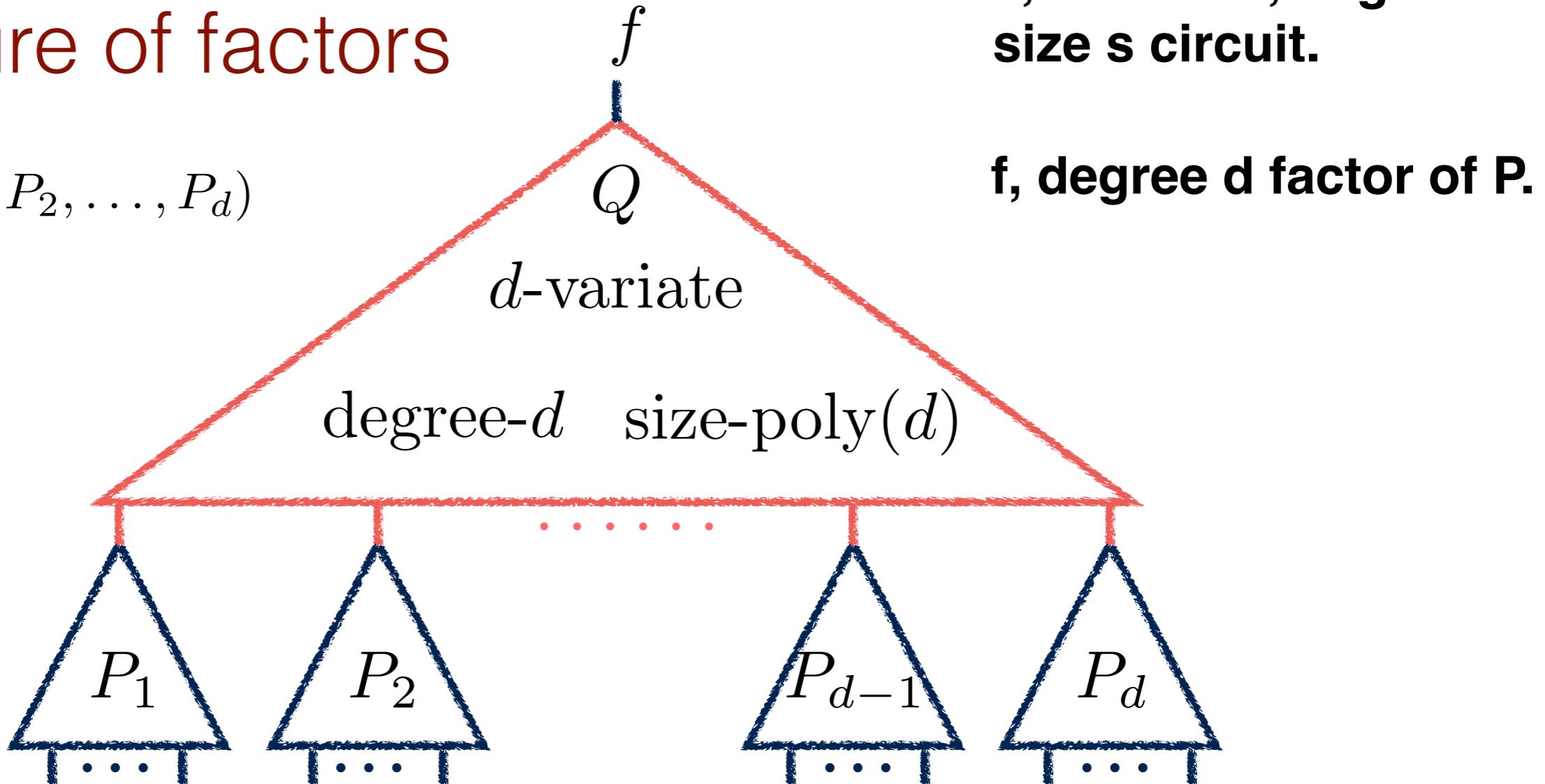
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We get a circuit for  $f$  of depth  $k + 2c + O(1)$  and size  $d^{O(d^\epsilon)} \cdot \text{poly}(n, s, D)$

# Proving the structural lemma

# Structure of factors

$$f = Q(P_1, P_2, \dots, P_d)$$



$$\text{size}(P_i) = \text{poly}(s, D)$$

structure preserved

low depth  $\rightarrow$  low depth  
formula  $\rightarrow$  formula  
VNP  $\rightarrow$  VNP

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Else, we translate the origin to ensure this.

# Defining the generators

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$$P(\mathbf{X}, f(\mathbf{0}) + Y) = P(\mathbf{X}, f(\mathbf{0})) + Y \frac{\partial P}{\partial Y}(\mathbf{X}, f(\mathbf{0})) + \cdots + Y^r \cdot \frac{1}{r!} \cdot \frac{\partial^r P}{\partial Y^r}(\mathbf{X}, f(\mathbf{0}))$$

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At the end of iteration  $i$ , we will be able to recover the homogeneous components of  $f$  of degree up to  $i$ .

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$L :=$  homogeneous component of  $f$  of degree equal to 1

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**Base case : Getting a circuit for the linear term**

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**So, we never look beyond the first d terms. f is a function of**

$$P_0, P_1, \dots, P_d$$

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- These can be addressed using some standard ideas  
(interpolation, homogenization etc.)

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- Low (but growing) degree factors of small formulas, low depth circuits have small formulas, low depth circuits respectively.
- Even somewhat non-trivial lower bounds for formulas, low depth circuits imply sub exponential time deterministic Identity Testing algorithms for them.

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Arithmetic formula lower bounds : Proving better than Quadratic lower bounds for arithmetic formula ? Resulting PIT applications ?

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**Thank You!**