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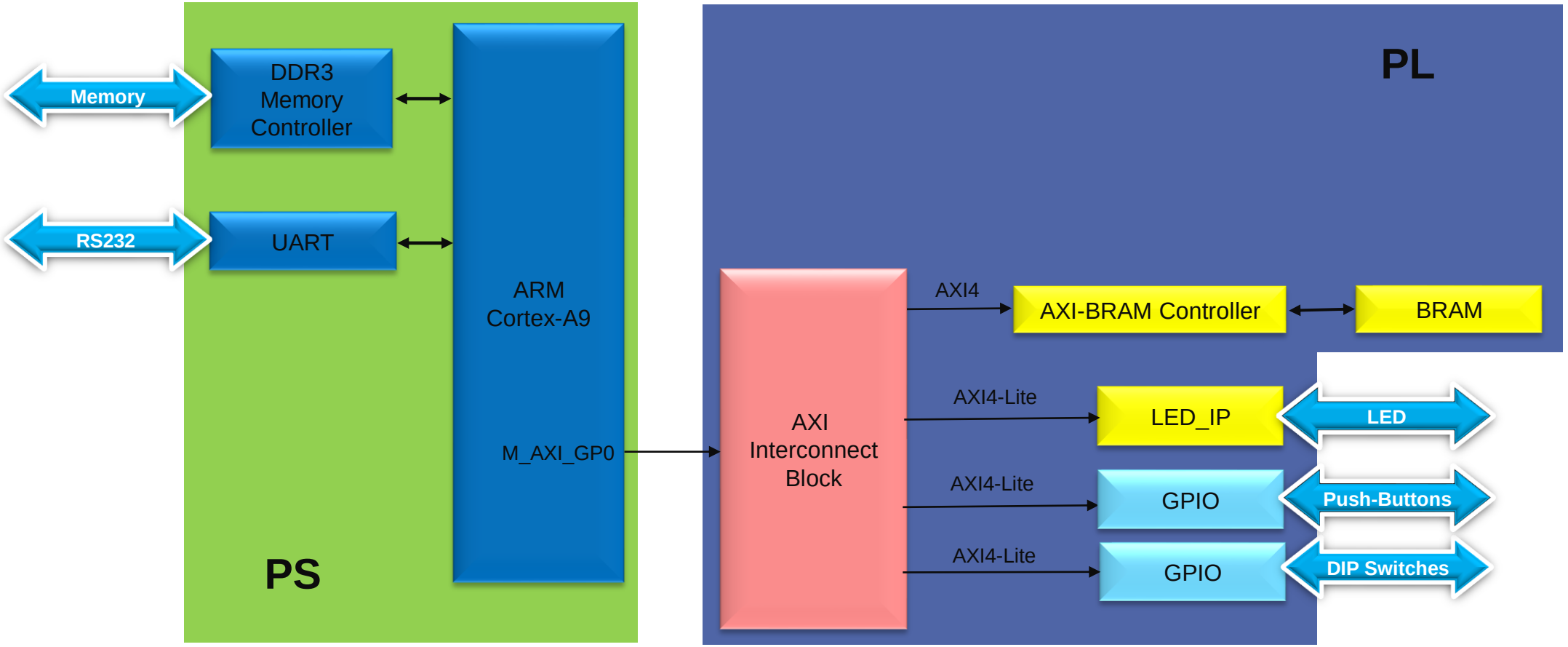
Lab3 Intro
Adding Custom IP in PL

2021.2

Introduction

- This lab guides you through the process of Creating and adding a custom IP.
- You will use the Create Import Peripheral Wizard to create the custom IP and use IP Packager to package it for use with IP Integrator.
- An AXI BRAM controller and BRAM will also be used.

ARM Cortex-A9 based Embedded System Design
Lab3: Adding Custom IP in PL



Procedure

- Open the project in Vivado
- Create/modify a Custom IP to create the peripheral functionality
- Package the IP using IP Packager
- Import and Add the peripheral into the existing system
- Add BRAM for the next lab and build the PL design

Summary

- A template for a peripheral was created using the Create and Package IP Wizard
- Logic was added to the templates to create a LED peripheral.
- The IP Packager was used to package the IP so that it could be imported into the IP catalog.
- The IP was imported and added to the design.
- The final step was to add a BRAM to the system and build the PL for the next lab

Thank You

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