```
/opt/home/gle/BeagleBoneBlue/bluebot/distance_sensor_pkg.h
#include
           "i2c_mux.h"
#include
           "VL53L1X.h"
#include
           "Arduino.h"
// I2C MUX port number for the 3 distance sensors
#define
                     0
            FRONT
#define
            RIGHT
                     5
#define
            LEFT
// i2c address for distance sensors
#define
           DIST_I2C_ADDR
                            0x29
extern VL53L1X front_sensor ;
extern VL53L1X left_sensor ;
extern VL53L1X right_sensor ;
void front_sensor_setup(void) ;
void left_sensor_setup(void) ;
void right_sensor_setup(void) ;
double distance_sensor(void) ;
```

Tue Mar 19 08:34:53 2019

```
/opt/home/gle/BeagleBoneBlue/bluebot/color sensor.h
// We will use I2C2 which is called 1 here (silly)
// SCL on P9_19 (3.3 V tolerant) I2C-2 (SCL)
// SDA on P9 20 (3.3 V tolerant) I2C-2 (SDA)
// When 1 prints some info for debugging
#define
                                          0
            COLOR_SENSOR_DEBUG
// #define
               COLOR_SENSOR_DEBUG
// i2c_scan 1 to test to see if device is there
#define
                COLOR_SENSOR_I2C_BUS
                                                  1
// Base address for the TCS34725 Color Sensor
#define
                COLOR_SENSOR_ADDR
                                           0x29
// Write buffer size
#define
                BUF_SIZE
                                 12
// Command bit
#define
            CMD_BIT
                             0 \times 80
// Color sensor registers
#define
            ENABLE
                             0 \times 00
#define
            ATIME
                             0x01
#define
            WTIME
                             0x03
#define
                             0x04
            AILTL
#define
            AILTH
                             0x05
#define
                             0x06
            AIHTL
#define
            AIHTH
                             0x07
#define
            PERS
                             0x0c
#define
            CONFIG
                             0x0d
#define
            CONTROL
                             0x0f
#define
            ID
                             0x12
#define
                             0x13
            STATUS
#define
                             0x14
            CDATA
#define
            CDATAH
                             0x15
#define
            RDATA
                             0x16
#define
            RDATAH
                             0x17
```

Wed Mar 13 18:00:03 2019

```
/opt/home/gle/BeagleBoneBlue/bluebot/color_sensor.h
                                                           Wed Mar 13 18:00:03 2019
                                                                                          2
#define
            GDATA
                            0x18
#define
            GDATAH
                            0x19
#define
            BDATA
                            0x1a
#define
            BDATAH
                            0x1b
// Gain settings
#define
                            0x00
            GAIN_1X
#define
            GAIN_4X
                            0x01
#define
            GAIN_16X
                            0x02
#define
            GAIN_60X
                            0x03
// Integration time (154 ms)
#define
           INTEG TIME
                            0xc0
// Function declaration
// Dumps raw data from the sensor
      init_color_sensor(void) ;
void
       cleanup_color_sensor(void) ;
void
      read_color_sensor(unsigned *c, unsigned int *r, unsigned int *g, unsigned int *b);
void
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/distance_sensor_pkg.c
                                                              Tue Mar 19 08:39:02 2019
/*
This example shows how to take simple range measurements with the VL53L1X. The
range readings are in units of mm.
#include
           <stdio.h>
#include
           <stdint.h>
#include
           "VL53L1X.h"
#include
           "Arduino.h"
#include
           <robotcontrol.h>
#include
           "i2c mux.h"
#include
           "distance sensor pkq.h"
// ************
// Front sensor setup
// ^^^^^^^
void front_sensor_setup(void) {
// Set i2c mux to FRONT port
 rc i2c_set_device_address(I2C_BUS, MUX_I2C_ADDR) ;
 disableMuxPort(ALL PORTS) ;
 enableMuxPort(FRONT) ;
// Now set i2c address to 0x29
 rc_i2c_set_device_address(I2C_BUS, DIST_I2C_ADDR);
// Sensor config
 front sensor.setTimeout(500);
 if (!front_sensor.init()) {
    printf("Failed to detect and initialize sensor!");
    while (1) {
        if (rc_get_state() == EXITING) break ;
 // Use long distance mode and allow up to 50000 us (50 ms) for a measurement.
 // You can change these settings to adjust the performance of the sensor, but
 // the minimum timing budget is 20 ms for short distance mode and 33 ms for
 // medium and long distance modes. See the VL53L1X datasheet for more
```

```
// information on range and timing limits.
 front sensor.setDistanceMode(VL53L1X::Long);
 front_sensor.setMeasurementTimingBudget(50000);
 // Start continuous readings at a rate of one measurement every 50 ms (the
 // inter-measurement period). This period should be at least as long as the
 // timing budget.
 front sensor.startContinuous(50);
// ************
// Left sensor setup
// ^^^^^
void left_sensor_setup(void) {
// Set i2c mux to LEFT port
 rc i2c set device address(I2C BUS, MUX I2C ADDR);
 disableMuxPort(ALL PORTS) ;
 enableMuxPort(LEFT) ;
// Now set i2c address to 0x29
 rc_i2c_set_device_address(I2C_BUS, DIST_I2C_ADDR);
// Sensor config
 left sensor.setTimeout(500);
 if (!left sensor.init()) {
    printf("Failed to detect and initialize sensor!");
    while (1) {
        if (rc get state() == EXITING) break ;
 // Use long distance mode and allow up to 50000 us (50 ms) for a measurement.
 // You can change these settings to adjust the performance of the sensor, but
 // the minimum timing budget is 20 ms for short distance mode and 33 ms for
 // medium and long distance modes. See the VL53L1X datasheet for more
 // information on range and timing limits.
```

```
left_sensor.setDistanceMode(VL53L1X::Long);
 left sensor.setMeasurementTimingBudget(50000);
 // Start continuous readings at a rate of one measurement every 50 ms (the
 // inter-measurement period). This period should be at least as long as the
 // timing budget.
 left_sensor.startContinuous(50);
// ^^^^^
// Right sensor setup
void right sensor setup(void) {
// Set i2c mux to RIGHT port
 rc_i2c_set_device_address(I2C_BUS, MUX_I2C_ADDR);
 disableMuxPort(ALL PORTS) ;
 enableMuxPort(RIGHT) ;
// Now set i2c address to 0x29
 rc_i2c_set_device_address(I2C_BUS, DIST_I2C_ADDR);
// Sensor config
 right_sensor.setTimeout(500);
 if (!right sensor.init()) {
    printf("Failed to detect and initialize sensor!");
    while (1) {
        if (rc get state() == EXITING) break ;
 // Use long distance mode and allow up to 50000 us (50 ms) for a measurement.
 // You can change these settings to adjust the performance of the sensor, but
 // the minimum timing budget is 20 ms for short distance mode and 33 ms for
 // medium and long distance modes. See the VL53L1X datasheet for more
 // information on range and timing limits.
 right_sensor.setDistanceMode(VL53L1X::Long);
 right sensor.setMeasurementTimingBudget(50000);
```

```
// Start continuous readings at a rate of one measurement every 50 ms (the
 // inter-measurement period). This period should be at least as long as the
 // timing budget.
 right sensor.startContinuous(50);
// ^^^^^^^
   Test the distance sensor
// ********************************
double distance sensor(void) {
  double inches;
// I2C bus will get initialized
 rc_i2c_init(MUX_I2C_BUS, MUX_I2C_ADDR);
// Sensor setup
  front sensor setup();
// right sensor setup();
// left_sensor_setup() ;
// Set i2c mux to RIGHT port
 rc_i2c_set_device_address(I2C_BUS, MUX_I2C_ADDR) ;
 disableMuxPort(ALL PORTS) ;
 enableMuxPort(FRONT) ;
// We want talk to the distance sensor
  rc_i2c_set_device_address(I2C_BUS, DIST_I2C_ADDR);
  int i;
  i = 0;
  while(1)
      if (rc_get_state() == EXITING) break ;
      inches = 0.03937 * right_sensor.read();
      i += 1 ;
      if (front_sensor.timeoutOccurred()) printf("TIMEOUT\n");
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/distance_sensor_pkg.c
        if (i == 5) break ;
    }
    front_sensor.stopContinuous();

// Disable all the MUX ports
    rc_i2c_set_device_address(MUX_I2C_BUS, MUX_I2C_ADDR) ;
    disableMuxPort(ALL_PORTS) ;

// Close the i2c channel
    rc_i2c_close(MUX_I2C_BUS) ;
    return inches ;
} // end main
```

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```
/opt/home/gle/BeagleBoneBlue/bluebot/servo_pkg.h
                                                       Tue Mar 19 15:45:59 2019
// Servo package
#include
           <stdio.h>
#include
           <getopt.h>
#include
            <stdlib.h>
#include
           <robotcontrol.h>
// Servo channel number
         DISTANCE_SENSOR_SERVO_CHANNEL
#define
                                          0
// Slope is in usec / degree
         DISTANCE_SERVO_SLOPE
#define
                                          10
         DISTANCE_SERVO_OFFSET
#define
                                          1500
void servo_setup(void) ;
void servo_cleanup(void);
void distance_sensor_servo(int angle) ;
void sweep_distance_sensor(void);
```

writeReg(PAD_I2C_HV__EXTSUP_CONFIG,

readReg(PAD_I2C_HV__EXTSUP_CONFIG) | 0x01);

```
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.c Thu Mar 14 10:44:41 2019
 // store oscillator info for later use
 fast_osc_frequency = readReg16Bit(OSC_MEASURED__FAST_OSC__FREQUENCY);
 osc_calibrate_val = readReg16Bit(RESULT__OSC_CALIBRATE_VAL);
 // VL53L1 DataInit() end
 // VL53L1 StaticInit() begin
 // Note that the API does not actually apply the configuration settings below
 // when VL53L1 StaticInit() is called: it keeps a copy of the sensor's
 // register contents in memory and doesn't actually write them until a
 // measurement is started. Writing the configuration here means we don't have
 // to keep it all in memory and avoids a lot of redundant writes later.
 // the API sets the preset mode to LOWPOWER_AUTONOMOUS here:
 // VL53L1 set preset mode() begin
 // VL53L1 preset mode standard ranging() begin
 // values labeled "tuning parm default" are from v15311_tuning_parm_defaults.h
 // (API uses these in VL53L1 init tuning parm storage struct())
 // static config
 // API resets PAD_I2C_HV__EXTSUP_CONFIG here, but maybe we don't want to do
 // that? (seems like it would disable 2V8 mode)
 writeReg16Bit(DSS_CONFIG__TARGET_TOTAL_RATE_MCPS, TargetRate); // should already be this value after reset
 writeReg(GPIO__TIO_HV_STATUS, 0x02);
 writeReq(SIGMA ESTIMATOR EFFECTIVE PULSE WIDTH NS, 8); // tuning parm default
 writeReg(SIGMA_ESTIMATOR__EFFECTIVE_AMBIENT_WIDTH_NS, 16); // tuning parm default
 writeReg(ALGO CROSSTALK COMPENSATION VALID HEIGHT MM, 0x01);
 writeReq(ALGO RANGE_IGNORE_VALID_HEIGHT_MM, 0xFF);
 writeReg(ALGO__RANGE_MIN_CLIP, 0); // tuning parm default
 writeReg(ALGO CONSISTENCY CHECK TOLERANCE, 2); // tuning parm default
 // general config
 writeReg16Bit(SYSTEM THRESH RATE HIGH, 0x0000);
 writeReg16Bit(SYSTEM THRESH RATE LOW, 0x0000);
 writeReg(DSS_CONFIG__APERTURE_ATTENUATION, 0x38);
 // timing config
 // most of these settings will be determined later by distance and timing
 // budget configuration
```

```
Thu Mar 14 10:44:41 2019
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.c
  writeReg16Bit(RANGE_CONFIG__SIGMA_THRESH, 360); // tuning parm default
 writeReg16Bit(RANGE CONFIG MIN COUNT RATE RTN LIMIT MCPS, 192); // tuning parm default
 // dynamic config
 writeReg(SYSTEM GROUPED PARAMETER HOLD 0, 0x01);
 writeReg(SYSTEM__GROUPED_PARAMETER_HOLD_1, 0x01);
 writeReg(SD_CONFIG__QUANTIFIER, 2); // tuning parm default
 // VL53L1 preset mode standard ranging() end
 // from VL53L1 preset mode timed ranging *
 // GPH is 0 after reset, but writing GPH0 and GPH1 above seem to set GPH to 1,
 // and things don't seem to work if we don't set GPH back to 0 (which the API
 // does here).
 writeReq(SYSTEM__GROUPED_PARAMETER_HOLD, 0x00);
 writeReg(SYSTEM__SEED_CONFIG, 1); // tuning parm default
  // from VL53L1_config_low_power_auto_mode
 writeReg(SYSTEM SEOUENCE CONFIG, 0x8B); // VHV, PHASECAL, DSS1, RANGE
 writeReq16Bit(DSS CONFIG MANUAL EFFECTIVE SPADS SELECT, 200 << 8);</pre>
 writeReq(DSS CONFIG ROI MODE CONTROL, 2); // REQUESTED EFFFECTIVE SPADS
 // VL53L1 set preset mode() end
  // default to long range, 50 ms timing budget
  // note that this is different than what the API defaults to
  setDistanceMode(Long);
  setMeasurementTimingBudget(50000);
 // VL53L1 StaticInit() end
 // the API triggers this change in VL53L1 init and start range() once a
  // measurement is started; assumes MM1 and MM2 are disabled
 writeReq16Bit(ALGO PART TO PART RANGE OFFSET MM,
    readReg16Bit(MM CONFIG OUTER OFFSET MM) * 4);
 return true;
// Write an 8-bit register
void VL53L1X::writeReg(uint16 t reg, uint8 t value)
  uint8 t buf[3];
```

```
buf[0] = (uint8 t) ((req >> 8) & 0xFF) ;
  buf[1] = (uint8 t) (reg & 0xFF);
  buf[2] = value ;
  rc_i2c_send_bytes(I2C_BUS, 3, buf);
  return ;
 Wire.beginTransmission(address);
 Wire.write((reg >> 8) & 0xFF); // reg high byte
 Wire.write( req
                  & 0xFF); // reg low byte
 Wire.write(value);
 last status = Wire.endTransmission();
// Write a 16-bit register
void VL53L1X::writeReq16Bit(uint16 t req, uint16 t value)
  uint8 t buf[4];
  buf[0] = (uint8_t) ((reg >> 8) & 0xFF) ;
  buf[1] = (uint8 t) (req & 0xFF);
  buf[2] = (uint8 t) ((value >> 8) & 0xFF);
  buf[3] = (uint8_t) (value & 0xFF) ;
  rc_i2c_send_bytes(I2C_BUS, 4, buf);
  return ;
 Wire.beginTransmission(address);
 Wire.write((reg >> 8) & 0xFF); // reg high byte
 Wire.write((value >> 8) & 0xFF); // value high byte
 last_status = Wire.endTransmission();
// Write a 32-bit register
void VL53L1X::writeReq32Bit(uint16 t req, uint32 t value)
  uint8 t buf[6];
  buf[0] = (uint8_t) ((reg >> 8) & 0xFF) ;
  buf[1] = (uint8 t) (req & 0xFF);
```

```
Thu Mar 14 10:44:41 2019
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.c
  buf[2] = (uint8 t) ((value >> 24) & 0xFF) ;
  buf[3] = (uint8 t) ((value >> 16) & 0xFF);
  buf[4] = (uint8 t) ((value >> 8) & 0xFF);
  buf[5] = (uint8_t) (value & 0xFF);
  rc_i2c_send_bytes(I2C_BUS, 6, buf);
  return ;
 Wire.beginTransmission(address);
 Wire.write((reg >> 8) & 0xFF); // reg high byte
 Wire.write((value >> 24) & 0xFF); // value highest byte
 Wire.write((value >> 16) & 0xFF);
 Wire.write((value >> 8) & 0xFF);
 Wire.write( value
                       & 0xFF); // value lowest byte
 last status = Wire.endTransmission();
// Read an 8-bit register
uint8 t VL53L1X::readReg(regAddr reg)
  uint8 t buf[2];
  uint8 t value;
// Send the 16-bit register address
  buf[0] = (uint8_t) ((reg >> 8) & 0xFF) ;
  buf[1] = (uint8_t) (reg & 0xFF);
  rc i2c send bytes(I2C BUS, 2, buf);
// Read the byte
  wire_read_bytes(I2C_BUS, 1, &value) ;
  return value;
 Wire.beginTransmission(address);
 Wire.write((reg >> 8) & 0xFF); // reg high byte
 Wire.write( req
                   & 0xFF); // reg low byte
 last status = Wire.endTransmission();
 Wire.requestFrom(address, (uint8_t)1);
 value = Wire.read();
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.c
                                                  Thu Mar 14 10:44:41 2019
* /
// Read a 16-bit register
uint16 t VL53L1X::readReg16Bit(uint16 t reg)
 uint16_t value;
 uint8_t buf[2];
// Send the 16-bit register address
  buf[0] = (uint8_t) ((reg >> 8) & 0xFF) ;
  buf[1] = (uint8_t) (reg \& 0xFF);
  rc i2c send bytes(I2C BUS, 2, buf);
// Read two bytes
  wire_read_bytes(I2C_BUS, 2, buf);
  value = (buf[0] << 8);</pre>
  value |= buf[1];
  return value;
 Wire.beginTransmission(address);
 Wire.write((reg >> 8) & 0xFF); // reg high byte
 last_status = Wire.endTransmission();
 Wire.requestFrom(address, (uint8 t)2);
 value = (uint16_t)Wire.read() << 8; // value high byte</pre>
 value |=
             Wire.read();  // value low byte
// Read a 32-bit register
uint32_t VL53L1X::readReg32Bit(uint16_t reg)
 uint32_t value;
 uint8_t buf[4];
// Send the 16-bit register address
```

// from VL53L1_preset_mode_standard_ranging_short_range()

writeReg(RANGE_CONFIG__VCSEL_PERIOD_A, 0x07);
writeReg(RANGE_CONFIG__VCSEL_PERIOD_B, 0x05);

// timing config

/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.c

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```
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/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.c
     writeReg(RANGE_CONFIG__VALID_PHASE_HIGH, 0x38);
     // dynamic config
     writeReg(SD_CONFIG__WOI_SD0, 0x07);
     writeReg(SD_CONFIG__WOI_SD1, 0x05);
     writeReg(SD CONFIG INITIAL PHASE SD0, 6); // tuning parm default
     writeReg(SD_CONFIG__INITIAL_PHASE_SD1, 6); // tuning parm default
     break;
    case Medium:
      // from VL53L1 preset mode standard ranging()
     // timing config
     writeReg(RANGE CONFIG VCSEL PERIOD A, 0x0B);
     writeReg(RANGE_CONFIG__VCSEL_PERIOD_B, 0x09);
     writeReg(RANGE_CONFIG__VALID_PHASE_HIGH, 0x78);
     // dynamic config
     writeReg(SD CONFIG WOI SD0, 0x0B);
     writeReg(SD CONFIG WOI SD1, 0x09);
     writeReg(SD_CONFIG__INITIAL_PHASE_SD0, 10); // tuning parm default
     writeReg(SD CONFIG INITIAL PHASE SD1, 10); // tuning parm default
     break;
    case Long: // long
     // from VL53L1_preset_mode_standard_ranging_long_range()
     // timing config
     writeReg(RANGE CONFIG VCSEL PERIOD A, 0x0F);
     writeReg(RANGE_CONFIG__VCSEL_PERIOD_B, 0x0D);
     writeReg(RANGE CONFIG VALID PHASE HIGH, 0xB8);
     // dynamic config
     writeReg(SD CONFIG WOI SD0, 0x0F);
     writeReg(SD_CONFIG__WOI_SD1, 0x0D);
     writeReq(SD CONFIG__INITIAL_PHASE_SD0, 14); // tuning parm default
     writeReg(SD CONFIG INITIAL PHASE SD1, 14); // tuning parm default
     break;
   default:
      // unrecognized mode - do nothing
```

```
// actually ends up with a slightly different value because it gets assigned,
 // retrieved, recalculated with a different macro period, and reassigned,
 // but it probably doesn't matter because it seems like the MM ("mode
  // mitigation"?) sequence steps are disabled in low power auto mode anyway.
 writeReg16Bit(MM CONFIG TIMEOUT MACROP A, encodeTimeout(
    timeoutMicrosecondsToMclks(1, macro period us)));
  // "Update Range Timing A timeout"
 writeReq16Bit(RANGE CONFIG TIMEOUT MACROP A, encodeTimeout(
    timeoutMicrosecondsToMclks(range config timeout us, macro period us)));
  // "Update Macro Period for Range B VCSEL Period"
 macro period us = calcMacroPeriod(readReg(RANGE CONFIG VCSEL PERIOD B));
 // "Update MM Timing B timeout"
  // (See earlier comment about MM Timing A timeout.)
 writeReg16Bit(MM_CONFIG__TIMEOUT_MACROP_B, encodeTimeout(
    timeoutMicrosecondsToMclks(1, macro period us)));
  // "Update Range Timing B timeout"
 writeReq16Bit(RANGE CONFIG TIMEOUT MACROP B, encodeTimeout(
    timeoutMicrosecondsToMclks(range config timeout us, macro period us)));
 // VL53L1 calc timeout register values() end
 return true;
// Get the measurement timing budget in microseconds
// based on VL53L1 SetMeasurementTimingBudgetMicroSeconds()
uint32 t VL53L1X::getMeasurementTimingBudget()
 // assumes PresetMode is LOWPOWER AUTONOMOUS and these sequence steps are
 // enabled: VHV, PHASECAL, DSS1, RANGE
 // VL53L1 get timeouts us() begin
 // "Update Macro Period for Range A VCSEL Period"
 uint32 t macro period us = calcMacroPeriod(readReg(RANGE CONFIG         VCSEL PERIOD A));
  // "Get Range Timing A timeout"
 uint32 t range config timeout us = timeoutMclksToMicroseconds(decodeTimeout(
    readReq16Bit(RANGE CONFIG TIMEOUT MACROP A)), macro period us);
```

```
// VL53L1_get_timeouts_us() end
 return 2 * range_config_timeout_us + TimingGuard;
// Start continuous ranging measurements, with the given inter-measurement
// period in milliseconds determining how often the sensor takes a measurement.
void VL53L1X::startContinuous(uint32 t period ms)
  // from VL53L1_set_inter_measurement_period_ms()
 writeReq32Bit(SYSTEM INTERMEASUREMENT PERIOD, period ms * osc calibrate val);
 writeReg(SYSTEM__INTERRUPT_CLEAR, 0x01); // sys_interrupt_clear_range
 writeReg(SYSTEM MODE START, 0x40); // mode range timed
// Stop continuous measurements
// based on VL53L1_stop_range()
void VL53L1X::stopContinuous()
 writeReg(SYSTEM__MODE_START, 0x80); // mode_range__abort
 // VL53L1 low power_auto_data_stop_range() begin
 calibrated = false;
  // "restore vhv configs"
 if (saved_vhv_init != 0)
    writeReg(VHV CONFIG INIT, saved vhv init);
  if (saved vhv timeout != 0)
     writeReg(VHV CONFIG TIMEOUT MACROP LOOP BOUND, saved vhv timeout);
 // "remove phasecal override"
 writeReg(PHASECAL CONFIG OVERRIDE, 0x00);
  // VL53L1_low_power_auto_data_stop_range() end
// Returns a range reading in millimeters when continuous mode is active
```

```
// (readRangeSingleMillimeters() also calls this function after starting a
// single-shot range measurement)
uint16 t VL53L1X::read(bool blocking)
 if (blocking)
    startTimeout();
    while (!dataReady())
      if (checkTimeoutExpired())
        did timeout = true;
        ranging_data.range_status = None;
        ranging data.range mm = 0;
        ranging data.peak signal count rate MCPS = 0;
        ranging_data.ambient_count_rate_MCPS = 0;
        return ranging_data.range_mm;
 readResults();
 if (!calibrated)
    setupManualCalibration();
    calibrated = true;
 updateDSS();
 getRangingData();
 writeReg(SYSTEM__INTERRUPT_CLEAR, 0x01); // sys_interrupt_clear_range
 return ranging data.range mm;
// convert a RangeStatus to a readable string
// Note that on an AVR, these strings are stored in RAM (dynamic memory), which
// makes working with them easier but uses up 200+ bytes of RAM (many AVR-based
// Arduinos only have about 2000 bytes of RAM). You can avoid this memory usage
// if you do not call this function in your sketch.
const char * VL53L1X::rangeStatusToString(RangeStatus status)
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.c
 switch (status)
    case RangeValid:
     return "range valid";
    case SigmaFail:
     return "sigma fail";
    case SignalFail:
     return "signal fail";
    case RangeValidMinRangeClipped:
     return "range valid, min range clipped";
    case OutOfBoundsFail:
     return "out of bounds fail";
    case HardwareFail:
     return "hardware fail";
    case RangeValidNoWrapCheckFail:
     return "range valid, no wrap check fail";
    case WrapTargetFail:
     return "wrap target fail";
    case XtalkSignalFail:
     return "xtalk signal fail";
    case SynchronizationInt:
     return "synchronization int";
    case MinRangeFail:
     return "min range fail";
    case None:
     return "no update";
    default:
     return "unknown status";
```

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```
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.c
                                                  Thu Mar 14 10:44:41 2019
                                                                                15
// Did a timeout occur in one of the read functions since the last call to
// timeoutOccurred()?
bool VL53L1X::timeoutOccurred()
 bool tmp = did timeout;
 did timeout = false;
 return tmp;
// "Setup ranges after the first one in low power auto mode by turning off
// FW calibration steps and programming static values"
// based on VL53L1 low power auto setup manual calibration()
void VL53L1X::setupManualCalibration()
 // "save original vhv configs"
 saved vhv init = readReg(VHV CONFIG INIT);
 saved_vhv_timeout = readReg(VHV_CONFIG__TIMEOUT_MACROP_LOOP_BOUND);
 // "disable VHV init"
 writeReq(VHV CONFIG INIT, saved vhv init & 0x7F);
 // "set loop bound to tuning param"
 writeReg(VHV_CONFIG__TIMEOUT_MACROP_LOOP_BOUND,
    (saved_vhv_timeout & 0x03) + (3 << 2)); // tuning parm default (LOWPOWERAUTO_VHV_LOOP_BOUND_DEFAULT)
 // "override phasecal"
 writeReg(PHASECAL_CONFIG__OVERRIDE, 0x01);
 writeReq(CAL CONFIG      VCSEL START, readReq(PHASECAL RESULT      VCSEL START));
// read measurement results into buffer
void VL53L1X::readResults()
 uint8 t buf[20];
// Sent the 2 byte register adress
 buf[0] = (RESULT__RANGE_STATUS >> 8) & 0xff;
 buf[1] = RESULT RANGE STATUS & 0xFF;
 rc i2c send bytes(I2C BUS, 2, buf);
// Read 17 bytes from the sensor
```

```
wire read bytes(I2C BUS, 17, buf);
// Save the bytes to the appropriate variable
 results.range status = buf[0];
 results.stream count = buf[2];
 results.dss actual effective spads sd0 = buf[3] << 8;
 results.dss actual effective spads sd0 |= buf[4];
 results.ambient_count_rate_mcps_sd0 = buf[7] << 8 ;</pre>
 results.ambient count rate mcps sd0 |= buf[8];
 results.final crosstalk_corrected_range_mm_sd0 = buf[13] << 8 ;</pre>
 results.final crosstalk corrected range mm sd0 |= buf[14];
 results.peak signal count rate crosstalk corrected mcps sd0 = buf[15] << 8;
 results.peak signal count rate crosstalk corrected mcps sd0 |= buf[16];
 last status = 0 ;
 Wire.beginTransmission(address);
 Wire.write((RESULT RANGE STATUS >> 8) & 0xFF); // req high byte
 Wire.write( RESULT RANGE STATUS
                                        & 0xFF); // reg low byte
 last status = Wire.endTransmission();
 Wire.requestFrom(address, (uint8_t)17);
 results.range status = Wire.read(); // 0
 Wire.read(); // report_status: not used //1
 results.stream count = Wire.read(); //2
 results.dss actual effective spads sd0 = (uint16 t)Wire.read() << 8; // high byte 3
 results.dss_actual_effective_spads_sd0 |=
                                                     Wire.read();  // low byte 4
 Wire.read(); // peak signal count rate mcps sd0: not used // 5
 Wire.read(); // 6
 results.ambient count rate mcps sd0 = (uint16 t)Wire.read() << 8; // high byte 7
 results.ambient_count_rate_mcps_sd0 |=
                                                  Wire.read();
                                                                // low byte 8
 Wire.read(); // sigma sd0: not used 9
 Wire.read(); // 10
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.c
                                                     Thu Mar 14 10:44:41 2019
                                                                                     17
 Wire.read(); // phase_sd0: not used 11
 Wire.read(); // 12
 results.final_crosstalk_corrected_range_mm_sd0 = (uint16_t)Wire.read() << 8; // high byte 13
 results.final_crosstalk_corrected_range_mm_sd0 |=
                                                              Wire.read();  // low byte 14
 results.peak_signal_count_rate_crosstalk_corrected_mcps_sd0 = (uint16_t)Wire.read() << 8; // high byte 15
 results.peak_signal_count_rate_crosstalk_corrected_mcps_sd0 |=
                                                                           Wire.read();
                                                                                         // low byte 16
* /
// perform Dynamic SPAD Selection calculation/update
// based on VL53L1 low power auto update DSS()
void VL53L1X::updateDSS()
 uint16_t spadCount = results.dss_actual_effective_spads_sd0;
 if (spadCount != 0)
    // "Calc total rate per spad"
   uint32 t totalRatePerSpad =
      (uint32 t)results.peak_signal_count_rate_crosstalk_corrected_mcps_sd0 +
     results.ambient count rate mcps sd0;
    // "clip to 16 bits"
    if (totalRatePerSpad > 0xFFFF) { totalRatePerSpad = 0xFFFF; }
    // "shift up to take advantage of 32 bits"
    totalRatePerSpad <<= 16;</pre>
    totalRatePerSpad /= spadCount;
    if (totalRatePerSpad != 0)
      // "get the target rate and shift up by 16"
     uint32_t requiredSpads = ((uint32_t)TargetRate << 16) / totalRatePerSpad;</pre>
      // "clip to 16 bit"
     if (requiredSpads > 0xFFFF) { requiredSpads = 0xFFFF; }
      // "override DSS config"
     writeReg16Bit(DSS_CONFIG__MANUAL_EFFECTIVE_SPADS_SELECT, requiredSpads);
      // DSS CONFIG ROI MODE CONTROL should already be set to REQUESTED EFFFECTIVE SPADS
```

```
return;
 // If we reached this point, it means something above would have resulted in a
 // divide by zero.
 // "We want to gracefully set a spad target, not just exit with an error"
  // "set target to mid point"
  writeReq16Bit(DSS CONFIG MANUAL EFFECTIVE SPADS SELECT, 0x8000);
// get range, status, rates from results buffer
// based on VL53L1 GetRangingMeasurementData()
void VL53L1X::getRangingData()
 // VL53L1 copy sys and core results to range results() begin
 uint16 t range = results.final crosstalk corrected range mm sd0;
 // "apply correction gain"
 // gain factor of 2011 is tuning parm default (VL53L1 TUNINGPARM LITE RANGING GAIN FACTOR DEFAULT)
 // Basically, this appears to scale the result by 2011/2048, or about 98%
 // (with the 1024 added for proper rounding).
 ranging_data.range_mm = ((uint32_t)range * 2011 + 0x0400) / 0x0800;
 // VL53L1_copy_sys_and_core_results_to_range_results() end
  // set range status in ranging data based on value of RESULT RANGE STATUS register
  // mostly based on ConvertStatusLite()
  switch(results.range status)
    case 17: // MULTCLIPFAIL
    case 2: // VCSELWATCHDOGTESTFAILURE
    case 1: // VCSELCONTINUITYTESTFAILURE
    case 3: // NOVHVVALUEFOUND
      // from SetSimpleData()
     ranging data.range status = HardwareFail;
     break;
    case 13: // USERROICLIP
     // from SetSimpleData()
     ranging data.range status = MinRangeFail;
```

```
break;
case 18: // GPHSTREAMCOUNTOREADY
 ranging_data.range_status = SynchronizationInt;
 break;
case 5: // RANGEPHASECHECK
  ranging_data.range_status = OutOfBoundsFail;
 break;
case 4: // MSRCNOTARGET
 ranging data.range status = SignalFail;
 break;
case 6: // SIGMATHRESHOLDCHECK
 ranging_data.range_status = SignalFail;
 break;
case 7: // PHASECONSISTENCY
 ranging_data.range_status = WrapTargetFail;
 break;
case 12: // RANGEIGNORETHRESHOLD
 ranging data.range status = XtalkSignalFail;
 break;
case 8: // MINCLIP
 ranging_data.range_status = RangeValidMinRangeClipped;
 break;
case 9: // RANGECOMPLETE
  // from VL53L1_copy_sys_and_core_results_to_range_results()
 if (results.stream count == 0)
    ranging_data.range_status = RangeValidNoWrapCheckFail;
  else
    ranging data.range status = RangeValid;
 break;
default:
 ranging_data.range_status = None;
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.c
                                                      Thu Mar 14 10:44:41 2019
                                                                                      20
  // from SetSimpleData()
 ranging_data.peak_signal_count_rate_MCPS =
    countRateFixedToFloat(results.peak_signal_count_rate_crosstalk_corrected_mcps_sd0);
 ranging data.ambient count rate MCPS =
    countRateFixedToFloat(results.ambient_count_rate_mcps_sd0);
// Decode sequence step timeout in MCLKs from register value
// based on VL53L1_decode_timeout()
uint32 t VL53L1X::decodeTimeout(uint16 t reg val)
 return ((uint32_t)(reg_val & 0xFF) << (reg_val >> 8)) + 1;
// Encode sequence step timeout register value from timeout in MCLKs
// based on VL53L1 encode timeout()
uint16_t VL53L1X::encodeTimeout(uint32_t timeout_mclks)
  // encoded format: "(LSByte * 2^MSByte) + 1"
 uint32 t ls byte = 0;
 uint16 t ms byte = 0;
 if (timeout_mclks > 0)
    ls_byte = timeout_mclks - 1;
    while ((ls byte & 0xFFFFFF00) > 0)
     ls byte >>= 1;
     ms byte++;
    return (ms byte << 8) | (ls byte & 0xFF);
  else { return 0; }
// Convert sequence step timeout from macro periods to microseconds with given
// macro period in microseconds (12.12 format)
// based on VL53L1_calc_timeout_us()
uint32_t VL53L1X::timeoutMclksToMicroseconds(uint32_t timeout_mclks, uint32_t macro_period_us)
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.c
                                                      Thu Mar 14 10:44:41 2019
                                                                                      21
 return ((uint64_t)timeout_mclks * macro_period_us + 0x800) >> 12;
// Convert sequence step timeout from microseconds to macro periods with given
// macro period in microseconds (12.12 format)
// based on VL53L1_calc_timeout_mclks()
uint32 t VL53L1X::timeoutMicrosecondsToMclks(uint32_t timeout_us, uint32_t macro_period_us)
 return (((uint32_t)timeout_us << 12) + (macro_period_us >> 1)) / macro_period_us;
// Calculate macro period in microseconds (12.12 format) with given VCSEL period
// assumes fast osc frequency has been read and stored
// based on VL53L1 calc macro period us()
uint32_t VL53L1X::calcMacroPeriod(uint8_t vcsel_period)
 // from VL53L1 calc pll period us()
 // fast osc frequency in 4.12 format; PLL period in 0.24 format
 uint32 t pll period_us = ((uint32_t)0x01 << 30) / fast_osc_frequency;</pre>
 // from VL53L1_decode_vcsel_period()
 uint8 t vcsel period pclks = (vcsel period + 1) << 1;</pre>
 // VL53L1_MACRO_PERIOD_VCSEL_PERIODS = 2304
 uint32_t macro_period_us = (uint32_t)2304 * pll_period_us;
 macro period us >>= 6;
 macro_period_us *= vcsel_period_pclks;
 macro_period_us >>= 6;
 return macro period us;
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/i2c_mux.h
                                                     Wed Mar 13 18:00:17 2019
// We need to enable and disable ports in the MUX
// More than one port may be enabled
//
#define
          MUX_I2C_BUS
#define
          MUX_I2C_ADDR
                            0x71
#define
          ALL_PORTS
                            0xff
// Routine to enable the specificed port
void enableMuxPort(uint8_t portNumber) ;
// Routine to disable the specfied port
void disableMuxPort(uint8_t portNumber) ;
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/color sensor.c
                                                      Wed Mar 13 17:58:40 2019
// Need access to std i/o routines
#include <stdio.h>
// Need access to i2c library funcitions
#include
          <robotcontrol.h>
// Color sensor related defines
          "color sensor.h"
#include
// We need our i2c MUX routines
#include
          "i2c mux.h"
// *************
// Routine to initialize the TCS3475 color sensor
// ^^^^^
void init color sensor(void){
// Set up a read buffer
  uint8_t rd_buf[BUF_SIZE] ;
// Set up a write buffer
  uint8_t wr_buf[BUF_SIZE] ;
// Make sure that the color sensor is selected
  rc i2c set device address(COLOR SENSOR I2C BUS, COLOR SENSOR ADDR);
// Need to enable the sensor by writing value to the ENABLE register
  wr_buf[0] = CMD_BIT | ENABLE ;
// Setting the lower two bits should enable the sensor
  wr buf[1] = 0x03 ;
  rc_i2c_write_byte(COLOR_SENSOR_I2C_BUS, wr_buf[0], wr_buf[1]) ;
// Let's read the ID register
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/color sensor.c
                                                      Wed Mar 13 17:58:40 2019
// Print it to the screen when debugging
// Check it to make sure it reads 0x44
  wr_buf[0] = CMD_BIT | ID ;
  rc_i2c_read_byte(COLOR_SENSOR_I2C_BUS, wr_buf[0], rd_buf);
  if (COLOR_SENSOR_DEBUG) {
        printf("We expect 0x44 and the color sensor returned the ID: %x\n", rd_buf[0]);
// Let's set the gain of the sensor
  wr buf[0] = CMD BIT | CONTROL;
  wr buf[1] = GAIN 16X ;
  rc i2c write byte(COLOR SENSOR I2C BUS, wr buf[0], wr buf[1]);
  if (COLOR_SENSOR_DEBUG) {
        printf("Gain setting is: %x\n", wr_buf[1]);
// Let's set the integration time of the sensor
  wr_buf[0] = CMD_BIT | ATIME ;
  wr buf[1] = INTEG TIME ;
  rc i2c write byte(COLOR SENSOR I2C BUS, wr buf[0], wr buf[1]);
  if (COLOR_SENSOR_DEBUG) {
        printf("Integration time is: %x\n", (int) wr_buf[1]) ;
  return ;
// *************
// Routine to cleanup the TCS3475 color sensor
void cleanup color sensor(void) {
  unsigned char wr_buf[BUF_SIZE] ;
// Need to disable the sensor by writing value to the ENABLE register
// Clearing the lower 2 bits should disable the sensor
  wr_buf[0] = CMD_BIT | ENABLE ;
  wr buf[1] = 0x00 ;
```

rc_i2c_write_byte(COLOR_SENSOR_I2C_BUS, wr_buf[0], wr_buf[1]);

```
return ;
// *************
// Routine to read the TCS3475 color sensor
// *************
void read color sensor(unsigned int *c,
                     unsigned int *r, unsigned int *g, unsigned int *b) {
// Set up a read buffer
  uint8 t rd buf[BUF SIZE] ;
// Set up a write buffer
  uint8 t wr buf[BUF SIZE] ;
// Let's get the "clear" data from the sensor
  wr buf[0] = CMD BIT | CDATA ;
  rc_i2c_read_bytes(COLOR_SENSOR_I2C_BUS, wr_buf[0], 2, rd_buf);
  *c = (unsigned int) rd buf[0];
  *c |= ((unsigned int) rd_buf[1]) << 8;
// Let's get the "red" data from the sensor
  wr_buf[0] = CMD_BIT | RDATA ;
  rc i2c read bytes(COLOR SENSOR I2C BUS, wr buf[0], 2, rd buf);
  *r = (unsigned int) rd buf[0];
  *r |= ((unsigned int) rd_buf[1]) << 8;
// Let's get the "green" data from the sensor
  wr buf[0] = CMD BIT | GDATA ;
  rc_i2c_read_bytes(COLOR_SENSOR_I2C_BUS, wr_buf[0], 2, rd_buf);
   *g = (unsigned int) rd_buf[0];
  *g |= ((unsigned int) rd_buf[1]) << 8;
// Let's get the "blue" data from the sensor
  wr buf[0] = CMD BIT | BDATA ;
  rc_i2c_read_bytes(COLOR_SENSOR_I2C_BUS, wr_buf[0], 2, rd_buf);
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/color_sensor.c Wed Mar 13 17:58:40 2019 4
  *b = (unsigned int) rd_buf[0];
  *b |= ((unsigned int) rd_buf[1]) << 8;
  return;
}</pre>
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/i2c mux.c
                                               Wed Mar 13 18:00:45 2019
// **************
// The color sensor shares device address 0x29 with
// the distance sensors so the solution is to use
// the Sparkfun i2c mux (8 ports)
// ^^^^^^
// We need our custom i2c routines
#include <stdint.h> // for uint8_t types etc
#include <stdlib.h>
#include <stdio.h>
#include <fcntl.h>
#include <unistd.h>
#include <sys/ioctl.h>
#include <linux/i2c-dev.h> //for IOCTL defs
         <robotcontrol.h>
#include
#include
         "i2c mux.h"
// ************
// Routine to enable a mux port
// **********
void enableMuxPort(uint8_t portNumber) {
  uint8_t
            settings ;
  int
            fd, ret;
// Select the MUX as the device we want to talk to
  rc_i2c_set_device_address(MUX_I2C_BUS, MUX_I2C_ADDR);
// Read the current MUX settings
  fd = rc i2c get fd(MUX I2C BUS);
  ret = read(fd, &settings, 1);
  if (ret != 1) printf("read failure in enableMuxPort\n") ;
// Set the wanted bit to enable the port
  if (portNumber <= 7) {
     settings |= (1 << portNumber) ;
  } else {
     settings = 0xff ;
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/i2c mux.c
                                                 Wed Mar 13 18:00:45 2019
// Write the byte to the MUX
// No register address
  rc i2c send byte(MUX I2C BUS, settings);
  return ;
// ***********
// Routine to disable a mux port
void disableMuxPort(uint8_t portNumber) {
  uint8_t
            settings ;
  int
            fd, ret;
// Select the MUX as the device we want to talk to
  rc_i2c_set_device_address(MUX_I2C_BUS, MUX_I2C_ADDR);
// Read the current MUX settings
  fd = rc_i2c_get_fd(MUX_I2C_BUS) ;
  ret = read(fd, &settings, 1);
  if (ret != 1) printf("read failure in disableMuxPort\n") ;
// Clear the wanted bit to disable the port
  if (portNumber <= 7) {</pre>
      settings &= ~(1 << portNumber) ;</pre>
  } else {
      settings = 0x00;
// Write the byte to the MUX
// No register address
  rc_i2c_send_byte(MUX_I2C_BUS, settings) ;
  return ;
```

```
Tue Mar 19 15:45:11 2019
/opt/home/gle/BeagleBoneBlue/bluebot/bluebot.c
// @file
          bluebot.c
// Simple robot program using beaglebone blue
// Revised on March 14, 2019
// Demonstrate working I2C mux which had a color
// sensor along with 3 distance sensors connected to it.
//
#include
          <stdio.h>
#include
          <siqnal.h>
#include
          <getopt.h>
#include
          <math.h>
#include
          <robotcontrol.h>
#include
          "arclib.h"
#include
          "VL53L1X.h"
#include
          "color sensor pkg.h"
#include
          "distance_sensor_pkg.h"
#include
          "servo pkq.h"
// ***********
// Create our robot structure as a global
// That way all our arc routines will have easy
// access to it
// ***********
arc robot t
              robot ;
bool
              DEBUG = false ;
rc filter t
              left_motor_filter = RC_FILTER_INITIALIZER ;
rc_filter_t
              right_motor_filter = RC_FILTER_INITIALIZER;
// Distance sensors
VL53L1X front sensor ;
VL53L1X left_sensor ;
VL53L1X right_sensor ;
// **********
// Routine to perform some general setup
// ***********
int general_setup(void) {
```

```
// Enter pointers to filters into our robot structure
// Our PID filters are defined as globals.
  robot.right_motor.pid = &(right_motor_filter) ;
  robot.left_motor.pid = &(left_motor_filter) ;
// make sure another instance isn't running
// if return value is -3 then a background process is running with
// higher privaledges and we couldn't kill it, in which case we should
// not continue or there may be hardware conflicts. If it returned -4
// then there was an invalid argument that needs to be fixed.
  if (rc_kill_existing_process(2.0)<-2) return ARC_FAIL ;
// Create a lock file
  rc make pid file();
// Start signal handler so we can exit cleanly
  if (rc_enable_signal_handler()==-1) {
       fprintf(stderr, "ERROR: failed to start signal handler\n");
      return ARC FAIL ;
// Set the system state to RUNNING
// If user issues a Ctrl-C then the state will change to EXITING
  rc set state(RUNNING) ;
  return ARC PASS ;
} // end general setup
// ^^^^^^
// Routine to configure robot
// ^^^^^^
int config(void) {
   double
             perimeter;
// Robot configuration settings
   robot.config.sample rate = 20 ;
                                                                   // Hz
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/bluebot.c
                                                  Tue Mar 19 15:45:11 2019
   robot.config.Ts = 1.0 / robot.config.sample rate ;
                                                                 // sec
   robot.config.Ts in ns = (uint64 t) (robot.config.Ts * 1e9) ;
   robot.config.r = 1.375;
                                                                 // wheel radius
   robot.config.d = 5.25 ;
                                                                 // distance between wheels
   robot.config.encoder_tics_per_revolution = 1440.0 ;
   robot.config.motor PID gain.Kp = 0.005 ;
                                                                 // Proportional gain constant
   robot.config.motor_PID_gain.Ki = 0.0005 ;
                                                                 // Integral gain constant
   robot.config.motor_PID_gain.Kd = 0 ;
                                                                 // Differential gain constant
// Right motor configuration
  robot.right motor.id = 2 ;
                                            // Blue motor number
  robot.right motor.swap = ARC SWAP ;
                                             // swap motor wires
// Left motor configuration
  robot.left_motor.id = 3 ;
                                            // Blue motor number
  robot.left motor.swap = ARC NO SWAP ; // dont't swap blk and red wires
// Config common to both motors
   perimeter = 2.0 * M PI * robot.config.r ;
   robot.config.tics per inch = robot.config.encoder tics per revolution / perimeter ;
   robot.config.inches per tic = 1.0 / robot.config.tics per inch ;
   robot.config.max_pwm = 0.9 ;
// Set current and target locations
   robot.location.theta = M PI / 2.0 ;
   robot.location.s = 0.0 ;
   robot.location.xy = RC VECTOR INITIALIZER ;
   rc_vector_zeros(&robot.location.xy, 2);
   robot.target.theta = M_PI / 2.0 ;
   robot.target.s = 0.0 ;
   robot.target.xy = RC VECTOR INITIALIZER ;
   rc_vector_zeros(&robot.target.xy, 2);
   return ARC PASS;
// ************
// Routine to cleanup after ourselves
// *************
```

```
int general_cleanup(void) {
// Clean up motors
   rc motor cleanup();
// Free up the memory used by the PID filters
   rc_filter_free(robot.right_motor.pid);
   rc_filter_free(robot.left_motor.pid);
// Turn the LEDs off and shutoff LED handlers
   rc led set(RC LED GREEN, ARC OFF);
   rc_led_set(RC_LED_RED, ARC_OFF);
   rc_led_cleanup();
// Cleanup the encoder stuff
   rc_encoder_eqep_cleanup() ;
// Remove the lock file
   rc_remove_pid_file();
// Print loation of robot and dump variables
  if (DEBUG) {
     arc print location(robot.location) ;
     arc_var_dump();
  return ARC PASS ;
^^^^
// ^^^^^
int main() {
 double distance;
// General stuff
```

```
general_setup() ;
  config();
  arc_init();
// Test the color sensor
    color_sensor_test();
// Test distance sensor servo
  sweep_distance_sensor();
// Test distance sensor
  distance = distance sensor();
  printf("Front sensor reading: \t%f\n", distance);
  fflush(stdout) ;
  arc_forward(distance / 2.0, 6.0);
  distance = distance sensor();
  printf("Front sensor reading: \t%f\n", distance);
  fflush(stdout);
  arc_forward(distance / 2.0, 6.0);
  distance = distance_sensor();
  printf("Front sensor reading: \t%f\n", distance);
  fflush(stdout) ;
  arc_forward(distance / 2.0, 6.0);
// Demo rotate
  arc_rotate(arc_deg2rad(-90.0));
  arc rotate(arc deg2rad(90.0));
// General cleanup
  general_cleanup();
  return 0 ;
} // end main()
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/color sensor pkg.c
// color_senso_pkg.c
//
// Will prove we can talk to I2C devices
// Need access to std i/o routines
#include <stdio.h>
// Robotic Control Library
#include <robotcontrol.h>
// Color sensor related defines
#include "color_sensor.h"
// We need our i2c mux routines
#include
         "i2c_mux.h"
// This is a program to test out a series of submodules
// that might be useful for robotics
#define
         ON
                 1
#define
                 0
         OFF
#define
         COLOR_SENSOR_MUX_PORT
// ^^^^^
// Main program
// *******
void color sensor test(void) {
// I2C bus will get initialized
  rc_i2c_init(MUX_I2C_BUS, MUX_I2C_ADDR);
// Select the color sensor
  disableMuxPort(ALL_PORTS) ;
```

Mon Mar 18 09:48:43 2019

```
Mon Mar 18 09:48:43 2019
/opt/home/gle/BeagleBoneBlue/bluebot/color sensor pkg.c
   enableMuxPort(COLOR_SENSOR_MUX_PORT) ;
// Initialize the color sensor
   init color sensor();
// Turn red and green LEDs off
\ensuremath{//} Turn on the green one when we detect the green LED
  rc_led_set(RC_LED_GREEN, 0);
  rc_led_set(RC_LED_RED, 0);
// Read the color sensor
  unsigned int c, r, q, b;
  int do_it = 1;
   while (do_it < 15) {
      read_color_sensor(&c, &r, &g, &b) ;
       printf("Color sensor reading: c = %6u r= %6u g = %6u blue = %6u\r", c, r, g, b);
       fflush(stdout);
       if (g > 1400) {
           rc_led_set(RC_LED_GREEN, ON) ;
          rc led set(RC LED RED, OFF) ;
       } else {
          rc_led_set(RC_LED_GREEN, OFF) ;
           rc_led_set(RC_LED_RED, ON) ;
       do_it += 1 ;
       if (rc get state() == EXITING) break ;
      rc_usleep(1000000);
   } // end while
  printf("\n"); fflush(stdout);
// Closing up the color sensor
   cleanup_color_sensor();
// Disable all the MUX ports
  rc_i2c_set_device_address(MUX_I2C_BUS, MUX_I2C_ADDR);
  disableMuxPort(ALL PORTS) ;
// Close the i2c channel
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/arclib.c
                                               Tue Mar 19 11:59:48 2019
// arclib is a library of robot routimes
// makes heavy use of strawson libcontrol
//
#include
          <stdio.h>
#include
          <stdlib.h>
#include
          <math.h>
#include
          <robotcontrol.h>
#include
         "arcdefs.h"
extern arc robot t robot;
// *********
// Routine to dump key values
// *********
void arc_var_dump(void) {
   printf("Sample rate is %g Hz\n", robot.config.sample_rate) ;
   printf("Sample period is %q ms\n", 1000.0 * robot.config.Ts);
   printf("Wheel radius is %g in\n", robot.config.r);
   printf("Encoder tics per wheel revolution is %g\n", robot.config.encoder tics per revolution);
   printf("Inches per tic is %q\n", robot.config.inches per tic);
   printf("Tics per inch is %g\n", robot.config.tics_per_inch);
   printf("Maximum pwm value is %g\n", robot.config.max_pwm);
   printf("Velocity of robot is %g in/sec\n", robot.velocity) ;
   printf("Right motor setpoint is %d\n", robot.right_motor.sp) ;
   printf("Left motor setpoint is %d\n", robot.left_motor.sp) ;
} // end arc dump()
// ^^^^^^
// Routine print location
// *********
void arc print location(arc location t loc) {
   printf("x is %g in\n", loc.xy.d[X]);
   printf("y is %g in\n", loc.xy.d[Y]);
   printf("theta is %g degrees\n", loc.theta * ARC_RAD2DEG) ;
} // end arc_print_location()
// ***********
// Routine to initialize everything
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/arclib.c
                                                   Tue Mar 19 11:59:48 2019
// **********
int arc init(void) {
   double
               Kp, Ki, Kd, dt;
// Set up the filters we need for PID on right and left motors
   Kp = robot.config.motor PID gain.Kp ;
   Ki = robot.config.motor_PID_gain.Ki ;
   Kd = robot.config.motor_PID_gain.Kd ;
   dt = robot.config.Ts ;
// Create the right and left motor PID filter
    if(rc_filter_pid(robot.right_motor.pid, Kp, Ki, Kd, (4.0 * dt), dt)){
        fprintf(stderr, "ERROR in arc_init(), failed to make PID right motor controller filter.\n");
        return ARC FAIL ;
   if(rc_filter_pid(robot.left_motor.pid, Kp, Ki, Kd, (4.0 * dt), dt)){
        fprintf(stderr, "ERROR in arc init(), failed to make PID left motor controller filter.\n");
       return ARC FAIL ;
// Initialize motors and encoders
    if (rc_motor_init_freq(RC_MOTOR_DEFAULT_PWM_FREQ)) {
       return ARC FAIL;
                          // Set PWM frequency
    if (rc motor init()==-1){
        fprintf(stderr, "ERROR: failed to initialize motors\n");
        return ARC_FAIL;
    if (rc encoder egep init()==-1){
        fprintf(stderr, "ERROR: failed to initialize eqep encoders\n");
       return ARC_FAIL;
// Turn both LEDs OFF
   rc led set(RC LED GREEN, ARC OFF);
   robot.greenLED = ARC OFF ;
   rc_led_set(RC_LED_RED, ARC_OFF);
```

Tue Mar 19 11:59:48 2019

```
ticsL = robot.left motor.tics ;
  ticsL *= robot.left motor.dir ;
  delta_s = 0.5 * (ticsR + ticsL);
  delta s *= robot.config.inches per tic ;
  robot.location.s += delta_s ;
  delta theta = ticsR - ticsL ;
  delta_theta *= robot.config.inches_per_tic ;
  delta_theta /= robot.config.d ;
  robot.location.theta += delta theta ;
  delta x = delta s * cos(robot.location.theta) ;
  delta y = delta s * sin(robot.location.theta) ;
  robot.location.xy.d[X] += delta_x ;
  robot.location.xy.d[Y] += delta_y ;
  return ;
} // end arc_update_location()
// ************
// Routine used to toggle green LED
// ^^^^^
void toggleGreenLED(void) {
  if (robot.greenLED == ARC_ON) {
     robot.greenLED = ARC_OFF ;
     rc led set(RC LED GREEN, ARC OFF);
  } else {
     robot.greenLED = ARC_ON ;
     rc led set(RC LED GREEN, ARC ON);
    return ;
} // end toggleGreenLED()
// ***********
// Routine used by arc goto for initialization
// ***********
void arc move init(int right dir, int left dir, bool soft start)
  double
         td, tmp;
```

```
// Reset the PID filters
// Enable saturation and ask for a slow start in the PID filters
   rc filter reset(robot.right motor.pid) ;
   rc filter reset(robot.left motor.pid) ;
   rc_filter_enable_saturation(robot.right_motor.pid, 0, robot.config.max_pwm);
   rc_filter_enable_saturation(robot.left_motor.pid, 0, robot.config.max pwm);
   if (soft start) {
      td = 2.0 * robot.config.Ts;
      rc_filter_enable_soft_start(robot.right_motor.pid, td) ;
      rc filter enable soft start(robot.left motor.pid, td) ;
// Set motor directions
   robot.right_motor.dir = right_dir ;
   robot.left motor.dir = left dir ;
// Encoder initialization
// Set encoder values to 0
   rc encoder write(robot.right motor.id, 0);
   rc encoder write(robot.left motor.id, 0);
// Compute number of tics we expect to see in sample period
// This is the setpoint into the PID controller
   robot.right_motor.pwm = 0.0 ;
   robot.left motor.pwm = 0.0 ;
   robot.right motor.cnt = 0 ;
   robot.left motor.cnt = 0 ;
   tmp = robot.config.Ts * robot.velocity ;
   tmp = tmp / robot.config.inches_per_tic ;
   robot.right motor.sp = (int) (tmp + 0.5);
   robot.left motor.sp = (int) (tmp + 0.5);
   robot.location.s = 0.0;
   return ;
} // end arc_move_init()
// ************
// Routine to move robot
// *************
```

```
void arc move(void) {
     int
                     error ;
// Read right and left motor encoders
// Compute how far each wheel moved in "tics"
// during previous sample period.
    robot.right motor.old cnt = robot.right motor.cnt ;
    robot.left_motor.old_cnt = robot.left_motor.cnt ;
    robot.right motor.cnt = abs(rc encoder read(robot.right motor.id)) ;
    robot.left motor.cnt = abs(rc encoder read(robot.left motor.id)) ;
    robot.right motor.tics = robot.right motor.cnt - robot.right motor.old cnt ;
    robot.left_motor.tics = robot.left_motor.cnt - robot.left_motor.old_cnt ;
// Update pwm values for motors
// We might need to "swap" black and red wires
    robot.right motor.pwm *= robot.right motor.swap * robot.right motor.dir ;
    robot.left motor.pwm *= robot.left motor.swap * robot.left motor.dir ;
    rc motor set(robot.right motor.id, robot.right motor.pwm) ;
    rc_motor_set(robot.left_motor.id, robot.left_motor.pwm) ;
// Compute error and filter
// Error is the difference between how many tics we actually moved
// and what we had expected to move.
    error = (double) (robot.right_motor.sp - robot.right_motor.tics);
    robot.right_motor.pwm = rc_filter_march(robot.right_motor.pid, error) ;
    error = (double) (robot.left_motor.sp - robot.left_motor.tics) ;
    robot.left motor.pwm = rc filter march(robot.left motor.pid, error) ;
// Update our location
    arc update location();
// Toggle the green LED to inform user that we are in the move routine
    toggleGreenLED();
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/arclib.c
                                               Tue Mar 19 11:59:48 2019
} // end arc_move()
// ************
// Routine to compute the heading
// Heading consists of a distance and an angle
// *************
arc_heading_t arc_compute_heading(void) {
  arc heading t heading;
  rc vector t
               dv = RC VECTOR INITIALIZER ;
  double
                angle ;
  rc vector zeros(&dv, 2);
// Compute the vector differnce between current and target locations
  rc_vector_subtract(robot.target.xy, robot.location.xy, &dv) ;
// Compute 2-norm (distance measure) of difference vector
  heading.distance = rc vector norm(dv, 2) ;
// Find angle
  angle = abs(atan(dv.d[Y] / dv.d[X])) ;
  if ((dv.d[X] >= 0) \&\& (dv.d[Y] >= 0)) angle = angle;
  if ((dv.d[X] >= 0) \&\& (dv.d[Y] <= 0)) angle += arc\_deg2rad(270.0);
  if ((dv.d[X] \le 0) \&\& (dv.d[Y] \le 0)) angle += arc\_deg2rad(180.0);
  if ((dv.d[X] \le 0) \&\& (dv.d[Y] \ge 0)) angle += arc_deg2rad(90.0);
  heading.angle = angle ;
  return heading ;
} // end arc_compute_heading()
// *************
// Routine to rotate robot
// Angle value should be in radians
// It the amount we want to rotate by
// + is CCW and - is CW
// *************
void arc_rotate(double angle) {
   int
                  target reached;
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/arclib.c
                                                  Tue Mar 19 11:59:48 2019
   uint64_t
                   start time ;
   unsigned int
                   delay time ;
   double
                   err, max err ;
  robot.location.theta = 0.0 ;
  robot.velocity = ROTATE VEL ;
// We will accept a specified amount of error in our final location
  max err = arc deg2rad(MAX ANGLE ERROR) ;
// Wheels must rotate in opposite direction for a rotation
  if (fabs(angle) <= max err) return ;</pre>
  if (angle >= 0.0) arc move init(ARC FWD, ARC BWD, false); // Counter clock-wise
  else arc_move_init(ARC_BWD, ARC_FWD, true);
                                                           // Clock-wise
// Keep "moving" until we get "very close" to our desired orientation
// Compute delay on the fly to ensure fixed sample period!
   target reached = false ;
  while (target reached == false) {
     start time = rc nanos since epoch();
     arc move();
     err = angle - robot.location.theta ;
     if (fabs(err) < max_err) target_reached = true ;</pre>
     if (rc get state() != EXITING) {
        delay_time = (unsigned int) ( (robot.config.Ts_in_ns - (rc_nanos_since_epoch() - start_time)) );
        delay_time /= 1000;
        rc usleep(delay time);
     } else return ;
   } // end while
  rc motor brake(0);
  rc_usleep(500000);
  return ;
} // end arc_rotate();
// ************
// Routine to "goto" given location (BUGGY)
// *************
void arc_goto(double x, double y, double theta, double velocity) {
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/arclib.c
                                                  Tue Mar 19 11:59:48 2019
                                                                                10
     err = heading.distance - robot.location.s ;
     if (fabs(err) < MAX_DIST_ERROR) target_reached = true ;</pre>
     if (rc get state() != EXITING) {
        delay_time = (unsigned int) ( (robot.config.Ts_in_ns - (rc_nanos_since_epoch() - start_time)) );
        delay time /= 1000;
        rc usleep(delay time);
      } else return ;
   } // end while
// Rotate to get in the desired position
   angle = final theta - robot.location.theta ;
   robot.velocity = ROTATE VEL ;
   arc rotate(angle) ;
   rc usleep(500000);
   return ;
} // end arc goto()
// *************
// Routine used to move forward
// *************
void arc_forward(double distance, double velocity) {
   bool
                   target reached;
   uint64 t
                   start time ;
   unsigned int
                   delay_time ;
   double
                   err ;
   robot.location.theta = 0.0 ;
   robot.velocity = velocity ;
   arc_move_init(ARC_FWD, ARC_FWD, true);
   target reached = false ;
   while (target reached == false) {
     start_time = rc_nanos_since_epoch();
     arc move();
     err = distance - robot.location.s ;
     if (fabs(err) < MAX_DIST_ERROR) target_reached = true ;</pre>
     if (rc_get_state() != EXITING) {
        delay time = (unsigned int) ( (robot.config.Ts in ns - (rc nanos since epoch() - start time)) );
        delay time /= 1000;
        rc usleep(delay time);
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/color_sensor_pkg.h
```

Mon Mar 18 08:06:36 2019

```
void color_sensor_test(void) ;
```

```
// Replicating the Arduino millis()
#include <stdio.h>
#include <fcntl.h>
#include <unistd.h>
#include <sys/ioctl.h>
#include <linux/i2c-dev.h> //for IOCTL defs
#include <robotcontrol.h>
unsigned long millis(void) {
 unsigned long ms;
 uint64_t nanos;
 nanos = rc_nanos_since_epoch() ;
 ms = (unsigned long) (nanos / 1000000);
 return ms ;
// Routine to read bytes using i2c
int wire_read_bytes(uint8_t bus, uint8_t count, uint8_t *buf) {
  int ret, fd;
  fd = rc_i2c_get_fd(bus) ;
  ret = read(fd, buf, count) ;
  if (ret != count) return -1;
  else return ret ;
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.h
                                                            Thu Mar 14 06:25:24 2019
                                                                                               1
#pragma once
#include "Arduino.h"
#include <stdint.h>
class VI<sub>5</sub>3I<sub>1</sub>1X
 public:
    // register addresses from API vl53l1x register map.h
    enum regAddr : uint16 t
      SOFT RESET
                                                                                           = 0 \times 00000,
      I2C SLAVE DEVICE ADDRESS
                                                                                           = 0 \times 0001,
      ANA CONFIG VHV REF SEL VDDPIX
                                                                                           = 0 \times 0002,
      ANA_CONFIG__VHV_REF_SEL_VQUENCH
                                                                                           = 0 \times 0003,
      ANA_CONFIG__REG_AVDD1V2_SEL
                                                                                           = 0 \times 0004,
      ANA CONFIG FAST OSC TRIM
                                                                                           = 0 \times 0005,
      OSC_MEASURED__FAST_OSC__FREQUENCY
                                                                                           = 0 \times 0006,
      OSC_MEASURED__FAST_OSC__FREQUENCY_HI
                                                                                           = 0 \times 0006,
      OSC MEASURED FAST OSC FREQUENCY LO
                                                                                           = 0 \times 0007.
                                                                                           = 0x0008,
      VHV_CONFIG__TIMEOUT_MACROP_LOOP_BOUND
      VHV CONFIG COUNT THRESH
                                                                                           = 0 \times 0009,
      VHV CONFIG OFFSET
                                                                                           = 0x000A
                                                                                           = 0x000B,
      VHV_CONFIG__INIT
      GLOBAL_CONFIG__SPAD_ENABLES_REF_0
                                                                                           = 0x000D,
      GLOBAL CONFIG SPAD ENABLES REF 1
                                                                                           = 0x000E
      GLOBAL CONFIG SPAD ENABLES REF 2
                                                                                           = 0x000F,
      GLOBAL_CONFIG__SPAD_ENABLES_REF_3
                                                                                           = 0 \times 0010,
      GLOBAL CONFIG SPAD ENABLES REF 4
                                                                                           = 0 \times 0011,
      GLOBAL CONFIG SPAD ENABLES REF 5
                                                                                           = 0 \times 0012,
      GLOBAL_CONFIG__REF_EN_START_SELECT
                                                                                           = 0 \times 0013,
      REF_SPAD_MAN__NUM_REQUESTED_REF_SPADS
                                                                                           = 0 \times 0014
      REF_SPAD_MAN__REF_LOCATION
                                                                                           = 0 \times 0015,
      ALGO CROSSTALK_COMPENSATION_PLANE_OFFSET_KCPS
                                                                                           = 0 \times 0016,
      ALGO CROSSTALK COMPENSATION PLANE OFFSET KCPS HI
                                                                                           = 0 \times 0016,
      ALGO__CROSSTALK_COMPENSATION_PLANE_OFFSET_KCPS_LO
                                                                                           = 0 \times 0017,
      ALGO__CROSSTALK_COMPENSATION_X_PLANE_GRADIENT_KCPS
                                                                                           = 0 \times 0018,
      ALGO CROSSTALK COMPENSATION X PLANE GRADIENT KCPS HI
                                                                                           = 0x0018,
      ALGO__CROSSTALK_COMPENSATION_X_PLANE_GRADIENT_KCPS_LO
                                                                                           = 0 \times 0019,
                                                                                           = 0x001A,
      ALGO CROSSTALK COMPENSATION Y PLANE GRADIENT KCPS
      ALGO CROSSTALK COMPENSATION Y PLANE GRADIENT KCPS HI
                                                                                           = 0 \times 001 A,
      ALGO CROSSTALK COMPENSATION Y PLANE GRADIENT KCPS LO
                                                                                           = 0x001B,
      REF SPAD CHAR TOTAL RATE TARGET MCPS
                                                                                           = 0 \times 001C,
```

	_				_
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.h	Thu Mar	14 0	6:25:24	2019	2
REF_SPAD_CHARTOTAL_RATE_TARGET_MCPS_HI					= 0x001C,
REF_SPAD_CHARTOTAL_RATE_TARGET_MCPS_LO					= 0x001D,
ALGOPART_TO_PART_RANGE_OFFSET_MM					= 0x001E,
ALGOPART_TO_PART_RANGE_OFFSET_MM_HI					= 0x001E,
ALGOPART_TO_PART_RANGE_OFFSET_MM_LO					= 0x001F,
MM_CONFIGINNER_OFFSET_MM					= 0x0020,
MM_CONFIGINNER_OFFSET_MM_HI					= 0x0020,
MM_CONFIGINNER_OFFSET_MM_LO					= 0x0021,
MM_CONFIGOUTER_OFFSET_MM					= 0x0022,
MM_CONFIGOUTER_OFFSET_MM_HI					= 0x0022,
MM_CONFIGOUTER_OFFSET_MM_LO					= 0x0023,
DSS_CONFIGTARGET_TOTAL_RATE_MCPS					= 0x0024,
DSS_CONFIGTARGET_TOTAL_RATE_MCPS_HI					= 0x0024,
DSS_CONFIGTARGET_TOTAL_RATE_MCPS_LO					= 0x0025,
DEBUGCTRL					= 0x0026,
TEST_MODECTRL					= 0x0027,
CLK_GATINGCTRL					= 0x0028,
NVM_BISTCTRL					= 0x0029,
NVM_BISTNUM_NVM_WORDS					= 0x002A,
NVM_BISTSTART_ADDRESS					= 0x002B,
HOST_IFSTATUS					= 0x002C,
PAD_I2C_HVCONFIG					= 0x002D,
PAD_I2C_HVEXTSUP_CONFIG					= 0x002E,
GPIO_HV_PADCTRL					= 0x002F,
GPIO_HV_MUXCTRL					= 0x0030,
GPIOTIO_HV_STATUS					= 0x0031,
GPIOFIO_HV_STATUS					= 0x0032,
ANA_CONFIGSPAD_SEL_PSWIDTH					= 0x0033,
ANA_CONFIGVCSEL_PULSE_WIDTH_OFFSET					= 0x0034,
ANA_CONFIGFAST_OSCCONFIG_CTRL					= 0x0035,
SIGMA_ESTIMATOREFFECTIVE_PULSE_WIDTH_NS					= 0x0036,
SIGMA_ESTIMATOREFFECTIVE_AMBIENT_WIDTH_NS					= 0x0037,
SIGMA_ESTIMATORSIGMA_REF_MM					= 0x0038,
ALGOCROSSTALK_COMPENSATION_VALID_HEIGHT_MM					= 0x0039,
SPARE_HOST_CONFIGSTATIC_CONFIG_SPARE_0					= 0x003A,
SPARE_HOST_CONFIGSTATIC_CONFIG_SPARE_1					= 0x003B,
ALGORANGE_IGNORE_THRESHOLD_MCPS					= 0x003C,
ALGORANGE_IGNORE_THRESHOLD_MCPS_HI					= 0x003C,
ALGORANGE_IGNORE_THRESHOLD_MCPS_LO					= 0x003D,
ALGORANGE_IGNORE_VALID_HEIGHT_MM					= 0x003E,
ALGORANGE_MIN_CLIP					= 0x003F,
ALGOCONSISTENCY_CHECKTOLERANCE					= 0x0040,
SPARE_HOST_CONFIGSTATIC_CONFIG_SPARE_2					= 0x0041,
SD_CONFIGRESET_STAGES_MSB					= 0x0042,

```
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.h
                                                              Thu Mar 14 06:25:24 2019
                                                                                                  3
                                                                                              = 0 \times 0043,
      SD CONFIG RESET STAGES LSB
      GPH CONFIG STREAM COUNT UPDATE VALUE
                                                                                              = 0 \times 0044
      GLOBAL CONFIG__STREAM_DIVIDER
                                                                                              = 0 \times 0045,
      SYSTEM__INTERRUPT_CONFIG_GPIO
                                                                                              = 0 \times 0046,
      CAL CONFIG__VCSEL_START
                                                                                              = 0 \times 0047
      CAL CONFIG REPEAT RATE
                                                                                              = 0 \times 0048
      CAL_CONFIG__REPEAT_RATE_HI
                                                                                              = 0 \times 0048,
      CAL CONFIG REPEAT RATE LO
                                                                                              = 0 \times 0049.
      GLOBAL CONFIG VCSEL WIDTH
                                                                                              = 0 \times 0004A
      PHASECAL_CONFIG__TIMEOUT_MACROP
                                                                                              = 0x004B,
                                                                                              = 0 \times 004C,
      PHASECAL_CONFIG__TARGET
      PHASECAL CONFIG OVERRIDE
                                                                                              = 0 \times 004 D
      DSS_CONFIG__ROI_MODE_CONTROL
                                                                                              = 0 \times 004 F,
      SYSTEM THRESH RATE HIGH
                                                                                              = 0 \times 0050,
      SYSTEM THRESH RATE HIGH HI
                                                                                              = 0 \times 0050,
                                                                                              = 0 \times 0051,
      SYSTEM__THRESH_RATE_HIGH_LO
      SYSTEM__THRESH_RATE_LOW
                                                                                              = 0 \times 0052,
       SYSTEM THRESH RATE LOW HI
                                                                                              = 0 \times 0052
      SYSTEM__THRESH_RATE_LOW_LO
                                                                                              = 0 \times 0053,
      DSS CONFIG MANUAL EFFECTIVE SPADS SELECT
                                                                                              = 0 \times 0.054.
      DSS CONFIG MANUAL EFFECTIVE SPADS SELECT HI
                                                                                              = 0 \times 0054
      DSS CONFIG MANUAL EFFECTIVE SPADS SELECT LO
                                                                                              = 0 \times 0055,
      DSS CONFIG MANUAL BLOCK SELECT
                                                                                              = 0 \times 0056,
      DSS CONFIG APERTURE ATTENUATION
                                                                                              = 0 \times 0057,
      DSS_CONFIG__MAX_SPADS_LIMIT
                                                                                              = 0 \times 0058,
      DSS_CONFIG__MIN_SPADS_LIMIT
                                                                                              = 0 \times 0059,
      MM_CONFIG__TIMEOUT_MACROP_A
                                                                                              = 0 \times 005A, // added by Pololu for 16-bit a
ccesses
      MM_CONFIG__TIMEOUT_MACROP_A_HI
                                                                                              = 0x005A,
      MM CONFIG TIMEOUT MACROP A LO
                                                                                              = 0x005B,
                                                                                              = 0 \times 005C, // added by Pololu for 16-bit a
      MM CONFIG TIMEOUT MACROP B
ccesses
      MM CONFIG TIMEOUT MACROP B HI
                                                                                              = 0x005C
                                                                                              = 0 \times 005 D,
      MM_CONFIG__TIMEOUT_MACROP_B_LO
      RANGE CONFIG TIMEOUT MACROP A
                                                                                              = 0 \times 005E, // added by Pololu for 16-bit a
ccesses
      RANGE_CONFIG__TIMEOUT_MACROP_A_HI
                                                                                              = 0 \times 005 E,
      RANGE CONFIG TIMEOUT MACROP A LO
                                                                                              = 0 \times 005 F,
      RANGE CONFIG VCSEL PERIOD A
                                                                                              = 0 \times 0060,
      RANGE CONFIG TIMEOUT MACROP B
                                                                                              = 0 \times 0061, // added by Pololu for 16-bit a
ccesses
      RANGE CONFIG TIMEOUT MACROP B HI
                                                                                              = 0 \times 0061
      RANGE CONFIG TIMEOUT MACROP B LO
                                                                                              = 0 \times 0062,
      RANGE CONFIG VCSEL PERIOD B
                                                                                              = 0 \times 0063,
```

/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.h	Thu Mar 14 06:25:24 2019	4
RANGE_CONFIGSIGMA_THRESH		$= 0 \times 0064$
RANGE_CONFIGSIGMA_THRESH_HI		= 0x0064
RANGE CONFIG SIGMA THRESH LO		= 0x0065,
RANGE_CONFIGMIN_COUNT_RATE_RTN_LIMIT_MCPS		= 0x0066
RANGE_CONFIGMIN_COUNT_RATE_RTN_LIMIT_MCPS_HI		= 0x0066,
RANGE_CONFIGMIN_COUNT_RATE_RTN_LIMIT_MCPS_LO		= 0x0067,
RANGE_CONFIGVALID_PHASE_LOW		= 0x0068,
RANGE CONFIG VALID PHASE HIGH		$= 0 \times 0069$
SYSTEM INTERMEASUREMENT PERIOD		$= 0 \times 006C$
SYSTEMINTERMEASUREMENT_PERIOD_3		$= 0 \times 006C$
SYSTEMINTERMEASUREMENT_PERIOD_2		= 0x006D,
SYSTEMINTERMEASUREMENT_PERIOD_1		$= 0 \times 006 E$
SYSTEMINTERMEASUREMENT_PERIOD_0		$= 0 \times 006 F$
SYSTEM FRACTIONAL ENABLE		$= 0 \times 0070$,
SYSTEMGROUPED_PARAMETER_HOLD_0		$= 0 \times 0071$
SYSTEMTHRESH_HIGH		$= 0 \times 0072$,
SYSTEMTHRESH_HIGH_HI		$= 0 \times 0072$,
SYSTEMTHRESH_HIGH_LO		$= 0 \times 0073$,
SYSTEMTHRESH_LOW		$= 0 \times 0074$,
SYSTEMTHRESH_LOW_HI		$= 0 \times 0074$,
SYSTEMTHRESH_LOW_LO		= 0x0075,
SYSTEMENABLE_XTALK_PER_QUADRANT		= 0x0076,
SYSTEMSEED_CONFIG		= 0x0077,
SD_CONFIGWOI_SD0		= 0x0078,
SD_CONFIGWOI_SD1		= 0x0079,
SD_CONFIGINITIAL_PHASE_SD0		= 0x007A,
SD_CONFIGINITIAL_PHASE_SD1		= 0x007B,
SYSTEMGROUPED_PARAMETER_HOLD_1		= 0x007C,
SD_CONFIGFIRST_ORDER_SELECT		= 0x007D,
SD_CONFIGQUANTIFIER		= 0x007E,
ROI_CONFIGUSER_ROI_CENTRE_SPAD		= 0x007F,
ROI_CONFIGUSER_ROI_REQUESTED_GLOBAL_XY_SIZE		= 0x0080,
SYSTEMSEQUENCE_CONFIG		= 0x0081,
SYSTEMGROUPED_PARAMETER_HOLD		= 0x0082,
POWER_MANAGEMENTGO1_POWER_FORCE		= 0x0083,
SYSTEMSTREAM_COUNT_CTRL		$= 0 \times 0084$,
FIRMWAREENABLE		= 0x0085,
SYSTEMINTERRUPT_CLEAR		= 0x0086,
SYSTEMMODE_START		= 0x0087,
RESULTINTERRUPT_STATUS		= 0x0088,
RESULTRANGE_STATUS		= 0x0089,
RESULTREPORT_STATUS		= 0x008A,
RESULTSTREAM_COUNT		= 0x008B,
RESULTDSS_ACTUAL_EFFECTIVE_SPADS_SD0		= 0x008C,

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RESULT D	SS_ACTUAL_EFFECTIVE_SPADS_SD0_HI		=	0x008C,
	SS_ACTUAL_EFFECTIVE_SPADS_SD0_LO		=	0x008D,
	EAK SIGNAL COUNT RATE MCPS SD0		=	0x008E,
RESULTP	EAK_SIGNAL_COUNT_RATE_MCPS_SD0_HI		=	0x008E,
	EAK_SIGNAL_COUNT_RATE_MCPS_SD0_LO		=	0x008F,
	MBIENT_COUNT_RATE_MCPS_SD0		=	0x0090,
	MBIENT_COUNT_RATE_MCPS_SD0_HI		=	0x0090,
RESULTA	MBIENT_COUNT_RATE_MCPS_SD0_LO		=	0x0091,
RESULTS	IGMA_SD0		=	0x0092,
RESULTS	IGMA_SD0_HI		=	0x0092,
RESULTS	IGMA_SD0_LO		=	0x0093,
RESULTP	HASE_SD0		=	0x0094,
RESULTP	HASE_SD0_HI		=	0x0094,
RESULTP	HASE_SD0_LO		=	0x0095,
RESULTF	INAL_CROSSTALK_CORRECTED_RANGE_MM_SD0		=	0x0096,
RESULTF	INAL_CROSSTALK_CORRECTED_RANGE_MM_SD0_	HI	=	0x0096,
RESULTF	INAL_CROSSTALK_CORRECTED_RANGE_MM_SD0_	LO	=	0x0097,
	EAK_SIGNAL_COUNT_RATE_CROSSTALK_CORREC			0x0098,
RESULTP	EAK_SIGNAL_COUNT_RATE_CROSSTALK_CORREC	TED_MCPS_SD0_HI	=	0x0098,
RESULTP	EAK_SIGNAL_COUNT_RATE_CROSSTALK_CORREC	TED_MCPS_SD0_LO	=	0x0099,
RESULTM	M_INNER_ACTUAL_EFFECTIVE_SPADS_SD0		=	0x009A,
	M_INNER_ACTUAL_EFFECTIVE_SPADS_SD0_HI		=	0x009A,
	M_INNER_ACTUAL_EFFECTIVE_SPADS_SD0_LO		=	0x009B,
RESULTM	M_OUTER_ACTUAL_EFFECTIVE_SPADS_SD0		=	0x009C,
RESULTM	M_OUTER_ACTUAL_EFFECTIVE_SPADS_SD0_HI		=	0x009C,
RESULTM	M_OUTER_ACTUAL_EFFECTIVE_SPADS_SD0_LO		=	0x009D,
RESULTA	VG_SIGNAL_COUNT_RATE_MCPS_SD0		=	0x009E,
RESULTA	VG_SIGNAL_COUNT_RATE_MCPS_SD0_HI		=	0x009E,
RESULTA	VG_SIGNAL_COUNT_RATE_MCPS_SD0_LO		=	0x009F,
RESULTD	SS_ACTUAL_EFFECTIVE_SPADS_SD1		=	0x00A0,
RESULTD	SS_ACTUAL_EFFECTIVE_SPADS_SD1_HI		=	0x00A0,
RESULTD	SS_ACTUAL_EFFECTIVE_SPADS_SD1_LO		=	0x00A1,
RESULTP	EAK_SIGNAL_COUNT_RATE_MCPS_SD1		=	0x00A2,
RESULTP	EAK_SIGNAL_COUNT_RATE_MCPS_SD1_HI		=	0x00A2,
RESULTP	EAK_SIGNAL_COUNT_RATE_MCPS_SD1_LO		=	0x00A3,
RESULTA	MBIENT_COUNT_RATE_MCPS_SD1		=	0x00A4,
RESULTA	MBIENT_COUNT_RATE_MCPS_SD1_HI		=	0x00A4,
RESULTA	MBIENT_COUNT_RATE_MCPS_SD1_LO		=	0x00A5,
RESULTS	IGMA_SD1		=	0x00A6,
RESULTS	IGMA_SD1_HI		=	0x00A6,
RESULTS	IGMA_SD1_LO		=	0x00A7,
RESULTP	HASE_SD1		=	0x00A8,
RESULTP	HASE_SD1_HI		=	0x00A8,
RESULTP	HASE_SD1_LO		=	0x00A9,

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                                                                                        6
      RESULT FINAL CROSSTALK CORRECTED RANGE MM SD1
                                                                                    = 0x00AA
                                                                                    = 0x00AA
      RESULT FINAL CROSSTALK CORRECTED RANGE MM SD1 HI
      RESULT FINAL CROSSTALK CORRECTED RANGE MM SD1 LO
                                                                                    = 0x00AB
                                                                                    = 0x00AC
      RESULT__SPARE_0_SD1
      RESULT__SPARE_0_SD1_HI
                                                                                    = 0x00AC
      RESULT__SPARE_0_SD1_LO
                                                                                    = 0x00AD
      RESULT__SPARE_1_SD1
                                                                                    = 0x00AE
      RESULT SPARE 1 SD1 HI
                                                                                    = 0x00AE
      RESULT SPARE 1 SD1 LO
                                                                                    = 0x00AF
                                                                                    = 0x00B0,
      RESULT SPARE 2 SD1
      RESULT SPARE 2 SD1 HI
                                                                                    = 0x00B0,
      RESULT SPARE 2 SD1 LO
                                                                                    = 0x00B1,
      RESULT SPARE 3 SD1
                                                                                    = 0x00B2,
     RESULT THRESH INFO
                                                                                    = 0x00B3,
      RESULT CORE AMBIENT WINDOW EVENTS SD0
                                                                                    = 0x00B4
     RESULT_CORE__AMBIENT_WINDOW_EVENTS_SD0_3
                                                                                    = 0x00B4,
      RESULT_CORE__AMBIENT_WINDOW_EVENTS_SD0_2
                                                                                    = 0x00B5,
      RESULT CORE AMBIENT WINDOW EVENTS SD0 1
                                                                                    = 0x00B6
     RESULT_CORE__AMBIENT_WINDOW_EVENTS_SD0_0
                                                                                    = 0x00B7
                                                                                    = 0x00B8,
      RESULT CORE RANGING TOTAL EVENTS SD0
      RESULT CORE RANGING TOTAL EVENTS SD0 3
                                                                                    = 0x00B8
     RESULT CORE RANGING TOTAL EVENTS SD0 2
                                                                                    = 0x00B9
      RESULT CORE RANGING TOTAL EVENTS SD0 1
                                                                                    = 0x00BA
      RESULT CORE RANGING TOTAL EVENTS SD0 0
                                                                                    = 0x00BB
      RESULT_CORE__SIGNAL_TOTAL_EVENTS_SD0
                                                                                    = 0 \times 00 BC
      RESULT_CORE__SIGNAL_TOTAL_EVENTS_SD0_3
                                                                                    = 0 \times 00 BC
      RESULT CORE SIGNAL TOTAL EVENTS SD0 2
                                                                                    = 0x00BD
      RESULT_CORE__SIGNAL_TOTAL_EVENTS_SD0_1
                                                                                    = 0x00BE
     RESULT_CORE__SIGNAL_TOTAL_EVENTS_SD0_0
                                                                                    = 0x00BF
      RESULT CORE TOTAL PERIODS ELAPSED SD0
                                                                                    = 0 \times 00 C0,
      RESULT CORE TOTAL PERIODS ELAPSED SD0 3
                                                                                    = 0x00C0
     RESULT CORE TOTAL PERIODS ELAPSED SD0 2
                                                                                    = 0 \times 00 C1
      RESULT CORE TOTAL PERIODS ELAPSED SD0 1
                                                                                    = 0 \times 00 C2
      RESULT_CORE__TOTAL_PERIODS_ELAPSED_SD0_0
                                                                                    = 0x00C3,
      RESULT CORE AMBIENT WINDOW EVENTS SD1
                                                                                    = 0x00C4,
      RESULT_CORE__AMBIENT_WINDOW_EVENTS_SD1_3
                                                                                    = 0x00C4
      RESULT_CORE__AMBIENT_WINDOW_EVENTS_SD1_2
                                                                                    = 0x00C5,
      RESULT CORE AMBIENT WINDOW EVENTS SD1 1
                                                                                    = 0x00C6,
      RESULT CORE AMBIENT WINDOW EVENTS SD1 0
                                                                                    = 0 \times 00 C7
     RESULT_CORE__RANGING_TOTAL_EVENTS_SD1
                                                                                    = 0 \times 00008,
      RESULT CORE RANGING TOTAL EVENTS SD1 3
                                                                                    = 0x00C8,
      RESULT CORE RANGING TOTAL EVENTS SD1 2
                                                                                    = 0x00C9
      RESULT CORE RANGING TOTAL EVENTS SD1 1
                                                                                    = 0x00CA
      RESULT CORE RANGING TOTAL EVENTS SD1 0
                                                                                    = 0 \times 00 CB
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RESULT CORE SIGNAL TOTAL EVENTS SD1		= 0x00CC
RESULT CORE SIGNAL TOTAL EVENTS SD1 3		$= 0 \times 0 0 CC$
RESULT_CORESIGNAL_TOTAL_EVENTS_SD1_2		$= 0 \times 00 CD$
RESULT_CORESIGNAL_TOTAL_EVENTS_SD1_1		= 0x00CE,
RESULT_CORESIGNAL_TOTAL_EVENTS_SD1_0		= 0x00CF,
RESULT_CORETOTAL_PERIODS_ELAPSED_SD1		= 0x00D0,
RESULT_CORETOTAL_PERIODS_ELAPSED_SD1_3		= 0x00D0,
RESULT_CORETOTAL_PERIODS_ELAPSED_SD1_2		= 0x00D1,
RESULT_CORETOTAL_PERIODS_ELAPSED_SD1_1		= 0x00D2,
RESULT_CORETOTAL_PERIODS_ELAPSED_SD1_0		= 0x00D3,
RESULT_CORESPARE_0		= 0x00D4,
PHASECAL_RESULTREFERENCE_PHASE		= 0x00D6,
PHASECAL_RESULTREFERENCE_PHASE_HI		= 0x00D6,
PHASECAL_RESULTREFERENCE_PHASE_LO		= 0x00D7,
PHASECAL_RESULTVCSEL_START		= 0x00D8,
REF_SPAD_CHAR_RESULTNUM_ACTUAL_REF_SPADS		= 0x00D9,
REF_SPAD_CHAR_RESULTREF_LOCATION		= 0x00DA,
VHV_RESULTCOLDBOOT_STATUS		= 0x00DB,
VHV_RESULTSEARCH_RESULT		= 0x00DC,
VHV_RESULTLATEST_SETTING		= 0x00DD,
RESULTOSC_CALIBRATE_VAL		= 0x00DE,
RESULTOSC_CALIBRATE_VAL_HI		= 0x00DE,
RESULTOSC_CALIBRATE_VAL_LO		= 0x00DF,
ANA_CONFIGPOWERDOWN_GO1		= 0x00E0,
ANA_CONFIGREF_BG_CTRL		= 0x00E1,
ANA_CONFIGREGDVDD1V2_CTRL		= 0x00E2,
ANA_CONFIGOSC_SLOW_CTRL		= 0x00E3,
TEST_MODESTATUS		= 0x00E4,
FIRMWARESYSTEM_STATUS		= 0x00E5,
FIRMWAREMODE_STATUS		= 0x00E6,
FIRMWARESECONDARY_MODE_STATUS		= 0x00E7,
FIRMWARECAL_REPEAT_RATE_COUNTER		= 0x00E8,
FIRMWARECAL_REPEAT_RATE_COUNTER_HI		= 0x00E8,
FIRMWARECAL_REPEAT_RATE_COUNTER_LO		= 0x00E9,
FIRMWAREHISTOGRAM_BIN		= 0x00EA,
GPHSYSTEMTHRESH_HIGH		= 0x00EC,
GPHSYSTEMTHRESH_HIGH_HI		= 0x00EC,
GPHSYSTEMTHRESH_HIGH_LO		= 0x00ED,
GPHSYSTEMTHRESH_LOW		$= 0 \times 00 EE$,
GPHSYSTEMTHRESH_LOW_HI		= 0x00EE,
GPHSYSTEMTHRESH_LOW_LO		= 0x00EF,
GPHSYSTEMENABLE_XTALK_PER_QUADRANT		= 0x00F0,
GPHSPARE_0		$= 0 \times 0.0 \text{F1},$
GPHSD_CONFIGWOI_SD0		= 0x00F2,

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GPH_SD_CONFIG_WOI_SD1	= 0x00F3,
GPHSD_CONFIGINITIAL_PHASE_SD0	= 0x00F4,
GPH SD CONFIG INITIAL PHASE SD1	= 0x00F5
GPH_SD_CONFIGFIRST_ORDER_SELECT	= 0x00F6,
GPH SD CONFIG QUANTIFIER	= 0x00F7,
GPHROI_CONFIGUSER_ROI_CENTRE_SPAD	= 0x00F8,
GPH ROI CONFIG USER ROI REQUESTED GLOBAL XY SIZE	= 0x00F9,
GPH SYSTEM SEQUENCE CONFIG	= 0x00FA,
GPH GPH ID	= 0x00FB,
SYSTEMINTERRUPT_SET	= 0x00FC
INTERRUPT_MANAGERENABLES	= 0x00FD,
INTERRUPT_MANAGERCLEAR	= 0x00FE
INTERRUPT MANAGER STATUS	$= 0 \times 00 FF$
MCU TO HOST BANK WR ACCESS EN	$= 0 \times 0100$,
POWER_MANAGEMENTGO1_RESET_STATUS	$= 0 \times 0101$,
PAD_STARTUP_MODEVALUE_RO	$= 0 \times 0102$
PAD_STARTUP_MODEVALUE_CTRL	= 0x0103,
PLL PERIOD US	$= 0 \times 0104$
PLL PERIOD US 3	$= 0 \times 0104$
PLL PERIOD US 2	$= 0 \times 0105$
PLL_PERIOD_US_1	= 0x0106,
PLL_PERIOD_US_0	$= 0 \times 0107$,
INTERRUPT SCHEDULER DATA OUT	$= 0 \times 0108$
INTERRUPT SCHEDULER DATA OUT 3	= 0x0108,
INTERRUPT_SCHEDULERDATA_OUT_2	$= 0 \times 0109$
INTERRUPT_SCHEDULERDATA_OUT_1	= 0x010A,
INTERRUPT_SCHEDULERDATA_OUT_0	= 0x010B,
NVM BIST COMPLETE	= 0x010C,
NVM BIST STATUS	$= 0 \times 010 D$,
IDENTIFICATIONMODEL_ID	= 0x010F,
IDENTIFICATIONMODULE_TYPE	$= 0 \times 0110$,
IDENTIFICATION REVISION ID	$= 0 \times 0111$,
IDENTIFICATION MODULE ID	$= 0 \times 0112$
IDENTIFICATIONMODULE_ID_HI	$= 0 \times 0112$
IDENTIFICATION MODULE ID LO	$= 0 \times 0113$,
ANA_CONFIGFAST_OSCTRIM_MAX	$= 0 \times 0114$
ANA_CONFIGFAST_OSCFREQ_SET	$= 0 \times 0115$
ANA_CONFIGVCSEL_TRIM	$= 0 \times 0116$,
ANA CONFIG VCSEL SELION	$= 0 \times 0117$
ANA_CONFIGVCSEL_SELION_MAX	$= 0 \times 0118$,
PROTECTED_LASER_SAFETYLOCK_BIT	$= 0 \times 0119$,
LASER_SAFETYKEY	$= 0 \times 011A$
LASER_SAFETYKEY_RO	= 0x011B,
LASER_SAFETYCLIP	= 0x011C,
_	,

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LASER_SAFETYMULT		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_0		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_1		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_2		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_3		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_4		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_5		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_6		= 0x01
GLOBAL_CONFIGSPAD_ENABLES_RTN_7		= 0x01
GLOBAL_CONFIGSPAD_ENABLES_RTN_8		= 0x01
GLOBAL_CONFIGSPAD_ENABLES_RTN_9		= 0x01
GLOBAL_CONFIGSPAD_ENABLES_RTN_10		= 0x01
GLOBAL_CONFIGSPAD_ENABLES_RTN_11		= 0x01
GLOBAL_CONFIGSPAD_ENABLES_RTN_12		= 0x01
GLOBAL_CONFIGSPAD_ENABLES_RTN_13		= 0x01
GLOBAL_CONFIGSPAD_ENABLES_RTN_14		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_15		= 0x01
GLOBAL_CONFIGSPAD_ENABLES_RTN_16		= 0x01
GLOBAL_CONFIGSPAD_ENABLES_RTN_17		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_18		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_19		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_20		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_21		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_22		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_23		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_24		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_25		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_26		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_27		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_28		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_29		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_30		$= 0 \times 01$
GLOBAL_CONFIGSPAD_ENABLES_RTN_31		$= 0 \times 01$
ROI_CONFIGMODE_ROI_CENTRE_SPAD		$= 0 \times 01$
ROI_CONFIGMODE_ROI_XY_SIZE		$= 0 \times 01$
GO2_HOST_BANK_ACCESSOVERRIDE		$= 0 \times 03$
MCU_UTIL_MULTIPLIERMULTIPLICAND		$= 0 \times 04$
MCU_UTIL_MULTIPLIERMULTIPLICAND_3		$= 0 \times 04$
MCU_UTIL_MULTIPLIERMULTIPLICAND_2		$= 0 \times 04$
MCU_UTIL_MULTIPLIERMULTIPLICAND_1		$= 0 \times 04$
MCU_UTIL_MULTIPLIERMULTIPLICAND_0		$= 0 \times 04$
MCU_UTIL_MULTIPLIERMULTIPLIER		$= 0 \times 04$
MCU_UTIL_MULTIPLIERMULTIPLIER_3		$= 0 \times 04$
MCU_UTIL_MULTIPLIERMULTIPLIER_2		$= 0 \times 04$

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MCU_UTIL_MULTIPLIERMULTIPLIER_1		$= 0 \times 0406$
MCU_UTIL_MULTIPLIERMULTIPLIER_0		$= 0 \times 0407,$
MCU_UTIL_MULTIPLIERPRODUCT_HI		= 0x0408
MCU_UTIL_MULTIPLIERPRODUCT_HI_3		= 0x0408
MCU_UTIL_MULTIPLIERPRODUCT_HI_2		= 0x0409
MCU_UTIL_MULTIPLIERPRODUCT_HI_1		= 0x040A,
MCU_UTIL_MULTIPLIERPRODUCT_HI_0		= 0x040B
MCU_UTIL_MULTIPLIERPRODUCT_LO		= 0x040C
MCU_UTIL_MULTIPLIERPRODUCT_LO_3		= 0x040C
MCU_UTIL_MULTIPLIERPRODUCT_LO_2		$= 0 \times 040 D$
MCU_UTIL_MULTIPLIERPRODUCT_LO_1		$= 0 \times 040 E$
MCU_UTIL_MULTIPLIERPRODUCT_LO_0		$= 0 \times 040 F$
MCU_UTIL_MULTIPLIERSTART		$= 0 \times 0410$
MCU_UTIL_MULTIPLIERSTATUS		$= 0 \times 0411$
MCU UTIL DIVIDER START		$= 0 \times 0412$
MCU_UTIL_DIVIDERSTATUS		$= 0 \times 0413$
MCU UTIL DIVIDER DIVIDEND		$= 0 \times 0414$
MCU_UTIL_DIVIDERDIVIDEND_3		$= 0 \times 0414$
MCU_UTIL_DIVIDERDIVIDEND_2		= 0x0415
MCU UTIL DIVIDER DIVIDEND 1		= 0x0416
MCU_UTIL_DIVIDERDIVIDEND_0		= 0x0417
MCU_UTIL_DIVIDERDIVISOR		= 0x0418
MCU_UTIL_DIVIDERDIVISOR_3		= 0x0418
MCU_UTIL_DIVIDERDIVISOR_2		= 0x0419
MCU_UTIL_DIVIDERDIVISOR_1		= 0x041A
MCU_UTIL_DIVIDERDIVISOR_0		= 0x041B
MCU_UTIL_DIVIDERQUOTIENT		= 0x011B = $0x041C$
MCU_UTIL_DIVIDERQUOTIENT_3		= 0x011C
MCU_UTIL_DIVIDERQUOTIENT_2		= 0x0110 $= 0x041D$
MCU_UTIL_DIVIDERQUOTIENT_1		= 0x041E
MCU UTIL DIVIDER QUOTIENT 0		= 0x041E = $0x041F$
TIMERO VALUE IN		= 0x041F = $0x0420$
		•
TIMEROVALUE_IN_3		= 0x0420
TIMEROVALUE_IN_2		$= 0 \times 0421$
TIMEROVALUE_IN_1		$= 0 \times 0422$
TIMEROVALUE_IN_O		$= 0 \times 0423$
TIMER1VALUE_IN		$= 0 \times 0424$
TIMER1VALUE_IN_3		$= 0 \times 0424$
TIMER1VALUE_IN_2		$= 0 \times 0425$
TIMER1VALUE_IN_1		$= 0 \times 0426$
TIMER1VALUE_IN_0		$= 0 \times 0427$
TIMEROCTRL		$= 0 \times 0428$
TIMER1CTRL		$= 0 \times 0429,$
MCU_GENERAL_PURPOSEGP_0		$= 0 \times 042C,$

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MCU_GENERAL_PURPOSEGP_1		= 0x042D,
MCU_GENERAL_PURPOSEGP_2		$= 0 \times 042E$,
MCU_GENERAL_PURPOSEGP_3		$= 0 \times 042 F$,
MCU_RANGE_CALCCONFIG		= 0x0430,
MCU_RANGE_CALCOFFSET_CORRECTED_RANGE		$= 0 \times 0432$,
MCU_RANGE_CALCOFFSET_CORRECTED_RANGE_HI		= 0x0432,
MCU_RANGE_CALCOFFSET_CORRECTED_RANGE_LO		= 0x0433,
MCU_RANGE_CALCSPARE_4		$= 0 \times 0434$,
MCU_RANGE_CALCSPARE_4_3		$= 0 \times 0434$,
MCU_RANGE_CALCSPARE_4_2		= 0x0435,
MCU_RANGE_CALCSPARE_4_1		$= 0 \times 0436$,
MCU_RANGE_CALCSPARE_4_0		$= 0 \times 0437$,
MCU_RANGE_CALCAMBIENT_DURATION_PRE_CALC		= 0x0438,
MCU_RANGE_CALCAMBIENT_DURATION_PRE_CALC_HI		$= 0 \times 0438$,
MCU_RANGE_CALCAMBIENT_DURATION_PRE_CALC_LO		$= 0 \times 0439$,
MCU_RANGE_CALCALGO_VCSEL_PERIOD		$= 0 \times 043C$,
MCU_RANGE_CALCSPARE_5		= 0x043D,
MCU_RANGE_CALCALGO_TOTAL_PERIODS		$= 0 \times 043 E$,
MCU_RANGE_CALCALGO_TOTAL_PERIODS_HI		= 0x043E,
MCU_RANGE_CALCALGO_TOTAL_PERIODS_LO		$= 0 \times 043 F$,
MCU_RANGE_CALCALGO_ACCUM_PHASE		= 0x0440,
MCU_RANGE_CALCALGO_ACCUM_PHASE_3		= 0x0440,
MCU_RANGE_CALCALGO_ACCUM_PHASE_2		= 0x0441,
<pre>MCU_RANGE_CALCALGO_ACCUM_PHASE_1</pre>		= 0x0442,
<pre>MCU_RANGE_CALCALGO_ACCUM_PHASE_0</pre>		= 0x0443,
<pre>MCU_RANGE_CALCALGO_SIGNAL_EVENTS</pre>		= 0x0444,
<pre>MCU_RANGE_CALCALGO_SIGNAL_EVENTS_3</pre>		= 0x0444,
<pre>MCU_RANGE_CALCALGO_SIGNAL_EVENTS_2</pre>		= 0x0445,
<pre>MCU_RANGE_CALCALGO_SIGNAL_EVENTS_1</pre>		= 0x0446,
<pre>MCU_RANGE_CALCALGO_SIGNAL_EVENTS_0</pre>		= 0x0447,
MCU_RANGE_CALCALGO_AMBIENT_EVENTS		= 0x0448,
<pre>MCU_RANGE_CALCALGO_AMBIENT_EVENTS_3</pre>		= 0x0448,
<pre>MCU_RANGE_CALCALGO_AMBIENT_EVENTS_2</pre>		$= 0 \times 0449,$
MCU_RANGE_CALCALGO_AMBIENT_EVENTS_1		= 0x044A,
<pre>MCU_RANGE_CALCALGO_AMBIENT_EVENTS_0</pre>		= 0x044B,
MCU_RANGE_CALCSPARE_6		= 0x044C,
MCU_RANGE_CALCSPARE_6_HI		= 0x044C,
MCU_RANGE_CALCSPARE_6_LO		= 0x044D,
<pre>MCU_RANGE_CALCALGO_ADJUST_VCSEL_PERIOD</pre>		$= 0 \times 044 E,$
<pre>MCU_RANGE_CALCALGO_ADJUST_VCSEL_PERIOD_HI</pre>		$= 0 \times 044 E,$
MCU_RANGE_CALCALGO_ADJUST_VCSEL_PERIOD_LO		= 0x044F,
MCU_RANGE_CALCNUM_SPADS		$= 0 \times 0450,$
MCU_RANGE_CALCNUM_SPADS_HI		$= 0 \times 0450,$
MCU_RANGE_CALCNUM_SPADS_LO		= 0x0451,

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MCU_RANGE_CALCPHASE_OUTPUT	$= 0 \times 0452$,
MCU_RANGE_CALCPHASE_OUTPUT_HI	$= 0 \times 0452$,
MCU_RANGE_CALCPHASE_OUTPUT_LO	$= 0 \times 0453$,
MCU_RANGE_CALCRATE_PER_SPAD_MCPS	$= 0 \times 0454$,
MCU_RANGE_CALCRATE_PER_SPAD_MCPS_3	= 0x0454,
MCU_RANGE_CALCRATE_PER_SPAD_MCPS_2	= 0x0455,
MCU_RANGE_CALCRATE_PER_SPAD_MCPS_1	= 0x0456,
MCU_RANGE_CALCRATE_PER_SPAD_MCPS_0	= 0x0457,
MCU_RANGE_CALCSPARE_7	= 0x0458,
MCU_RANGE_CALCSPARE_8	= 0x0459,
MCU_RANGE_CALCPEAK_SIGNAL_RATE_MCPS	= 0x045A,
MCU_RANGE_CALCPEAK_SIGNAL_RATE_MCPS_HI	= 0x045A,
MCU_RANGE_CALCPEAK_SIGNAL_RATE_MCPS_LO	= 0x045B,
MCU_RANGE_CALCAVG_SIGNAL_RATE_MCPS	$= 0 \times 045C$
MCU_RANGE_CALCAVG_SIGNAL_RATE_MCPS_HI	$= 0 \times 045C$
MCU_RANGE_CALCAVG_SIGNAL_RATE_MCPS_LO	$= 0 \times 045D$
MCU RANGE CALC AMBIENT RATE MCPS	= 0x045E,
MCU RANGE CALC AMBIENT RATE MCPS HI	= 0x045E,
MCU_RANGE_CALCAMBIENT_RATE_MCPS_LO	= 0x045F,
MCU_RANGE_CALCXTALK	$= 0 \times 0460$
MCU_RANGE_CALCXTALK_HI	= 0x0460,
MCU RANGE CALC XTALK LO	= 0x0461,
MCU RANGE CALC CALC STATUS	$= 0 \times 0462$
MCU RANGE CALC DEBUG	$= 0 \times 0463$
MCU_RANGE_CALCPEAK_SIGNAL_RATE_XTALK_CORR_MCPS	= 0x0464,
MCU_RANGE_CALCPEAK_SIGNAL_RATE_XTALK_CORR_MCPS_HI	$= 0 \times 0464$
MCU_RANGE_CALCPEAK_SIGNAL_RATE_XTALK_CORR_MCPS_LO	= 0x0465,
MCU RANGE CALC SPARE 0	= 0x0468,
MCU_RANGE_CALCSPARE_1	= 0x0100, $= 0x0469$,
MCU_RANGE_CALCSPARE_2	= 0x0465, = $0x046A$,
MCU_RANGE_CALCSPARE_2 MCU_RANGE_CALCSPARE_3	= 0x046R, = $0x046B,$
	= 0x040B, $= 0x0470$,
PATCHCTRL	•
PATCHJMP_ENABLES	$= 0 \times 0472$
PATCHJMP_ENABLES_HI	$= 0 \times 0472$,
PATCHJMP_ENABLES_LO	$= 0 \times 0473$,
PATCHDATA_ENABLES	$= 0 \times 0474$
PATCHDATA_ENABLES_HI	$= 0 \times 0474$,
PATCHDATA_ENABLES_LO	$= 0 \times 0475$,
PATCH_OFFSET_0	$= 0 \times 0476$,
PATCHOFFSET_0_HI	$= 0 \times 0476$,
PATCHOFFSET_0_LO	$= 0 \times 0477$,
PATCHOFFSET_1	$= 0 \times 0478,$
PATCHOFFSET_1_HI	$= 0 \times 0478$,
PATCHOFFSET_1_LO	$= 0 \times 0479$,

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PATCHOFFSET_2		= 0x047A
PATCHOFFSET_2_HI		$= 0 \times 047 A$
PATCH_OFFSET_2_LO		$= 0 \times 047B$
PATCHOFFSET_3		$= 0 \times 047C$
PATCHOFFSET_3_HI		$= 0 \times 047C$
PATCHOFFSET_3_LO		$= 0 \times 047 D,$
PATCHOFFSET_4		= 0x047E,
PATCHOFFSET_4_HI		$= 0 \times 047E$,
PATCHOFFSET_4_LO		= 0x047F,
PATCHOFFSET_5		= 0x0480,
PATCHOFFSET_5_HI		= 0x0480,
PATCHOFFSET_5_LO		= 0x0481,
PATCHOFFSET_6		= 0x0482,
PATCHOFFSET_6_HI		= 0x0482,
PATCHOFFSET_6_LO		= 0x0483,
PATCHOFFSET_7		= 0x0484,
PATCHOFFSET_7_HI		$= 0 \times 0484$,
PATCHOFFSET_7_LO		= 0x0485,
PATCHOFFSET_8		= 0x0486,
PATCHOFFSET_8_HI		= 0x0486,
PATCHOFFSET_8_LO		= 0x0487,
PATCHOFFSET_9		= 0x0488,
PATCHOFFSET_9_HI		= 0x0488,
PATCHOFFSET_9_LO		= 0x0489,
PATCHOFFSET_10		= 0x048A,
PATCHOFFSET_10_HI		= 0x048A,
PATCHOFFSET_10_LO		= 0x048B,
PATCHOFFSET_11		= 0x048C,
PATCHOFFSET_11_HI		= 0x048C,
PATCHOFFSET_11_LO		= 0x048D,
PATCHOFFSET_12		= 0x048E,
PATCHOFFSET_12_HI		= 0x048E,
PATCHOFFSET_12_LO		= 0x048F,
PATCHOFFSET_13		= 0x0490,
PATCHOFFSET_13_HI		= 0x0490,
PATCHOFFSET_13_LO		= 0x0491,
PATCHOFFSET_14		= 0x0492,
PATCHOFFSET_14_HI		= 0x0492,
PATCHOFFSET_14_LO		= 0x0493,
PATCHOFFSET_15		= 0x0494,
PATCHOFFSET_15_HI		= 0x0494,
PATCHOFFSET_15_LO		= 0x0495,
PATCHADDRESS_0		= 0x0496,
PATCHADDRESS_0_HI		= 0x0496,

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PATCHADDRESS_0_LO		$= 0 \times 0497$
PATCH ADDRESS 1		$= 0 \times 0498$
PATCH ADDRESS 1 HI		= 0x0498,
PATCH_ADDRESS_1_LO		$= 0 \times 0499$
PATCH ADDRESS 2		$= 0 \times 049 A$
PATCHADDRESS_2_HI		$= 0 \times 049 A$
PATCHADDRESS_2_LO		= 0x049B,
PATCH_ADDRESS_3		= 0x049C,
PATCH_ADDRESS_3_HI		= 0x049C,
PATCHADDRESS_3_LO		$= 0 \times 049 D,$
PATCHADDRESS_4		= 0x049E
PATCHADDRESS_4_HI		$= 0 \times 049 E$,
PATCHADDRESS_4_LO		$= 0 \times 049 F$,
PATCHADDRESS_5		$= 0 \times 04A0$,
PATCHADDRESS_5_HI		$= 0 \times 04 A0$,
PATCHADDRESS_5_LO		$= 0 \times 04 A1$,
PATCHADDRESS_6		$= 0 \times 04A2$,
PATCHADDRESS_6_HI		= 0x04A2,
PATCHADDRESS_6_LO		= 0x04A3,
PATCHADDRESS_7		$= 0 \times 04A4$,
PATCHADDRESS_7_HI		= 0x04A4,
PATCHADDRESS_7_LO		= 0x04A5,
PATCHADDRESS_8		= 0x04A6,
PATCHADDRESS_8_HI		= 0x04A6,
PATCHADDRESS_8_LO		= 0x04A7,
PATCHADDRESS_9		= 0x04A8,
PATCHADDRESS_9_HI		= 0x04A8,
PATCHADDRESS_9_LO		= 0x04A9,
PATCHADDRESS_10		= 0x04AA,
PATCHADDRESS_10_HI		= 0x04AA,
PATCHADDRESS_10_LO		= 0x04AB,
PATCHADDRESS_11		= 0x04AC,
PATCHADDRESS_11_HI		= 0x04AC,
PATCHADDRESS_11_LO		= 0x04AD,
PATCHADDRESS_12		= 0x04AE,
PATCHADDRESS_12_HI		$= 0 \times 04 AE$,
PATCHADDRESS_12_LO		= 0x04AF,
PATCHADDRESS_13		= 0x04B0,
PATCHADDRESS_13_HI		= 0x04B0,
PATCHADDRESS_13_LO		= 0x04B1,
PATCHADDRESS_14		= 0x04B2,
PATCHADDRESS_14_HI		= 0x04B2,
PATCHADDRESS_14_LO		= 0x04B3,
PATCHADDRESS_15		= 0x04B4,

PATCH_ADDRESS_15_HI	opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.h	Thu Mar 14 06:25:24 2019	15
PATCH	PATCH ADDRESS 15 HI		= 0x04B4,
SPI_ASYNC_MUX_CTRL = 0x04C0, CLK_CONFIG = 0x04C1, GPIO_LV_MUX_CTRL = 0x04CC, GPIO_LV_PAD_CTRL = 0x04CC, GPIO_LV_PAD_CTRL = 0x04D0, PAD_IZC_LV_CONFIG = 0x04D0, PAD_STARTUP_MODE_VALUE_RO_GOI = 0x04D4, HOST_IF_STATUS_GOI = 0x04D4, MCU_CLK_GATING_CTRL = 0x04D8, TEST_BIST_ROM_CTRL = 0x04E2, TEST_BIST_ROM_RESULT = 0x04E2, TEST_BIST_ROM_MCU_SIG = 0x04E2, TEST_BIST_ROM_MCU_SIG_HI = 0x04E2, TEST_BIST_ROM_CTRL = 0x04E2, TEST_BIST_ROM_CTRL = 0x04E2, TEST_BIST_RAM_CTRL = 0x04E3, TEST_BIST_RAM_RESULT = 0x04E5, TEST_DLL_BIST_MIN_THRESHOLD = 0x04F0, TEST_PLL_BIST_MIN_THRESHOLD HI = 0x04F0, TEST_PLL_BIST_MIN_THRESHOLD LO = 0x04F1, TEST_PLL_BIST_MAX_THRESHOLD LO = 0x04F2, TEST_PLL_BIST_MAX_THRESHOLD LO = 0x04F4, TEST_PLL_BIST_COUNT_OUT_HI = 0x04F4, TEST_PLL_BIST_COUNT_OUT_HI = 0x04F4, TEST_PLL_BIST_COUNT_OUT_HI = 0x04F4,			= 0x04B5,
CLKCONTG = 0x04c4, GPIO_LV_MUXCTRL = 0x04c0, GPIO_LV_PADCTRL = 0x04c0, PAD_L3C_LVCONFIG = 0x04d0, PAD_STARTUP_MODEVALUE_RO_GO1 = 0x04D4, HOST_IFSTATUS_GO1 = 0x04D5, MCU_CLK_GATINGCTRL = 0x04E1, TESTBIST_ROM_CTRL = 0x04e1, TESTBIST_ROM_MCU_SIG = 0x04e2, TESTBIST_ROM_MCU_SIG_HI = 0x04e2, TESTBIST_ROM_MCU_SIG_LO = 0x04e2, TESTBIST_RAM_CTRL = 0x04e2, TESTBIST_RAM_CTRL = 0x04e4, TESTBIST_RAM_TERSHOLD = 0x04e3, TESTPLL_BIST_MIN_THRESHOLD HI = 0x04e6, TESTPLL_BIST_MIN_THRESHOLD LO = 0x04e7, TESTPLL_BIST_MAX_THRESHOLD HI = 0x04e7, TESTPLL_BIST_MAX_THRESHOLD HI = 0x04e7, TESTPLL_BIST_COUNT_OUT_HI = 0x04e7, TESTPLL_BIST_COUNT_OUT_HI = 0x04e7, TESTPLL_BIST_COUNT_OUT_HI = 0x04e7, TESTPLL_BIST_COUNT_OUT_HO = 0x0e7, TESTPLL_BIST_COUNT_OUT_HO = 0x0e7, TESTPLL_BIST_COUNT_OUT_HO = 0x0e6, TESTPLL			•
GPIO_LV_MUX_CTRL GPIO_LV_PAD_CTRL GPIO_LV_PAD_CTRL GPIO_LV_PAD_CTRL GPIO_LV_PAD_CTRL GPIO_LV_PAD_CTRL GPIO_LV_PAD_CTRL GPIO_LV_PAD_CTRL GPIO_LV_CONFIG GRAND			· ·
GPIO_LV_PAD_CTRL = 0x04DD, PAD_STARTUP_MODE_VALUE_RO_GO1 = 0x04Dd, HOST_IF_STATUS_GO1 = 0x04Dd, MCU_CLK_GATING_CTRL = 0x04DE, TEST_BIST_ROM_CTRL = 0x04E0, TEST_BIST_ROM_RESULT = 0x04E2, TEST_BIST_ROM_MCU_SIG = 0x04E2, TEST_BIST_ROM_MCU_SIG HI = 0x04E2, TEST_BIST_ROM_MCU_SIG LO = 0x04E2, TEST_BIST_RAM_CTRL = 0x04E4, TEST_BIST_RAM_CTRL = 0x04E4, TEST_BIST_RAM_RESULT = 0x04E6, TEST_TMC = 0x04E6, TEST_TIMC = 0x04E6, TEST_PLL_BIST_MIN_THRESHOLD = 0x04E6, TEST_PLL_BIST_MIN_THRESHOLD LO = 0x04F0, TEST_PLL_BIST_MAX_THRESHOLD_LO = 0x04F1, TEST_PLL_BIST_MAX_THRESHOLD_HI = 0x04F2, TEST_PLL_BIST_COUNT_OUT_HI = 0x04F2, TEST_PLL_BIST_COUNT_OUT_HI = 0x04F4, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F4, TEST_PLL_BIST_COUNT_OUT_LO = 0x06F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x06F6, RANGING_CORE_REVISION_ID			· ·
PAD_IZC_LV_CONFIG PAD_STARTUP_MODE_VALUE_RO_GO1			•
HOST_IF_STATUS_GOI			= 0x04D0,
HOST_IF_STATUS_GOI			$= 0 \times 04 D4$
MCU_CLK_GATING_CTRL = 0x04DB, TEST_BIST_ROM_CTRL = 0x04E1, TEST_BIST_ROM_RESULT = 0x04E2, TEST_BIST_ROM_MCU_SIG = 0x04E2, TEST_BIST_ROM_MCU_SIG_HI = 0x04E2, TEST_BIST_RAM_CTRL = 0x04E4, TEST_BIST_RAM_CTRL = 0x04E4, TEST_BIST_RAM_RESULT = 0x04E8, TEST_TIMC = 0x04E8, TEST_PLL_BIST_MIN_THRESHOLD = 0x04F0, TEST_PLL_BIST_MIN_THRESHOLD_HI = 0x04F1, TEST_PLL_BIST_MX_THRESHOLD_HI = 0x04F2, TEST_PLL_BIST_MX_THRESHOLD_HI = 0x04F2, TEST_PLL_BIST_MX_THRESHOLD_HI = 0x04F2, TEST_PLL_BIST_COUNT_OUT = 0x04F2, TEST_PLL_BIST_COUNT_OUT = 0x04F4, TEST_PLL_BIST_COUNT_OUT_HI = 0x04F4, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x06F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x06F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x06F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x06F6, TEST_PLL_B			= 0x04D5,
TEST_BIST_ROM_CTRL = 0x04E0, TEST_BIST_ROM_MCU_SIG = 0x04E2, TEST_BIST_ROM_MCU_SIG_HI = 0x04E2, TEST_BIST_ROM_MCU_SIG_LO = 0x04E4, TEST_BIST_RAM_CTRL = 0x04E4, TEST_BIST_RAM_CTRL = 0x04E5, TEST_BIST_RAM_RESULT = 0x04E5, TEST_TMC = 0x04E6, TEST_PLL_BIST_MIN_THRESHOLD = 0x04F0, TEST_PLL_BIST_MIN_THRESHOLD_HI = 0x04F0, TEST_PLL_BIST_MX_THRESHOLD_HI = 0x04F2, TEST_PLL_BIST_MX_THRESHOLD_HI = 0x04F2, TEST_PLL_BIST_MX_THRESHOLD_HI = 0x04F2, TEST_PLL_BIST_COUNT_OUT = 0x04F4, TEST_PLL_BIST_COUNT_OUT = 0x04F4, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F4, TEST_PLL_BIST_CONCO = 0x04F6, TEST_PLL_BIST_CONCO = 0x04F6, TEST_PLL_BIST_CTRL = 0x04F6, TEST_PLL_BIST_CTRL = 0x0680, RANGING_CORE_DEVICE_ID = 0x0680, RANGING_CORE_DEVICE_ID = 0x0680, RANGING_CORE_DEVICE_ID = 0x0680, RANGING_CORE_DEVICE_TEST_ARMGING	_		
TEST_BIST_ROM_RESULT			•
TEST_BIST_ROM_MCU_SIG TEST_BIST_ROM_MCU_SIG_HI			•
TEST_BIST_ROM_MCU_SIG_HI = 0x04E2, TEST_BIST_ROM_MCU_SIG_LO = 0x04E3, TEST_BIST_RAM_CTRL = 0x04E4, TEST_BIST_RAM_CTRL = 0x04E5, TEST_BIST_RAM_RESULT = 0x04E5, TEST_TMC = 0x04F0, TEST_PLL_BIST_MIN_THRESHOLD = 0x04F0, TEST_PLL_BIST_MIN_THRESHOLD_HI = 0x04F0, TEST_PLL_BIST_MIN_THRESHOLD_LO = 0x04F1, TEST_PLL_BIST_MAX_THRESHOLD_LO = 0x04F1, TEST_PLL_BIST_MAX_THRESHOLD_HI = 0x04F2, TEST_PLL_BIST_MAX_THRESHOLD_LO = 0x04F3, TEST_PLL_BIST_COUNT_OUT = 0x04F3, TEST_PLL_BIST_COUNT_OUT = 0x04F4, TEST_PLL_BIST_COUNT_OUT_HI = 0x04F4, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F5, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x0680, RANGING_CORE_DEVICE_ID = 0x0680, RANGING_CORE_DEVICE_ID = 0x0681, RANGING_CORE_CLK_CTRL1 = 0x0681, RANGING_CORE_CLK_CTRL1 = 0x0681, RANGING_CORE_CLK_CTRL2 = 0x0684, RANGING_CORE_CLK_CTRL2 = 0x0684, RANGING_CORE_CLK_CTRL2 = 0x0684, RANGING_CORE_LLM_INIT_1 = 0x0687, RANGING_CORE_LLM_LIMIT_1 = 0x0689, RANGING_CORE_LOW_LIMIT_REF_1 = 0x0699, RANGING_CORE_LOW_LIMIT_REF_1 = 0x0699, RANGING_CORE_QUANTIFIER_LMSB = 0x0694, RANGING_CORE_QUANTIFIER_LBSB = 0x0696, RANGING_CORE_QUANTIFIER_LBSB = 0x0696,			•
TEST_BIST_RAM_CTRL			$= 0 \times 04 E2$
TEST_BIST_RAM_CTRL			•
TEST_BIST_RAM_RESULT = 0x04E5, TEST_TMC = 0x04F8, TEST_PLL_BIST_MIN_THRESHOLD = 0x04F0, TEST_PLL_BIST_MIN_THRESHOLD_HI = 0x04F0, TEST_PLL_BIST_MIN_THRESHOLD_LO = 0x04F1, TEST_PLL_BIST_MAX_THRESHOLD_LO = 0x04F1, TEST_PLL_BIST_MAX_THRESHOLD_LO = 0x04F2, TEST_PLL_BIST_MAX_THRESHOLD_LO = 0x04F2, TEST_PLL_BIST_MAX_THRESHOLD_LO = 0x04F3, TEST_PLL_BIST_COUNT_OUT = 0x04F4, TEST_PLL_BIST_COUNT_OUT_HI = 0x04F4, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x06F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x06F1, RANGING_CORE_CLK_CTRL1 = 0x06F1, RANGING_CORE_CLK_CTRL1 = 0x06F1, RANGING_CORE_CLK_CTRL2 = 0x06F1, RANGING_CORE_LOW_LIMIT_1 = 0x06F1, RANGING_CORE_LOW_LIMIT_REF_1 = 0x06F1, RANGING_CORE_LOW_LIMIT_REF_1 = 0x06F2, RANGING_CORE_QUANTIFIER_LMSB = 0x06F5, RANGING_CORE_QUANTIFIER_LLSB = 0x06F5, RANGING_CORE_QUANTIFIE			•
TEST_TMC			= 0x04E5,
TEST_PLL_BIST_MIN_THRESHOLD = 0x04F0, TEST_PLL_BIST_MIN_THRESHOLD_HI = 0x04F0, TEST_PLL_BIST_MIN_THRESHOLD_LO = 0x04F1, TEST_PLL_BIST_MAX_THRESHOLD = 0x04F2, TEST_PLL_BIST_MAX_THRESHOLD_HI = 0x04F3, TEST_PLL_BIST_COUNT_OUT = 0x04F4, TEST_PLL_BIST_COUNT_OUT_HI = 0x04F4, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F5, TEST_PLL_BIST_GONGO = 0x04F6, TEST_PLL_BIST_CTRL = 0x04F7, RANGING_CORE_DEVICE_ID = 0x0680, RANGING_CORE_REVISION_ID = 0x0681, RANGING_CORE_CLK_CTRL1 = 0x0681, RANGING_CORE_CLK_CTRL2 = 0x0684, RANGING_CORE_WOI_1 = 0x0684, RANGING_CORE_WOI_REF_1 = 0x0686, RANGING_CORE_START_RANGING = 0x0687, RANGING_CORE_LOW_LIMIT_1 = 0x0690, RANGING_CORE_LOW_LIMIT_1 = 0x0691, RANGING_CORE_LOW_LIMIT_REF_1 = 0x0692, RANGING_CORE_LOW_LIMIT_REF_1 = 0x0693, RANGING_CORE_QUANTIFIER_1_MSB = 0x0694, RANGING_CORE_QUANTIFIER_1_LSB = 0x0696,			= 0x04E8,
TEST_PLL_BIST_MIN_THRESHOLD_HI = 0x04F0, TEST_PLL_BIST_MIN_THRESHOLD_LO = 0x04F1, TEST_PLL_BIST_MAX_THRESHOLD = 0x04F2, TEST_PLL_BIST_MAX_THRESHOLD_HI = 0x04F2, TEST_PLL_BIST_MAX_THRESHOLD_HI = 0x04F3, TEST_PLL_BIST_COUNT_OUT = 0x04F4, TEST_PLL_BIST_COUNT_OUT_HI = 0x04F4, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F5, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F5, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F5, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F6, TEST_PLL_BIST_COUNT_OUT_LO = 0x0680, RANGING_CORE_DEVICE_ID = 0x0680, RANGING_CORE_DEVICE_ID = 0x0681, RANGING_CORE_CLK_CTRL1 = 0x0681, RANGING_CORE_CLK_CTRL1 = 0x0681, RANGING_CORE_CLK_CTRL2 = 0x0684, RANGING_CORE_WOI_1 = 0x0685, RANGING_CORE_WOI_1 = 0x0686, RANGING_CORE_WOI_TEF_1 = 0x0686, RANGING_CORE_START_RANGING = 0x0687, RANGING_CORE_LOW_LIMIT_1 = 0x0691, RANGING_CORE_LOW_LIMIT_REF_1 = 0x0692, RANGING_CORE_LOW_LIMIT_REF_1 = 0x0693, RANGING_CORE_QUANTIFIER_1_MSB = 0x0694, RANGING_CORE_QUANTIFIER_1_LSB = 0x0696, RANGING_CORE_QUANTIFIER_REF_1_MSB = 0x0696, RANGING_CORE_QUANTIFIER_REF_1_MSB = 0x0696,	TEST PLL BIST MIN THRESHOLD		= 0x04F0,
TEST_PLL_BIST_MIN_THRESHOLD_LO			= 0x04F0,
TEST_PLL_BIST_MAX_THRESHOLD_HI = 0x04F2, TEST_PLL_BIST_MAX_THRESHOLD_LO = 0x04F3, TEST_PLL_BIST_COUNT_OUT = 0x04F4, TEST_PLL_BIST_COUNT_OUT_HI = 0x04F4, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F5, TEST_PLL_BIST_GONOGO = 0x04F6, TEST_PLL_BIST_CTRL = 0x04F7, RANGING_CORE_DEVICE_ID = 0x0680, RANGING_CORE_REVISION_ID = 0x0681, RANGING_CORE_CLK_CTRL1 = 0x0683, RANGING_CORE_CLK_CTRL2 = 0x0684, RANGING_CORE_WOI_1 = 0x0685, RANGING_CORE_WOI_REF_1 = 0x0686, RANGING_CORE_START_RANGING = 0x0687, RANGING_CORE_LOW_LIMIT_1 = 0x0690, RANGING_CORE_LOW_LIMIT_REF_1 = 0x0691, RANGING_CORE_HIGH_LIMIT_REF_1 = 0x0692, RANGING_CORE_QUANTIFIER_1_MSB = 0x0694, RANGING_CORE_QUANTIFIER_LISB = 0x0696, RANGING_CORE_QUANTIFIER_REF_1_MSB = 0x0696,			= 0x04F1,
TEST_PLL_BIST_MAX_THRESHOLD_HI = 0x04F2, TEST_PLL_BIST_MAX_THRESHOLD_LO = 0x04F3, TEST_PLL_BIST_COUNT_OUT = 0x04F4, TEST_PLL_BIST_COUNT_OUT_HI = 0x04F4, TEST_PLL_BIST_COUNT_OUT_LO = 0x04F5, TEST_PLL_BIST_GONOGO = 0x04F6, TEST_PLL_BIST_CTRL = 0x04F7, RANGING_CORE_DEVICE_ID = 0x0680, RANGING_CORE_REVISION_ID = 0x0681, RANGING_CORE_CLK_CTRL1 = 0x0683, RANGING_CORE_CLK_CTRL2 = 0x0684, RANGING_CORE_WOI_1 = 0x0685, RANGING_CORE_WOI_REF_1 = 0x0686, RANGING_CORE_START_RANGING = 0x0687, RANGING_CORE_LOW_LIMIT_1 = 0x0690, RANGING_CORE_LOW_LIMIT_REF_1 = 0x0691, RANGING_CORE_HIGH_LIMIT_REF_1 = 0x0692, RANGING_CORE_QUANTIFIER_1_MSB = 0x0694, RANGING_CORE_QUANTIFIER_LISB = 0x0696, RANGING_CORE_QUANTIFIER_REF_1_MSB = 0x0696,	TESTPLL_BIST_MAX_THRESHOLD		= 0x04F2,
TESTPLL_BIST_COUNT_OUT TESTPLL_BIST_COUNT_OUT_HI TESTPLL_BIST_COUNT_OUT_LO = 0x04F4, TESTPLL_BIST_COUNT_OUT_LO = 0x04F5, TESTPLL_BIST_GONOGO = 0x04F6, TESTPLL_BIST_CTRL = 0x04F7, RANGING_COREDEVICE_ID = 0x0680, RANGING_COREREVISION_ID = 0x0681, RANGING_CORECLK_CTRL1 = 0x0683, RANGING_CORECLK_CTRL2 = 0x0684, RANGING_COREWOI_1 = 0x0685, RANGING_COREWOI_REF_1 = 0x0686, RANGING_CORESTART_RANGING = 0x0687, RANGING_CORESTART_RANGING = 0x0687, RANGING_CORELOW_LIMIT_1 = 0x0690, RANGING_CORELOW_LIMIT_REF_1 = 0x0691, RANGING_CORELOW_LIMIT_REF_1 = 0x0692, RANGING_COREQUANTIFIER_1_MSB = 0x0695, RANGING_COREQUANTIFIER_TLSB = 0x0696,			= 0x04F2,
TESTPLL_BIST_COUNT_OUT_HI	TESTPLL_BIST_MAX_THRESHOLD_LO		= 0x04F3,
TESTPLL_BIST_COUNT_OUT_LO = 0x04F5, TESTPLL_BIST_GONOGO = 0x04F6, TESTPLL_BIST_CTRL = 0x04F7, RANGING_COREDEVICE_ID = 0x0680, RANGING_COREREVISION_ID = 0x0681, RANGING_CORECLK_CTRL1 = 0x0683, RANGING_CORECLK_CTRL2 = 0x0684, RANGING_COREWOI_1 = 0x0685, RANGING_COREWOI_REF_1 = 0x0686, RANGING_CORESTART_RANGING = 0x0687, RANGING_CORELOW_LIMIT_1 = 0x0690, RANGING_COREHIGH_LIMIT_1 = 0x0691, RANGING_CORELOW_LIMIT_REF_1 = 0x0692, RANGING_COREQUANTIFIER_1_MSB = 0x0694, RANGING_COREQUANTIFIER_1_LSB = 0x0695, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0695,	TESTPLL_BIST_COUNT_OUT		$= 0 \times 04 F4$,
TESTPLL_BIST_GONOGO	TESTPLL_BIST_COUNT_OUT_HI		$= 0 \times 04 F4$,
TESTPLL_BIST_CTRL	TESTPLL_BIST_COUNT_OUT_LO		= 0x04F5,
RANGING_COREDEVICE_ID	TESTPLL_BIST_GONOGO		= 0x04F6,
RANGING_COREREVISION_ID = 0x0681, RANGING_CORECLK_CTRL1 = 0x0683, RANGING_CORECLK_CTRL2 = 0x0684, RANGING_COREWOI_1 = 0x0685, RANGING_COREWOI_REF_1 = 0x0686, RANGING_CORESTART_RANGING = 0x0687, RANGING_CORELOW_LIMIT_1 = 0x0690, RANGING_COREHIGH_LIMIT_1 = 0x0691, RANGING_CORELOW_LIMIT_REF_1 = 0x0692, RANGING_COREHIGH_LIMIT_REF_1 = 0x0693, RANGING_COREQUANTIFIER_1_MSB = 0x0694, RANGING_COREQUANTIFIER_1_LSB = 0x0696, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0696,	TESTPLL_BIST_CTRL		= 0x04F7,
RANGING_CORECLK_CTRL1	RANGING_COREDEVICE_ID		= 0x0680,
RANGING_CORECLK_CTRL2 = 0x0684, RANGING_COREWOI_1 = 0x0685, RANGING_COREWOI_REF_1 = 0x0686, RANGING_CORESTART_RANGING = 0x0687, RANGING_CORELOW_LIMIT_1 = 0x0690, RANGING_COREHIGH_LIMIT_1 = 0x0691, RANGING_CORELOW_LIMIT_REF_1 = 0x0692, RANGING_COREHIGH_LIMIT_REF_1 = 0x0693, RANGING_COREQUANTIFIER_1_MSB = 0x0694, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0695, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0696,	RANGING_COREREVISION_ID		= 0x0681,
RANGING_COREWOI_1 = 0x0685, RANGING_COREWOI_REF_1 = 0x0686, RANGING_CORESTART_RANGING = 0x0687, RANGING_CORELOW_LIMIT_1 = 0x0690, RANGING_COREHIGH_LIMIT_1 = 0x0691, RANGING_CORELOW_LIMIT_REF_1 = 0x0692, RANGING_COREHIGH_LIMIT_REF_1 = 0x0693, RANGING_COREQUANTIFIER_1_MSB = 0x0694, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0695, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0696,	RANGING_CORECLK_CTRL1		= 0x0683,
RANGING_COREWOI_REF_1 = 0x0686, RANGING_CORESTART_RANGING = 0x0687, RANGING_CORELOW_LIMIT_1 = 0x0690, RANGING_COREHIGH_LIMIT_1 = 0x0691, RANGING_CORELOW_LIMIT_REF_1 = 0x0692, RANGING_COREHIGH_LIMIT_REF_1 = 0x0693, RANGING_COREQUANTIFIER_1_MSB = 0x0694, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0695, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0696,	RANGING_CORECLK_CTRL2		= 0x0684,
RANGING_CORESTART_RANGING = 0x0687, RANGING_CORELOW_LIMIT_1 = 0x0690, RANGING_COREHIGH_LIMIT_1 = 0x0691, RANGING_CORELOW_LIMIT_REF_1 = 0x0692, RANGING_COREHIGH_LIMIT_REF_1 = 0x0693, RANGING_COREQUANTIFIER_1_MSB = 0x0694, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0695, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0696,	RANGING_COREWOI_1		= 0x0685,
RANGING_CORELOW_LIMIT_1 = 0x0690, RANGING_COREHIGH_LIMIT_1 = 0x0691, RANGING_CORELOW_LIMIT_REF_1 = 0x0692, RANGING_COREHIGH_LIMIT_REF_1 = 0x0693, RANGING_COREQUANTIFIER_1_MSB = 0x0694, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0695, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0696,	RANGING_COREWOI_REF_1		= 0x0686,
RANGING_COREHIGH_LIMIT_1 = 0x0691, RANGING_CORELOW_LIMIT_REF_1 = 0x0692, RANGING_COREHIGH_LIMIT_REF_1 = 0x0693, RANGING_COREQUANTIFIER_1_MSB = 0x0694, RANGING_COREQUANTIFIER_1_LSB = 0x0695, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0696,	RANGING_CORESTART_RANGING		= 0x0687,
RANGING_CORE_LOW_LIMIT_REF_1 = 0x0692, RANGING_CORE_HIGH_LIMIT_REF_1 = 0x0693, RANGING_CORE_QUANTIFIER_1_MSB = 0x0694, RANGING_CORE_QUANTIFIER_1_LSB = 0x0695, RANGING_CORE_QUANTIFIER_REF_1_MSB = 0x0696,	RANGING_CORELOW_LIMIT_1		= 0x0690,
RANGING_COREHIGH_LIMIT_REF_1 = 0x0693, RANGING_COREQUANTIFIER_1_MSB = 0x0694, RANGING_COREQUANTIFIER_1_LSB = 0x0695, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0696,	RANGING_COREHIGH_LIMIT_1		= 0x0691,
RANGING_COREQUANTIFIER_1_MSB = 0x0694, RANGING_COREQUANTIFIER_1_LSB = 0x0695, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0696,	RANGING_CORELOW_LIMIT_REF_1		= 0x0692,
RANGING_COREQUANTIFIER_1_LSB = 0x0695, RANGING_COREQUANTIFIER_REF_1_MSB = 0x0696,	RANGING_COREHIGH_LIMIT_REF_1		= 0x0693,
RANGING_COREQUANTIFIER_REF_1_MSB = 0×0696 ,	RANGING_COREQUANTIFIER_1_MSB		= 0x0694,
	RANGING_COREQUANTIFIER_1_LSB		= 0x0695,
$RANGING_CORE__QUANTIFIER_REF_1_LSB = 0x0697,$	RANGING_COREQUANTIFIER_REF_1_MSB		= 0x0696,
	RANGING_COREQUANTIFIER_REF_1_LSB		= 0x0697,

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RANGING_CORE_	_AMBIENT_OFFSET_1_MSB					=	0x0698,
	_AMBIENT_OFFSET_1_LSB					=	0x0699,
RANGING_CORE_	_AMBIENT_OFFSET_REF_1_MSB					=	0x069A,
RANGING_CORE_	_AMBIENT_OFFSET_REF_1_LSB					=	0x069B,
RANGING_CORE_	_FILTER_STRENGTH_1					=	0x069C,
	_FILTER_STRENGTH_REF_1					=	0x069D,
RANGING_CORE_	_SIGNAL_EVENT_LIMIT_1_MSB					=	0x069E,
RANGING_CORE_	_SIGNAL_EVENT_LIMIT_1_LSB					=	0x069F,
RANGING_CORE_	_SIGNAL_EVENT_LIMIT_REF_1_MSB					=	0x06A0,
RANGING_CORE_	_SIGNAL_EVENT_LIMIT_REF_1_LSB					=	0x06A1,
RANGING_CORE_	_TIMEOUT_OVERALL_PERIODS_MSB					=	$0 \times 0 6 A4$,
RANGING_CORE_	_TIMEOUT_OVERALL_PERIODS_LSB					=	0x06A5,
RANGING_CORE_	_INVERT_HW					=	0x06A6,
RANGING_CORE_	_FORCE_HW					=	0x06A7,
RANGING_CORE_	_STATIC_HW_VALUE					=	0x06A8,
RANGING_CORE_	_FORCE_CONTINUOUS_AMBIENT					=	0x06A9,
RANGING_CORE_	_TEST_PHASE_SELECT_TO_FILTER					=	0x06AA,
RANGING_CORE_	_TEST_PHASE_SELECT_TO_TIMING_GEN					=	0x06AB,
RANGING_CORE_	_INITIAL_PHASE_VALUE_1					=	0x06AC,
RANGING_CORE_	_INITIAL_PHASE_VALUE_REF_1					=	0x06AD,
RANGING_CORE_	_FORCE_UP_IN					=	0x06AE,
RANGING_CORE_	_FORCE_DN_IN					=	0x06AF,
RANGING_CORE_	_STATIC_UP_VALUE_1					=	0x06B0,
RANGING_CORE_	_STATIC_UP_VALUE_REF_1					=	0x06B1,
RANGING_CORE_	_STATIC_DN_VALUE_1					=	0x06B2,
RANGING_CORE_	_STATIC_DN_VALUE_REF_1					=	0x06B3,
RANGING_CORE_	_MONITOR_UP_DN					=	0x06B4,
RANGING_CORE_	_INVERT_UP_DN					=	0x06B5,
RANGING_CORE_	_CPUMP_1					=	0x06B6,
RANGING_CORE_	_CPUMP_2					=	0x06B7,
RANGING_CORE_	_CPUMP_3					=	0x06B8,
RANGING_CORE_	_OSC_1					=	0x06B9,
RANGING_CORE_	_PLL_1					=	0x06BB,
RANGING_CORE_	_PLL_2					=	0x06BC,
RANGING_CORE_	_REFERENCE_1					=	0x06BD,
RANGING_CORE_	_REFERENCE_3					=	0x06BF,
RANGING_CORE_	_REFERENCE_4					=	0x06C0,
RANGING_CORE_	_REFERENCE_5					=	0x06C1,
RANGING_CORE_	_REGAVDD1V2					=	0x06C3,
RANGING_CORE_	_CALIB_1					=	0x06C4,
RANGING_CORE_	_CALIB_2					=	0x06C5,
RANGING_CORE_	_CALIB_3					=	0x06C6,
RANGING_CORE_	_TST_MUX_SEL1					=	0x06C9,
RANGING_CORE_	TST MUX SEL2					=	0x06CA,

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RANGING_CORETST_MUX		= 0x06
RANGING_COREGPIO_OUT_TESTMUX		$= 0 \times 06$
RANGING_CORECUSTOM_FE		$= 0 \times 06$
RANGING_CORECUSTOM_FE_2		$= 0 \times 06$
RANGING_CORESPAD_READOUT		$= 0 \times 06$
RANGING_CORESPAD_READOUT_1		$= 0 \times 06$
RANGING_CORESPAD_READOUT_2		$= 0 \times 06$
RANGING_CORESPAD_PS		$= 0 \times 06$
RANGING_CORELASER_SAFETY_2		$= 0 \times 06$
RANGING_CORENVM_CTRLMODE		$= 0 \times 07$
RANGING_CORENVM_CTRLPDN		$= 0 \times 07$
RANGING_CORENVM_CTRLPROGN		$= 0 \times 07$
RANGING_CORENVM_CTRLREADN		$= 0 \times 07$
RANGING_CORENVM_CTRLPULSE_WIDTH_MSB		$= 0 \times 07$
RANGING_CORENVM_CTRLPULSE_WIDTH_LSB		$= 0 \times 07$
RANGING_CORENVM_CTRLHV_RISE_MSB		$= 0 \times 07$
RANGING_CORENVM_CTRLHV_RISE_LSB		$= 0 \times 07$
RANGING_CORENVM_CTRLHV_FALL_MSB		$= 0 \times 07$
RANGING_CORENVM_CTRLHV_FALL_LSB		$= 0 \times 0.7$
RANGING_CORENVM_CTRLTST		$= 0 \times 07$
RANGING_CORENVM_CTRLTESTREAD		$= 0 \times 0.7$
RANGING_CORENVM_CTRLDATAIN_MMM		$= 0 \times 07$
RANGING_CORENVM_CTRLDATAIN_LMM		$= 0 \times 0.7$
RANGING_CORENVM_CTRLDATAIN_LLM		$= 0 \times 07$
RANGING_CORENVM_CTRLDATAIN_LLL		$= 0 \times 07$
RANGING_CORENVM_CTRLDATAOUT_MMM		$= 0 \times 0.7$
RANGING_CORENVM_CTRLDATAOUT_LMM		$= 0 \times 07$
RANGING_CORENVM_CTRLDATAOUT_LLM		$= 0 \times 0.7$
RANGING_CORENVM_CTRLDATAOUT_LLL		$= 0 \times 07$
RANGING_CORENVM_CTRLADDR		$= 0 \times 0.7$
RANGING_CORENVM_CTRLDATAOUT_ECC		$= 0 \times 07$
RANGING_CORERET_SPAD_EN_0		$= 0 \times 0.7$
RANGING_CORERET_SPAD_EN_1		$= 0 \times 07$
RANGING_CORERET_SPAD_EN_2		$= 0 \times 0.7$
RANGING_CORERET_SPAD_EN_3		$= 0 \times 0.7$
RANGING_CORERET_SPAD_EN_4		$= 0 \times 0.7$
RANGING_CORERET_SPAD_EN_5		$= 0 \times 07$
RANGING_CORERET_SPAD_EN_6		$= 0 \times 07$
RANGING_CORERET_SPAD_EN_7		$= 0 \times 07$
RANGING_CORERET_SPAD_EN_8		$= 0 \times 0.7$
RANGING_CORERET_SPAD_EN_9		$= 0 \times 0.7$
RANGING_CORERET_SPAD_EN_10		$= 0 \times 07$
RANGING_CORERET_SPAD_EN_11		$= 0 \times 07$
RANGING_CORERET_SPAD_EN_12		$= 0 \times 07$

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RANGING CORE	_RET_SPAD_EN_13			= 0x07A3,
	RET SPAD EN 14			= 0x07A4
	 RET_SPAD_EN_15			$= 0 \times 07 \text{A5}$
	RET SPAD EN 16			= 0x07A6,
RANGING CORE	RET_SPAD_EN_17			= 0x07A7
	SPAD SHIFT EN			= 0x07BA,
RANGING_CORE	SPAD_DISABLE_CTRL			= 0x07BB,
RANGING_CORE_	_SPAD_EN_SHIFT_OUT_DEBUG			= 0x07BC,
RANGING_CORE_				= 0x07BD,
RANGING_CORE_	_GPIO_DIR			= 0x07BE,
RANGING_CORE_	_VCSEL_PERIOD			= 0x0880,
RANGING_CORE_	_VCSEL_START			= 0x0881,
RANGING_CORE_	_VCSEL_STOP			= 0x0882,
RANGING_CORE_	_VCSEL_1			= 0x0885,
RANGING_CORE_	_VCSEL_STATUS			= 0x088D,
RANGING_CORE_	_STATUS			= 0x0980,
RANGING_CORE_	_LASER_CONTINUITY_STATE			= 0x0981,
RANGING_CORE_	_RANGE_1_MMM			= 0x0982,
RANGING_CORE_	_RANGE_1_LMM			= 0x0983,
RANGING_CORE_	_RANGE_1_LLM			= 0x0984,
RANGING_CORE_	_RANGE_1_LLL			= 0x0985,
RANGING_CORE_	_RANGE_REF_1_MMM			= 0x0986,
RANGING_CORE_	_RANGE_REF_1_LMM			= 0x0987,
RANGING_CORE_	_RANGE_REF_1_LLM			= 0x0988,
RANGING_CORE_	_RANGE_REF_1_LLL			= 0x0989,
RANGING_CORE_	_AMBIENT_WINDOW_EVENTS_1_MMM			= 0x098A,
RANGING_CORE_	_AMBIENT_WINDOW_EVENTS_1_LMM			= 0x098B,
RANGING_CORE_	_AMBIENT_WINDOW_EVENTS_1_LLM			= 0x098C,
RANGING_CORE_	_AMBIENT_WINDOW_EVENTS_1_LLL			= 0x098D,
RANGING_CORE_	_RANGING_TOTAL_EVENTS_1_MMM			= 0x098E,
RANGING_CORE_	_RANGING_TOTAL_EVENTS_1_LMM			= 0x098F,
RANGING_CORE_	_RANGING_TOTAL_EVENTS_1_LLM			= 0x0990,
RANGING_CORE_	_RANGING_TOTAL_EVENTS_1_LLL			= 0x0991,
RANGING_CORE_	_SIGNAL_TOTAL_EVENTS_1_MMM			= 0x0992,
RANGING_CORE_	_SIGNAL_TOTAL_EVENTS_1_LMM			= 0x0993,
RANGING_CORE_	_SIGNAL_TOTAL_EVENTS_1_LLM			= 0x0994,
RANGING_CORE_	_SIGNAL_TOTAL_EVENTS_1_LLL			= 0x0995,
RANGING_CORE_	_TOTAL_PERIODS_ELAPSED_1_MM			= 0x0996,
	_TOTAL_PERIODS_ELAPSED_1_LM			= 0x0997,
	_TOTAL_PERIODS_ELAPSED_1_LL			= 0x0998,
	_AMBIENT_MISMATCH_MM			= 0x0999,
RANGING_CORE_	_AMBIENT_MISMATCH_LM			= 0x099A,
	_AMBIENT_MISMATCH_LL			= 0x099B,
RANGING_CORE_	_AMBIENT_WINDOW_EVENTS_REF_1_MMN	I		= 0x099C,

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RANGING_CORE_	_AMBIENT_WINDOW_EVENTS_REF_1_LMM		= 0x099D
RANGING_CORE_	_AMBIENT_WINDOW_EVENTS_REF_1_LLM		$= 0 \times 099E$
RANGING_CORE_	AMBIENT_WINDOW_EVENTS_REF_1_LLL		= 0x099F
RANGING_CORE_	_RANGING_TOTAL_EVENTS_REF_1_MMM		= 0x09A0
	RANGING_TOTAL_EVENTS_REF_1_LMM		= 0x09A1
	RANGING_TOTAL_EVENTS_REF_1_LLM		= 0x09A2
	RANGING_TOTAL_EVENTS_REF_1_LLL		= 0x09A3
	SIGNAL_TOTAL_EVENTS_REF_1_MMM		= 0x09A4
RANGING_CORE_	_SIGNAL_TOTAL_EVENTS_REF_1_LMM		$= 0 \times 09 A5$
RANGING_CORE_	_SIGNAL_TOTAL_EVENTS_REF_1_LLM		= 0x09A6
RANGING_CORE_	_SIGNAL_TOTAL_EVENTS_REF_1_LLL		= 0x09A7
	TOTAL_PERIODS_ELAPSED_REF_1_MM		= 0x09A8
	TOTAL_PERIODS_ELAPSED_REF_1_LM		= 0x09A9
	_TOTAL_PERIODS_ELAPSED_REF_1_LL		= 0x09AA
	_AMBIENT_MISMATCH_REF_MM		= 0x09AB
	_AMBIENT_MISMATCH_REF_LM		$= 0 \times 09 AC$
	AMBIENT_MISMATCH_REF_LL		= 0x09AD
RANGING_CORE_	GPIO_CONFIGA0		= 0x0A00
RANGING_CORE_	_RESET_CONTROLA0		= 0x0A01
	INTR_MANAGERA0		$= 0 \times 0 A 0 2$
RANGING_CORE_	POWER_FSM_TIME_OSCA0		$= 0 \times 0 A 0 6$
RANGING_CORE_	VCSEL_ATEST_A0		$= 0 \times 0 A 0 7$
RANGING CORE	VCSEL PERIOD CLIPPED A0		= 0x0A08
RANGING_CORE_	VCSEL_STOP_CLIPPED_A0		$= 0 \times 0 A 0 9$
RANGING_CORE_	CALIB_2_A0		= 0x0A0A
RANGING_CORE_	_STOP_CONDITIONA0		= 0x0A0B
RANGING_CORE_	STATUS_RESETA0		= 0x0A0C
RANGING_CORE_	_READOUT_CFGA0		= 0x0A0D
RANGING_CORE_	_window_settinga0		$= 0 \times 0 A 0 E$
RANGING_CORE_	_VCSEL_DELAYA0		= 0x0A1A
RANGING_CORE_	_REFERENCE_2A0		= 0x0A1B
RANGING_CORE_	_REGAVDD1V2A0		$= 0 \times 0 A1D$
RANGING_CORE_	_TST_MUXA0		= 0x0A1F
RANGING_CORE_	_CUSTOM_FE_2A0		= 0x0A20
RANGING_CORE_	_SPAD_READOUTA0		= 0x0A21
RANGING_CORE_			= 0x0A22
	_SPARE_REGISTERA0		$= 0 \times 0 A23$
RANGING_CORE_	_VCSEL_CONT_STAGE5_BYPASSA0		= 0x0A24
RANGING_CORE_	_RET_SPAD_EN_18		= 0x0A25
RANGING_CORE_	_RET_SPAD_EN_19		= 0x0A26
RANGING_CORE_	_RET_SPAD_EN_20		$= 0 \times 0 A27$
	_RET_SPAD_EN_21		= 0x0A28
RANGING_CORE_	_RET_SPAD_EN_22		= 0x0A29
	RET SPAD EN 23		= 0x0A2A

```
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.h
                                                       Thu Mar 14 06:25:24 2019
                                                                                         20
                                                                                     = 0x0A2B
      RANGING CORE RET SPAD EN 24
      RANGING CORE RET SPAD EN 25
                                                                                     = 0x0A2C
      RANGING CORE__RET_SPAD_EN_26
                                                                                     = 0x0A2D
                                                                                     = 0x0A2E
      RANGING_CORE__RET_SPAD_EN_27
      RANGING CORE RET SPAD EN 28
                                                                                     = 0x0A2F
      RANGING CORE RET SPAD EN 29
                                                                                     = 0x0A30,
      RANGING_CORE__RET_SPAD_EN_30
                                                                                     = 0x0A31,
      RANGING_CORE__RET_SPAD_EN_31
                                                                                     = 0 \times 0 A 32.
      RANGING CORE REF SPAD EN 0 EWOK
                                                                                     = 0x0A33,
      RANGING CORE REF SPAD EN 1 EWOK
                                                                                     = 0x0A34,
      RANGING_CORE__REF_SPAD_EN_2__EWOK
                                                                                     = 0x0A35,
      RANGING CORE REF SPAD EN 3 EWOK
                                                                                     = 0x0A36
      RANGING CORE REF SPAD EN 4 EWOK
                                                                                     = 0 \times 0 A37,
      RANGING CORE REF SPAD EN 5 EWOK
                                                                                     = 0x0A38,
      RANGING CORE REF EN START SELECT
                                                                                     = 0x0A39,
      RANGING_CORE__REGDVDD1V2_ATEST__EWOK
                                                                                     = 0 \times 0 A41,
      SOFT_RESET_GO1
                                                                                     = 0x0B00,
      PRIVATE PATCH BASE ADDR RSLV
                                                                                     = 0x0E00,
      PREV_SHADOW_RESULT__INTERRUPT_STATUS
                                                                                     = 0 \times 0 ED0,
      PREV SHADOW RESULT RANGE STATUS
                                                                                     = 0 \times 0 ED1,
      PREV SHADOW RESULT REPORT STATUS
                                                                                     = 0x0ED2
      PREV SHADOW RESULT STREAM COUNT
                                                                                     = 0x0ED3,
      PREV SHADOW RESULT DSS ACTUAL EFFECTIVE SPADS SD0
                                                                                     = 0x0ED4,
      PREV SHADOW RESULT DSS ACTUAL EFFECTIVE SPADS SD0 HI
                                                                                     = 0x0ED4,
      PREV_SHADOW_RESULT__DSS_ACTUAL_EFFECTIVE_SPADS_SD0_LO
                                                                                     = 0x0ED5
      PREV_SHADOW_RESULT__PEAK_SIGNAL_COUNT_RATE_MCPS_SD0
                                                                                     = 0x0ED6,
      PREV SHADOW RESULT PEAK SIGNAL COUNT RATE MCPS SD0 HI
                                                                                     = 0 \times 0 ED6,
      PREV_SHADOW_RESULT__PEAK_SIGNAL_COUNT_RATE_MCPS_SD0_LO
                                                                                     = 0x0ED7,
      PREV_SHADOW_RESULT__AMBIENT_COUNT_RATE_MCPS_SD0
                                                                                     = 0x0ED8,
      PREV SHADOW RESULT AMBIENT COUNT RATE MCPS SD0 HI
                                                                                     = 0x0ED8,
      PREV SHADOW RESULT AMBIENT COUNT RATE MCPS SD0 LO
                                                                                     = 0x0ED9,
      PREV SHADOW RESULT SIGMA SD0
                                                                                     = 0x0EDA,
      PREV SHADOW RESULT SIGMA SDO HI
                                                                                     = 0x0EDA,
      PREV_SHADOW_RESULT__SIGMA_SD0_LO
                                                                                     = 0x0EDB,
      PREV SHADOW RESULT PHASE SD0
                                                                                     = 0x0EDC
      PREV SHADOW RESULT PHASE SDO HI
                                                                                     = 0 \times 0 EDC
      PREV_SHADOW_RESULT__PHASE_SD0_LO
                                                                                     = 0x0EDD,
      PREV SHADOW RESULT_FINAL_CROSSTALK_CORRECTED_RANGE_MM_SD0
                                                                                     = 0x0EDE
      PREV SHADOW RESULT FINAL CROSSTALK CORRECTED RANGE MM SD0 HI
                                                                                     = 0x0EDE
      PREV_SHADOW_RESULT__FINAL_CROSSTALK_CORRECTED_RANGE_MM_SD0_LO
                                                                                     = 0x0EDF,
      PREV SHADOW RESULT PEAK SIGNAL COUNT RATE CROSSTALK CORRECTED MCPS SD0
                                                                                     = 0 \times 0 \text{EEO}.
      PREV SHADOW RESULT PEAK SIGNAL COUNT RATE CROSSTALK CORRECTED MCPS SD0 HI = 0x0EE0,
      PREV SHADOW RESULT PEAK SIGNAL COUNT RATE CROSSTALK CORRECTED MCPS SD0 LO = 0x0EE1,
      PREV SHADOW RESULT MM INNER ACTUAL EFFECTIVE SPADS SD0
                                                                                     = 0 \times 0 \times 2
```

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/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.h
                                                        Thu Mar 14 06:25:24 2019
                                                                                          21
      PREV SHADOW RESULT MM INNER ACTUAL EFFECTIVE SPADS SD0 HI
                                                                                      = 0 \times 0 \times 2
      PREV SHADOW RESULT MM INNER ACTUAL EFFECTIVE SPADS SD0 LO
                                                                                      = 0x0EE3,
      PREV SHADOW RESULT MM OUTER ACTUAL EFFECTIVE SPADS SD0
                                                                                      = 0x0EE4
      PREV_SHADOW_RESULT__MM_OUTER_ACTUAL_EFFECTIVE_SPADS_SD0_HI
                                                                                      = 0x0EE4,
      PREV SHADOW RESULT MM OUTER ACTUAL EFFECTIVE SPADS SD0 LO
                                                                                      = 0x0EE5
      PREV SHADOW RESULT AVG SIGNAL COUNT RATE MCPS SD0
                                                                                      = 0x0EE6
      PREV_SHADOW_RESULT__AVG_SIGNAL_COUNT_RATE_MCPS_SD0_HI
                                                                                      = 0x0EE6
                                                                                      = 0 \times 0 \text{EE7}.
      PREV SHADOW RESULT AVG SIGNAL COUNT RATE MCPS SD0 LO
      PREV SHADOW RESULT DSS ACTUAL EFFECTIVE SPADS SD1
                                                                                      = 0x0EE8,
      PREV SHADOW RESULT DSS ACTUAL EFFECTIVE SPADS SD1 HI
                                                                                      = 0x0EE8,
      PREV SHADOW RESULT DSS ACTUAL EFFECTIVE SPADS SD1 LO
                                                                                      = 0x0EE9,
      PREV SHADOW RESULT PEAK SIGNAL COUNT RATE MCPS SD1
                                                                                      = 0x0EEA,
      PREV SHADOW RESULT PEAK SIGNAL COUNT RATE MCPS SD1 HI
                                                                                      = 0x0EEA,
      PREV SHADOW RESULT PEAK SIGNAL COUNT RATE MCPS SD1 LO
                                                                                      = 0x0EEB,
      PREV SHADOW RESULT AMBIENT COUNT RATE MCPS SD1
                                                                                      = 0x0EEC
      PREV_SHADOW_RESULT__AMBIENT_COUNT_RATE_MCPS_SD1_HI
                                                                                      = 0x0EEC
      PREV_SHADOW_RESULT__AMBIENT_COUNT_RATE_MCPS_SD1_LO
                                                                                      = 0x0EED,
      PREV SHADOW RESULT SIGMA SD1
                                                                                      = 0x0EEE
      PREV_SHADOW_RESULT__SIGMA_SD1_HI
                                                                                      = 0x0EEE
      PREV SHADOW RESULT SIGMA SD1 LO
                                                                                      = 0 \times 0 \text{EEF}
      PREV SHADOW RESULT PHASE SD1
                                                                                      = 0x0EF0,
      PREV SHADOW RESULT PHASE SD1 HI
                                                                                      = 0x0EF0,
      PREV SHADOW RESULT PHASE SD1 LO
                                                                                      = 0 \times 0 \text{EF1},
      PREV SHADOW RESULT FINAL CROSSTALK CORRECTED RANGE MM SD1
                                                                                      = 0x0EF2
      PREV_SHADOW_RESULT__FINAL_CROSSTALK_CORRECTED_RANGE_MM_SD1_HI
                                                                                      = 0x0EF2,
      PREV_SHADOW_RESULT__FINAL_CROSSTALK_CORRECTED_RANGE_MM_SD1_LO
                                                                                      = 0 \times 0 \text{EF3}.
      PREV SHADOW RESULT SPARE 0 SD1
                                                                                      = 0 \times 0 \text{EF4}
      PREV SHADOW RESULT SPARE 0 SD1 HI
                                                                                      = 0 \times 0 \text{EF4}
      PREV_SHADOW_RESULT__SPARE_0_SD1_LO
                                                                                      = 0 \times 0 \text{EF5}.
      PREV SHADOW RESULT SPARE 1 SD1
                                                                                      = 0x0EF6
      PREV SHADOW RESULT SPARE 1 SD1 HI
                                                                                      = 0x0EF6,
      PREV SHADOW RESULT SPARE 1 SD1 LO
                                                                                      = 0x0EF7,
      PREV SHADOW RESULT SPARE 2 SD1
                                                                                      = 0x0EF8,
      PREV_SHADOW_RESULT__SPARE_2_SD1_HI
                                                                                      = 0x0EF8,
      PREV SHADOW RESULT SPARE 2 SD1 LO
                                                                                      = 0x0EF9,
      PREV SHADOW RESULT SPARE 3 SD1
                                                                                      = 0x0EFA
      PREV_SHADOW_RESULT__SPARE_3_SD1_HI
                                                                                      = 0x0EFA,
      PREV SHADOW RESULT SPARE 3 SD1 LO
                                                                                      = 0 \times 0 EFB
      PREV SHADOW RESULT CORE AMBIENT WINDOW EVENTS SD0
                                                                                      = 0x0EFC
      PREV_SHADOW_RESULT_CORE__AMBIENT_WINDOW_EVENTS_SD0_3
                                                                                      = 0x0EFC,
      PREV SHADOW RESULT CORE AMBIENT WINDOW EVENTS SD0 2
                                                                                      = 0 \times 0 EFD
      PREV SHADOW RESULT CORE AMBIENT WINDOW EVENTS SD0 1
                                                                                      = 0x0EFE
      PREV SHADOW RESULT CORE AMBIENT WINDOW EVENTS SD0 0
                                                                                      = 0x0EFF,
      PREV SHADOW RESULT CORE RANGING TOTAL EVENTS SDO
                                                                                      = 0x0F00,
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.h
                                                        Thu Mar 14 06:25:24 2019
                                                                                         22
                                                                                     = 0x0F00,
      PREV SHADOW RESULT CORE RANGING TOTAL EVENTS SD0 3
                                                                                     = 0x0F01,
      PREV SHADOW RESULT CORE RANGING TOTAL EVENTS SD0 2
      PREV SHADOW RESULT CORE RANGING TOTAL EVENTS SD0 1
                                                                                     = 0 \times 0 F02
      PREV_SHADOW_RESULT_CORE__RANGING_TOTAL_EVENTS_SD0_0
                                                                                     = 0x0F03,
      PREV_SHADOW_RESULT_CORE__SIGNAL_TOTAL_EVENTS_SD0
                                                                                     = 0x0F04,
      PREV SHADOW RESULT CORE SIGNAL TOTAL EVENTS SD0 3
                                                                                     = 0x0F04,
      PREV_SHADOW_RESULT_CORE__SIGNAL_TOTAL_EVENTS_SD0_2
                                                                                     = 0x0F05,
      PREV_SHADOW_RESULT_CORE__SIGNAL_TOTAL_EVENTS SD0 1
                                                                                     = 0 \times 0 = 0 = 0 = 0
      PREV SHADOW RESULT CORE SIGNAL TOTAL EVENTS SD0 0
                                                                                     = 0x0F07
      PREV SHADOW RESULT CORE TOTAL PERIODS ELAPSED SD0
                                                                                     = 0x0F08,
      PREV SHADOW RESULT CORE TOTAL PERIODS ELAPSED SD0 3
                                                                                     = 0x0F08,
      PREV SHADOW RESULT CORE TOTAL PERIODS ELAPSED SD0 2
                                                                                     = 0x0F09,
      PREV SHADOW RESULT CORE TOTAL PERIODS ELAPSED SD0 1
                                                                                     = 0x0F0A,
      PREV SHADOW RESULT CORE TOTAL PERIODS ELAPSED SD0 0
                                                                                     = 0x0F0B,
      PREV SHADOW RESULT CORE AMBIENT WINDOW EVENTS SD1
                                                                                     = 0x0F0C
      PREV_SHADOW_RESULT_CORE__AMBIENT_WINDOW_EVENTS_SD1_3
                                                                                     = 0x0F0C,
      PREV_SHADOW_RESULT_CORE__AMBIENT_WINDOW_EVENTS_SD1_2
                                                                                     = 0x0F0D,
      PREV SHADOW RESULT CORE AMBIENT WINDOW EVENTS SD1 1
                                                                                     = 0x0F0E,
      PREV_SHADOW_RESULT_CORE__AMBIENT_WINDOW_EVENTS_SD1_0
                                                                                     = 0x0F0F,
      PREV SHADOW RESULT CORE RANGING TOTAL EVENTS SD1
                                                                                     = 0 \times 0 F10.
      PREV SHADOW RESULT CORE RANGING TOTAL EVENTS SD1 3
                                                                                     = 0 \times 0 F10,
      PREV SHADOW RESULT CORE RANGING TOTAL EVENTS SD1 2
                                                                                     = 0 \times 0 F11,
      PREV SHADOW RESULT CORE RANGING TOTAL EVENTS SD1 1
                                                                                     = 0 \times 0 F12,
      PREV SHADOW RESULT CORE RANGING TOTAL EVENTS SD1 0
                                                                                     = 0x0F13,
      PREV_SHADOW_RESULT_CORE__SIGNAL_TOTAL_EVENTS_SD1
                                                                                     = 0x0F14,
      PREV_SHADOW_RESULT_CORE__SIGNAL_TOTAL_EVENTS_SD1_3
                                                                                     = 0 \times 0 F14.
      PREV SHADOW RESULT CORE SIGNAL TOTAL EVENTS SD1 2
                                                                                     = 0x0F15,
      PREV_SHADOW_RESULT_CORE__SIGNAL_TOTAL_EVENTS_SD1_1
                                                                                     = 0x0F16,
      PREV_SHADOW_RESULT_CORE__SIGNAL_TOTAL_EVENTS_SD1_0
                                                                                     = 0 \times 0 F17.
      PREV SHADOW RESULT CORE TOTAL PERIODS ELAPSED SD1
                                                                                     = 0x0F18,
      PREV_SHADOW_RESULT_CORE__TOTAL_PERIODS_ELAPSED_SD1_3
                                                                                     = 0x0F18,
      PREV SHADOW RESULT CORE TOTAL PERIODS ELAPSED SD1 2
                                                                                     = 0x0F19,
      PREV SHADOW RESULT CORE TOTAL PERIODS ELAPSED SD1 1
                                                                                     = 0x0F1A,
      PREV_SHADOW_RESULT_CORE__TOTAL_PERIODS_ELAPSED_SD1_0
                                                                                     = 0x0F1B,
      PREV_SHADOW_RESULT_CORE_ SPARE 0
                                                                                     = 0x0F1C,
      RESULT DEBUG STATUS
                                                                                     = 0x0F20,
      RESULT__DEBUG_STAGE
                                                                                     = 0 \times 0 F21,
      GPH SYSTEM THRESH RATE HIGH
                                                                                     = 0 \times 0 F24.
      GPH SYSTEM THRESH RATE HIGH HI
                                                                                     = 0x0F24,
      GPH__SYSTEM__THRESH_RATE_HIGH_LO
                                                                                     = 0x0F25,
                                                                                     = 0x0F26,
      GPH SYSTEM THRESH RATE LOW
      GPH SYSTEM THRESH RATE LOW HI
                                                                                     = 0x0F26,
      GPH SYSTEM THRESH RATE LOW LO
                                                                                     = 0 \times 0 F27,
      GPH SYSTEM INTERRUPT CONFIG GPIO
                                                                                     = 0x0F28,
```

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/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.h
                                                       Thu Mar 14 06:25:24 2019
                                                                                        23
      GPH DSS CONFIG ROI MODE CONTROL
                                                                                    = 0x0F2F,
      GPH DSS CONFIG MANUAL EFFECTIVE SPADS SELECT
                                                                                    = 0x0F30,
      GPH DSS CONFIG MANUAL_EFFECTIVE_SPADS_SELECT_HI
                                                                                    = 0x0F30,
      GPH__DSS_CONFIG__MANUAL_EFFECTIVE_SPADS_SELECT_LO
                                                                                    = 0x0F31,
      GPH DSS CONFIG MANUAL BLOCK SELECT
                                                                                    = 0x0F32,
      GPH DSS CONFIG MAX SPADS LIMIT
                                                                                    = 0x0F33,
      GPH__DSS_CONFIG__MIN_SPADS_LIMIT
                                                                                    = 0x0F34,
      GPH MM CONFIG__TIMEOUT_MACROP_A_HI
                                                                                    = 0 \times 0 F36.
      GPH MM CONFIG TIMEOUT MACROP A LO
                                                                                    = 0x0F37,
      GPH MM CONFIG TIMEOUT MACROP B HI
                                                                                    = 0x0F38,
      GPH MM CONFIG TIMEOUT MACROP B LO
                                                                                    = 0x0F39,
      GPH RANGE CONFIG TIMEOUT MACROP A HI
                                                                                    = 0x0F3A
      GPH RANGE CONFIG TIMEOUT MACROP A LO
                                                                                    = 0x0F3B,
      GPH RANGE CONFIG VCSEL PERIOD A
                                                                                    = 0x0F3C,
      GPH RANGE CONFIG VCSEL PERIOD B
                                                                                    = 0x0F3D,
      GPH__RANGE_CONFIG__TIMEOUT_MACROP_B_HI
                                                                                    = 0x0F3E,
      GPH__RANGE_CONFIG__TIMEOUT_MACROP_B_LO
                                                                                    = 0x0F3F,
      GPH RANGE CONFIG SIGMA THRESH
                                                                                    = 0x0F40,
      GPH__RANGE_CONFIG__SIGMA_THRESH_HI
                                                                                    = 0x0F40,
      GPH RANGE CONFIG SIGMA THRESH LO
                                                                                    = 0 \times 0 F41.
      GPH RANGE CONFIG MIN COUNT RATE RTN LIMIT MCPS
                                                                                    = 0x0F42
      GPH RANGE CONFIG MIN COUNT RATE RTN LIMIT MCPS HI
                                                                                    = 0x0F42,
      GPH RANGE CONFIG MIN COUNT RATE RTN LIMIT MCPS LO
                                                                                    = 0x0F43,
      GPH RANGE CONFIG VALID PHASE LOW
                                                                                    = 0 \times 0 F44,
      GPH__RANGE_CONFIG__VALID_PHASE_HIGH
                                                                                    = 0x0F45,
      FIRMWARE__INTERNAL_STREAM_COUNT_DIV
                                                                                    = 0 \times 0 F46.
      FIRMWARE INTERNAL STREAM COUNTER VAL
                                                                                    = 0 \times 0 F47.
      DSS CALC ROI CTRL
                                                                                    = 0 \times 0 F54.
      DSS_CALC__SPARE_1
                                                                                    = 0 \times 0 F55.
      DSS CALC SPARE 2
                                                                                    = 0x0F56,
      DSS CALC SPARE 3
                                                                                    = 0x0F57,
      DSS CALC SPARE 4
                                                                                    = 0x0F58,
      DSS CALC SPARE 5
                                                                                    = 0x0F59,
      DSS_CALC__SPARE_6
                                                                                    = 0x0F5A,
      DSS CALC SPARE 7
                                                                                    = 0x0F5B,
      DSS CALC USER ROI SPAD EN 0
                                                                                    = 0x0F5C
      DSS_CALC__USER_ROI_SPAD_EN_1
                                                                                    = 0x0F5D,
      DSS CALC USER ROI SPAD EN 2
                                                                                    = 0 \times 0 F5E.
      DSS CALC USER ROI SPAD EN 3
                                                                                    = 0x0F5F
      DSS_CALC__USER_ROI_SPAD_EN_4
                                                                                    = 0x0F60,
      DSS CALC USER ROI SPAD EN 5
                                                                                    = 0x0F61,
      DSS CALC USER ROI SPAD EN 6
                                                                                    = 0x0F62
      DSS CALC USER ROI SPAD EN 7
                                                                                    = 0x0F63,
      DSS CALC USER ROI SPAD EN 8
                                                                                    = 0x0F64,
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/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.h	Thu Mar 14 06:25:24 2019 24	
DSS_CALCUSER_ROI_SPAD_EN_9	$= 0 \times 0 F65,$	
DSS_CALCUSER_ROI_SPAD_EN_10	= 0x0F66,	
DSS_CALCUSER_ROI_SPAD_EN_11	= 0x0F67,	
DSS_CALCUSER_ROI_SPAD_EN_12	= 0x0F68,	
DSS_CALCUSER_ROI_SPAD_EN_13	= 0x0F69,	
DSS_CALCUSER_ROI_SPAD_EN_14	= 0x0F6A,	
DSS_CALCUSER_ROI_SPAD_EN_15	= 0x0F6B,	
DSS_CALCUSER_ROI_SPAD_EN_16	= 0x0F6C,	
DSS_CALCUSER_ROI_SPAD_EN_17	= 0x0F6D,	
DSS_CALCUSER_ROI_SPAD_EN_18	= 0x0F6E,	
DSS_CALCUSER_ROI_SPAD_EN_19	= 0x0F6F,	
DSS_CALCUSER_ROI_SPAD_EN_20	= 0x0F70,	
DSS_CALCUSER_ROI_SPAD_EN_21	= 0x0F71,	
DSS CALC USER ROI SPAD EN 22	= 0x0F72,	
DSS_CALCUSER_ROI_SPAD_EN_23	= 0x0F73,	
DSS_CALCUSER_ROI_SPAD_EN_24	= 0x0F74,	
DSS CALC USER ROI SPAD EN 25	$= 0 \times 0 F75,$	
DSS_CALCUSER_ROI_SPAD_EN_26	= 0x0F76,	
DSS_CALCUSER_ROI_SPAD_EN_27	= 0x0F77,	
DSS_CALCUSER_ROI_SPAD_EN_28	= 0x0F78,	
DSS_CALCUSER_ROI_SPAD_EN_29	= 0x0F79,	
DSS_CALCUSER_ROI_SPAD_EN_30	= 0x0F7A,	
DSS_CALCUSER_ROI_SPAD_EN_31	= 0x0F7B,	
DSS_CALCUSER_ROI_0	= 0x0F7C,	
DSS_CALCUSER_ROI_1	= 0x0F7D,	
DSS_CALCMODE_ROI_0	= 0x0F7E,	
DSS_CALCMODE_ROI_1	= 0x0F7F,	
SIGMA_ESTIMATOR_CALCSPARE_0	= 0x0F80,	
VHV_RESULTPEAK_SIGNAL_RATE_MCPS	= 0x0F82,	
VHV_RESULTPEAK_SIGNAL_RATE_MCPS_HI	= 0x0F82,	
VHV_RESULTPEAK_SIGNAL_RATE_MCPS_LO	= 0x0F83,	
VHV_RESULTSIGNAL_TOTAL_EVENTS_REF	= 0x0F84,	
VHV_RESULTSIGNAL_TOTAL_EVENTS_REF_3	= 0x0F84,	
VHV_RESULTSIGNAL_TOTAL_EVENTS_REF_2	= 0x0F85,	
VHV_RESULTSIGNAL_TOTAL_EVENTS_REF_1	= 0x0F86,	
VHV_RESULTSIGNAL_TOTAL_EVENTS_REF_0	= 0x0F87,	
PHASECAL_RESULTPHASE_OUTPUT_REF	= 0x0F88,	
PHASECAL_RESULTPHASE_OUTPUT_REF_HI	= 0x0F88,	
PHASECAL_RESULTPHASE_OUTPUT_REF_LO	= 0x0F89,	
DSS_RESULTTOTAL_RATE_PER_SPAD	= 0x0F8A,	
DSS_RESULTTOTAL_RATE_PER_SPAD_HI	= 0x0F8A,	
DSS_RESULTTOTAL_RATE_PER_SPAD_LO	= 0x0F8B,	
DSS_RESULTENABLED_BLOCKS	= 0x0F8C,	
DSS_RESULTNUM_REQUESTED_SPADS	= 0x0F8E,	

home/gle/BeagleBoneBlue/bluebot/VL53L1X.h	Thu	Mar 1	.4 0	6:25:2	24 2	2019		25
DSS_RESULTNUM_REQUESTED_SPADS_HI							=	0x0F8
DSS_RESULTNUM_REQUESTED_SPADS_LO							=	0x0F8
MM_RESULTINNER_INTERSECTION_RATE							=	0x0F9
MM_RESULTINNER_INTERSECTION_RATE_HI							=	0x0F9
MM_RESULTINNER_INTERSECTION_RATE_LO							=	0x0F9
MM_RESULTOUTER_COMPLEMENT_RATE							=	$0 \times 0 F$
MM_RESULTOUTER_COMPLEMENT_RATE_HI							=	0x0F
MM_RESULTOUTER_COMPLEMENT_RATE_LO							=	0x0F
MM_RESULTTOTAL_OFFSET							=	0x0F
MM_RESULTTOTAL_OFFSET_HI							=	0x0F
MM_RESULTTOTAL_OFFSET_LO							=	0x0F
XTALK_CALCXTALK_FOR_ENABLED_SPADS							=	0x0F
XTALK_CALCXTALK_FOR_ENABLED_SPADS_3							=	0x0F
XTALK_CALCXTALK_FOR_ENABLED_SPADS_2							=	0x0F
XTALK_CALCXTALK_FOR_ENABLED_SPADS_1							=	0x0F
XTALK_CALCXTALK_FOR_ENABLED_SPADS_0							=	0x0F
XTALK_RESULTAVG_XTALK_USER_ROI_KCPS							=	0x0F
XTALK_RESULTAVG_XTALK_USER_ROI_KCPS_3							=	$0 \times 0 F$
XTALK_RESULTAVG_XTALK_USER_ROI_KCPS_2							=	$0 \times 0 F$
XTALK_RESULTAVG_XTALK_USER_ROI_KCPS_1							=	0x0F
XTALK_RESULTAVG_XTALK_USER_ROI_KCPS_0							=	$0 \times 0 F$
XTALK_RESULTAVG_XTALK_MM_INNER_ROI_KCPS							=	$0 \times 0 F$
XTALK_RESULTAVG_XTALK_MM_INNER_ROI_KCPS_3							=	0x0F
XTALK_RESULTAVG_XTALK_MM_INNER_ROI_KCPS_2							=	0x0F
XTALK_RESULTAVG_XTALK_MM_INNER_ROI_KCPS_1							=	$0 \times 0 F$
XTALK_RESULTAVG_XTALK_MM_INNER_ROI_KCPS_0							=	0x0F
XTALK_RESULTAVG_XTALK_MM_OUTER_ROI_KCPS							=	$0 \times 0 F$
XTALK_RESULTAVG_XTALK_MM_OUTER_ROI_KCPS_3							=	$0 \times 0 F$
XTALK_RESULTAVG_XTALK_MM_OUTER_ROI_KCPS_2							=	0x0F
XTALK_RESULTAVG_XTALK_MM_OUTER_ROI_KCPS_1							=	0x0F
XTALK_RESULTAVG_XTALK_MM_OUTER_ROI_KCPS_0							=	$0 \times 0 F$
RANGE_RESULTACCUM_PHASE							=	0x0F
RANGE_RESULTACCUM_PHASE_3							=	0x0F
RANGE_RESULTACCUM_PHASE_2							=	0x0F
RANGE_RESULTACCUM_PHASE_1							=	$0 \times 0 F$
RANGE_RESULTACCUM_PHASE_0							=	0x0F
RANGE_RESULTOFFSET_CORRECTED_RANGE							=	0x0F
RANGE_RESULTOFFSET_CORRECTED_RANGE_HI							=	0x0F
RANGE_RESULTOFFSET_CORRECTED_RANGE_LO							=	0x0F
SHADOW_PHASECAL_RESULTVCSEL_START							=	$0 \times 0 F$
SHADOW_RESULTINTERRUPT_STATUS							=	0x0F
SHADOW_RESULTRANGE_STATUS							=	0x0F
SHADOW_RESULTREPORT_STATUS							=	0x0F
SHADOW_RESULTSTREAM_COUNT							=	0x0F

```
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.h
                                                       Thu Mar 14 06:25:24 2019
                                                                                        26
                                                                                   = 0x0FB4,
      SHADOW RESULT DSS ACTUAL EFFECTIVE SPADS SD0
      SHADOW RESULT DSS ACTUAL EFFECTIVE SPADS SDO HI
                                                                                   = 0x0FB4,
      SHADOW RESULT DSS ACTUAL EFFECTIVE SPADS SD0 LO
                                                                                   = 0 \times 0 FB5
      SHADOW_RESULT__PEAK_SIGNAL_COUNT_RATE_MCPS_SD0
                                                                                   = 0x0FB6,
      SHADOW RESULT PEAK SIGNAL COUNT RATE MCPS SDO HI
                                                                                   = 0x0FB6,
      SHADOW_RESULT__PEAK_SIGNAL_COUNT_RATE_MCPS_SD0_LO
                                                                                   = 0x0FB7
      SHADOW_RESULT__AMBIENT_COUNT_RATE_MCPS_SD0
                                                                                   = 0x0FB8,
                                                                                   = 0 \times 0 FB8.
      SHADOW_RESULT__AMBIENT_COUNT_RATE_MCPS_SD0_HI
      SHADOW RESULT AMBIENT COUNT RATE MCPS SD0 LO
                                                                                   = 0x0FB9
      SHADOW RESULT SIGMA SDO
                                                                                   = 0x0FBA,
      SHADOW RESULT SIGMA SDO HI
                                                                                   = 0x0FBA
      SHADOW RESULT SIGMA SD0 LO
                                                                                   = 0x0FBB
      SHADOW RESULT PHASE SD0
                                                                                   = 0x0FBC,
      SHADOW RESULT PHASE SDO HI
                                                                                   = 0x0FBC,
      SHADOW RESULT PHASE SD0 LO
                                                                                   = 0x0FBD,
      SHADOW_RESULT__FINAL_CROSSTALK_CORRECTED_RANGE_MM_SD0
                                                                                   = 0x0FBE
      SHADOW_RESULT__FINAL_CROSSTALK_CORRECTED_RANGE_MM_SD0_HI
                                                                                   = 0x0FBE,
      SHADOW RESULT FINAL CROSSTALK CORRECTED RANGE MM SD0 LO
                                                                                   = 0x0FBF
      SHADOW RESULT__PEAK_SIGNAL_COUNT_RATE_CROSSTALK_CORRECTED_MCPS_SD0
                                                                                   = 0x0FC0,
      SHADOW RESULT PEAK SIGNAL COUNT RATE CROSSTALK CORRECTED MCPS SD0 HI
                                                                                   = 0x0FC0,
      SHADOW RESULT PEAK SIGNAL COUNT RATE CROSSTALK CORRECTED MCPS SD0 LO
                                                                                   = 0x0FC1,
      SHADOW RESULT MM INNER ACTUAL EFFECTIVE SPADS SD0
                                                                                   = 0x0FC2
      SHADOW RESULT MM INNER ACTUAL EFFECTIVE SPADS SD0 HI
                                                                                   = 0x0FC2
      SHADOW RESULT MM INNER ACTUAL EFFECTIVE SPADS SD0 LO
                                                                                   = 0x0FC3,
      SHADOW_RESULT__MM_OUTER_ACTUAL_EFFECTIVE_SPADS_SD0
                                                                                   = 0x0FC4,
      SHADOW RESULT MM OUTER ACTUAL EFFECTIVE SPADS SD0 HI
                                                                                   = 0x0FC4,
      SHADOW RESULT MM OUTER ACTUAL EFFECTIVE SPADS SD0 LO
                                                                                   = 0x0FC5,
      SHADOW_RESULT__AVG_SIGNAL_COUNT_RATE_MCPS_SD0
                                                                                   = 0x0FC6,
      SHADOW_RESULT__AVG_SIGNAL_COUNT_RATE_MCPS_SD0_HI
                                                                                   = 0 \times 0 FC6.
      SHADOW RESULT AVG SIGNAL COUNT RATE MCPS SD0 LO
                                                                                   = 0 \times 0 FC7.
      SHADOW RESULT DSS ACTUAL EFFECTIVE SPADS SD1
                                                                                   = 0x0FC8,
      SHADOW RESULT DSS ACTUAL EFFECTIVE SPADS SD1 HI
                                                                                   = 0x0FC8,
      SHADOW RESULT DSS ACTUAL EFFECTIVE SPADS SD1 LO
                                                                                   = 0x0FC9,
                                                                                   = 0x0FCA,
      SHADOW_RESULT__PEAK_SIGNAL_COUNT_RATE_MCPS_SD1
      SHADOW RESULT PEAK SIGNAL COUNT RATE MCPS SD1 HI
                                                                                   = 0x0FCA,
      SHADOW RESULT PEAK SIGNAL COUNT RATE MCPS SD1 LO
                                                                                   = 0x0FCB
      SHADOW_RESULT__AMBIENT_COUNT_RATE_MCPS_SD1
                                                                                   = 0x0FCC
      SHADOW RESULT AMBIENT COUNT RATE MCPS SD1 HI
                                                                                   = 0 \times 0 FCC
      SHADOW RESULT AMBIENT COUNT RATE MCPS SD1 LO
                                                                                   = 0x0FCD
      SHADOW_RESULT__SIGMA_SD1
                                                                                   = 0x0FCE
                                                                                   = 0x0FCE
      SHADOW RESULT SIGMA SD1 HI
      SHADOW RESULT SIGMA SD1 LO
                                                                                   = 0x0FCF
      SHADOW RESULT PHASE SD1
                                                                                   = 0x0FD0,
      SHADOW RESULT PHASE SD1 HI
                                                                                   = 0 \times 0 FD0,
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.h
                                                        Thu Mar 14 06:25:24 2019
                                                                                          28
                                                                                      = 0 \times 0 FF3,
      SHADOW_RESULT_CORE__RANGING_TOTAL_EVENTS_SD1_0
      SHADOW RESULT CORE SIGNAL TOTAL EVENTS SD1
                                                                                      = 0 \times 0 FF4,
      SHADOW RESULT CORE SIGNAL TOTAL EVENTS SD1 3
                                                                                      = 0 \times 0 FF4,
      SHADOW_RESULT_CORE__SIGNAL_TOTAL_EVENTS_SD1_2
                                                                                      = 0 \times 0 FF5,
      SHADOW RESULT_CORE__SIGNAL_TOTAL_EVENTS_SD1_1
                                                                                      = 0x0FF6,
      SHADOW RESULT CORE SIGNAL TOTAL EVENTS SD1 0
                                                                                      = 0 \times 0 FF7.
      SHADOW_RESULT_CORE__TOTAL_PERIODS_ELAPSED_SD1
                                                                                      = 0x0FF8,
      SHADOW RESULT CORE TOTAL PERIODS ELAPSED SD1 3
                                                                                      = 0 \times 0 FF8.
      SHADOW RESULT CORE TOTAL PERIODS ELAPSED SD1 2
                                                                                      = 0 \times 0 FF9,
      SHADOW RESULT CORE TOTAL PERIODS ELAPSED SD1 1
                                                                                      = 0x0FFA,
      SHADOW RESULT CORE TOTAL PERIODS ELAPSED SD1 0
                                                                                      = 0x0FFB,
      SHADOW_RESULT_CORE__SPARE_0
                                                                                      = 0x0FFC,
      SHADOW PHASECAL RESULT REFERENCE PHASE HI
                                                                                      = 0 \times 0 FFE,
      SHADOW PHASECAL RESULT REFERENCE PHASE LO
                                                                                      = 0 \times 0 FFF,
    };
    enum DistanceMode { Short, Medium, Long, Unknown };
    enum RangeStatus : uint8 t
      RangeValid
                                     0,
      // "sigma estimator check is above the internal defined threshold"
      // (sigma = standard deviation of measurement)
      SigmaFail
                                 = 1,
      // "signal value is below the internal defined threshold"
      SignalFail
                                 =
                                     2,
      // "Target is below minimum detection threshold."
      RangeValidMinRangeClipped =
      // "phase is out of bounds"
      // (nothing detected in range; try a longer distance mode if applicable)
      OutOfBoundsFail
                                 = 4,
      // "HW or VCSEL failure"
      HardwareFail
                                      5,
      // "The Range is valid but the wraparound check has not been done."
      RangeValidNoWrapCheckFail = 6,
      // "Wrapped target, not matching phases"
      // "no matching phase in other VCSEL period timing."
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/VL53L1X.h Thu Mar 14 06:25:24 2019
                                                                                 29
     WrapTargetFail
     // "Internal algo underflow or overflow in lite ranging."
   // ProcessingFail
                       = 8: not used in API
     // "Specific to lite ranging."
     // should never occur with this lib (which uses low power auto ranging,
     // as the API does)
     XtalkSignalFail
                          = 9,
     // "1st interrupt when starting ranging in back to back mode. Ignore
     // data."
     // should never occur with this lib
                         = 10, // (the API spells this "syncronisation")
     SynchronizationInt
     // "All Range ok but object is result of multiple pulses merging together.
     // Used by RQL for merged pulse detection"
   // RangeValid MergedPulse = 11: not used in API
     // "Used by ROL as different to phase fail."
   // TargetPresentLackOfSignal = 12:
     // "Target is below minimum detection threshold."
     MinRangeFail = 13,
     // "The reported range is invalid"
  // RangeInvalid = 14: can't actually be returned by API (range can never become negative, even after corr
ection)
     // "No Update."
                              = 255,
     None
   };
   struct RangingData
     uint16 t range mm;
     RangeStatus range_status;
     float peak_signal_count_rate_MCPS;
     float ambient count rate MCPS;
   };
   RangingData ranging data;
   uint8 t last status; // status of last I2C transmission
```

```
VL53L1X();
  void setAddress(uint8_t new_addr);
  uint8_t getAddress() { return address; }
  bool init(bool io_2v8 = true);
  void writeReg(uint16 t reg, uint8 t value);
  void writeReg16Bit(uint16 t reg, uint16 t value);
  void writeReq32Bit(uint16 t req, uint32 t value);
  uint8 t readReg(regAddr reg);
  uint16 t readReq16Bit(uint16 t req);
  uint32 t readReq32Bit(uint16 t req);
 bool setDistanceMode(DistanceMode mode);
  DistanceMode getDistanceMode() { return distance_mode; }
 bool setMeasurementTimingBudget(uint32_t budget_us);
  uint32 t getMeasurementTimingBudget();
  void startContinuous(uint32 t period ms);
  void stopContinuous();
  uint16 t read(bool blocking = true);
  uint16_t readRangeContinuousMillimeters(bool blocking = true) { return read(blocking); } // alias of read()
  // check if sensor has new reading available
  // assumes interrupt is active low (GPIO HV MUX CTRL bit 4 is 1)
 bool dataReady() { return (readReg(GPIO__TIO_HV_STATUS) & 0x01) == 0; }
  static const char * rangeStatusToString(RangeStatus status);
  void setTimeout(uint16 t timeout) { io timeout = timeout; }
  uint16_t getTimeout() { return io_timeout; }
  bool timeoutOccurred();
private:
  // The Arduino two-wire interface uses a 7-bit number for the address.
  // and sets the last bit correctly based on reads and writes
  static const uint8 t AddressDefault = 0b0101001;
  // value used in measurement timing budget calculations
  // assumes PresetMode is LOWPOWER AUTONOMOUS
```

```
//
 // vhv = LOWPOWER AUTO VHV LOOP DURATION US + LOWPOWERAUTO VHV LOOP BOUND
         (tuning parm default) * LOWPOWER_AUTO VHV LOOP DURATION US
 //
        = 245 + 3 * 245 = 980
 // TimingGuard = LOWPOWER AUTO OVERHEAD BEFORE A RANGING +
                 LOWPOWER AUTO OVERHEAD BETWEEN A B RANGING + vhv
//
                = 1448 + 2100 + 980 = 4528
 static const uint32 t TimingGuard = 4528;
// value in DSS CONFIG TARGET TOTAL RATE MCPS register, used in DSS
 // calculations
static const uint16 t TargetRate = 0x0A00;
// for storing values read from RESULT RANGE STATUS (0x0089)
 // through RESULT PEAK SIGNAL COUNT RATE CROSSTALK CORRECTED MCPS SD0 LOW
 // (0x0099)
 struct ResultBuffer
  uint8_t range_status;
// uint8 t report status: not used
  uint8 t stream count;
  uint16 t dss actual effective spads sd0;
// uint16 t peak signal count rate mcps sd0: not used
  uint16 t ambient count rate mcps sd0;
// uint16_t sigma_sd0: not used
// uint16_t phase_sd0: not used
  uint16 t final crosstalk corrected range mm sd0;
  uint16 t peak signal count rate crosstalk corrected mcps sd0;
 };
// making this static would save RAM for multiple instances as long as there
 // aren't multiple sensors being read at the same time (e.g. on separate
// I2C buses)
ResultBuffer results;
uint8 t address;
uint16 t io timeout;
bool did timeout;
uint16_t timeout_start_ms;
uint16 t fast osc frequency;
uint16 t osc calibrate val;
```

float countRateFixedToFloat(uint16_t count_rate_fixed) { return (float)count_rate_fixed / (1 << 7); }</pre>

// Convert count rate from fixed point 9.7 format to float

};

```
/opt/home/gle/BeagleBoneBlue/bluebot/Arduino.h Thu Mar 14 08:52:18 2019 1
//
// Arduino
//
#define I2C_BUS 1
unsigned long millis(void);
int wire_read_bytes(uint8_t bus, uint8_t count, uint8_t *buf);
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/servo pkg.c
                                             Tue Mar 19 15:47:13 2019
// Package of servo related functions
#include "servo_pkg.h"
// ^^^^^
  Routine to setup servo related stuff
// ^^^^^^
void servo_setup(void) {
// Turn of 6V servo power rails
  rc servo power rail en(1);
  return ;
} // end servo_setup();
// ^^^^^
  Routine to cleanup servo related stuff
// ^^^^^
void servo_cleanup(void) {
  rc servo power rail en(0);
  rc_servo_cleanup();
  return ;
} // end servo_cleanup()
// ********
// Routine to command distance sensor servo
// to a specified angle
// *********
void distance_sensor_angle(int angle) {
  int pw;
  pw = DISTANCE_SERVO_SLOPE * angle + DISTANCE_SERVO_OFFSET ;
  rc_servo_send_pulse_us(DISTANCE_SENSOR_SERVO_CHANNEL, pw);
  return ;
```

```
} // end distance_sensor servo()
// ********
// Routine to sweep the distance sensor
// ***********
void sweep_distance_sensor(void) {
        angle, i;
  int
  servo_setup();
// We could initialize the distance sensor here
// Command servo to -90 degrees
// Wait a second
  distance_sensor_angle(-90) ;
  rc_usleep(1000000);
// Now sweep in 10 degree increments
// So like 18 steps
// About 100 ms per step
  for (angle = -90; i <= 90; i += 10) {
       for (i = 1; i <= 5; i++) {
           distance_sensor_angle(angle) ;
// We could take some distance readings here
           rc_usleep(20000);
// Command servo to 0 degrees
// Wait a second
  distance_sensor_angle(0) ;
  rc_usleep(1000000);
  servo cleanup();
  return ;
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/servo_pkg.c
```

Tue Mar 19 15:47:13 2019

3

} // end sweep_distance_sensor()

```
/opt/home/gle/BeagleBoneBlue/bluebot/arcdefs.h
                                                       Mon Mar 18 20:54:02 2019
// Define our types here
#include <robotcontrol.h>
// Some defines that we would like to use
#define
           ARC ON
                          1
#define
           ARC_OFF
                          0
#define
           ARC_FAIL
                          1
#define
           ARC PASS
                          0
#define
           ARC_SWAP
                          -1.0
#define
           ARC_NO_SWAP
                          1.0
#define
           ARC PI
                          3.14159
#define
           M_TO_INCH
                          39.37
#define
           INCH_TO_M
                          (1.0/39.37)
#define
           ARC TRUE
                          1
#define
                          0
           ARC_FALSE
#define
           ARC_FWD
                          1
#define
           ARC BWD
#define
           ARC_RAD2DEG
                           57.2958
#define
                          0
#define
                          1
           Y
#define
           FP_TOL
                          1e-3
#define
           ROTATE_VEL
                          6.0
#define
           NORTH
                          0.5 * M_PI
                          1.5 * M_PI
#define
           SOUTH
#define
           EAST
#define
           WEST
                          M PI
// Max angle error in degrees
#define
           MAX_ANGLE_ERROR 2.0
// Max distance error in inches
#define
           MAX_DIST_ERROR
                            0.25
// Structure to hold PID gains
typedef struct arc_PIDgain_t {
```

1

```
Mon Mar 18 20:54:02 2019
/opt/home/gle/BeagleBoneBlue/bluebot/arcdefs.h
   double
         Kp ;
   double Ki;
   double Kd;
} arc_PIDgain_t ;
// Structure to hold motor data
// ************
typedef struct arc_motor_t {
   rc filter t
                *pid ;
                            // Pointer to a filter to be used for PID control
                            // Motor number {1, 2, 3, 4}
   int.
                id;
                            // -1 = swap blk and red wires, 1 is don't swap
   int
               swap ;
                           // 1 = forward and -1 = backward
// PID setpoint
                dir ;
   int
   int
              sp ;
                            // Encoder value
   int
               cnt ;
                           // Old encoder value
   int
                old cnt ;
              tics ;
   int
   double
               ; mwg
                            // pwm value
} arc motor t ;
// *************
// Structure to hold heading
// *************
typedef struct arc_heading_t {
  double distance;
  double angle;
} arc heading t ;
// **************
// Structure to hold location data
// *************
typedef struct arc location t {
   rc_vector_t xy;
                               // x,y cordinates in inches
   double theta;
                               // orientation (pi / 2 for looking north)
   double s;
                               // distance traveled
} arc_location_t ;
// *************
// Structure to hold robot config data
```

```
Mon Mar 18 20:54:02 2019
/opt/home/gle/BeagleBoneBlue/bluebot/arcdefs.h
// ************
typedef struct arc config t {
   double
                  sample rate ;
                                                      // Sampling frequency
                                                      // Period used for updates
   double
                  Ts ;
                  Ts in ns ;
   uint64 t
   double
                  r;
                                                      // Wheel radius in inches
   double
                  d ;
                                                      // Spacing between wheels in inches
   double
                  encoder tics per revolution ;
   double
                  inches per tic ;
   double
                  tics per inch ;
   double
                  max pwm ;
   arc PIDgain t
                  motor PID gain ;
                                                      // PID gain constants for motor
   rc mpu config t mpu config ;
                                                      // MPU config
} arc config t ;
// ************
// Structure which defines our robot
// ************
typedef struct arc_robot_t {
   arc config t
                  confiq ;
                                        // Struct that contains our robot configuration
                  right motor ;
                                        // Struct for the right motor
   arc motor t
   arc motor t
                  left motor ;
                                        // Struct for the left motor
   arc_location_t location ;
                                        // Robot's current location
   arc_location_t target;
                                        // Target location
   double
                  velocity ;
                                        // Velocity of robot we desire in in / sec
   double
                  state ;
                                       // Robot state
                                      // State of the green LED
   int
                  greenLED ;
                  redLED ;
                                      // State of the red LED
   int
                  mpu data;
                                      // MPU data
   rc_mpu_data_t
} arc_robot_t ;
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/arclib.h
                                                    Tue Mar 19 06:00:07 2019
#include "arcdefs.h"
// Here are the routines in the arclib
// Dump variables to screen
void arc_var_dump(void) ;
// Print robot location
void arc_print_location(arc_location_t loc);
// Routine to toggle the green LED
void toggleGreenLED(void) ;
// Routine to convert degrees to radians
double arc_deg2rad(double deg) ;
// Routine to convert radians to degrees
double arc_rad2deg(double rad) ;
// Constrain angle between -pi to +pi
double arc_constrain_angle(double angle) ;
// Used to initialize the various modules and data structures we need
int arc init(void);
// Used to update robot's location in arena
void arc_update_location();
// Routine to return a heading
arc_heading_t arc_compute_heading(void) ;
// Helper function
```

```
/opt/home/gle/BeagleBoneBlue/bluebot/arclib.h Tue Mar 19 06:00:07 2019
void arc_move_init(int right_dir, int left_dir, bool soft_start);

// Routine to move the robot
void arc_move(void);

// Routine to rotate the robot
void arc_rotate(double angle);

// Used to move robot to a new target location
void arc_goto(double x, double y, double angle, double velocity);

// Routine used to move forward
void arc_forward(double distance, double velocity);
```