U8 R47

20 Q6

20 Q5

2

BOARD CHARACTERISTICS

Copper Layer Count: 4 Board Thickness: 1.5470 mm

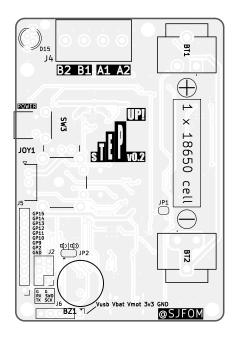
Board overall dimensions: 54.9766 mm x 79.9766 mm

Min track/spacing: 0.1500 mm / 0.1500 mm Min hole diameter: 0.2000 mm

Copper Finish: HAL lead-free Impedance Control: Yes Castellated pads: No Plated Board Edge: No

Edge card connectors: No

Layer Name	Туре	Material	Thickness (mm)	Color	Epsilon R	Loss Tangent
TopLayer_Silk.Silkscreen	Top Silk Screen	Not specified	0 mm	White	1	0
TopLayer_Paste.Paste	Top Solder Paste		0 mm			0
TopLayer_Mask.Mask	Top Solder Mask	Not specified 0.01 mm		Green	3.3	0
L1_TopLayer.Cu	copper	0.035 mm			1	0
Dielectric	prepreg	FR4	0.1785 mm	Not specified	4.56	0.02
L2_InnerLayer1.Cu	copper		0.035 mm		1	0
Dielectric	core	FR4	1.03 mm	Not specified	4.6	0.02
L3_InnerLayer1.Cu	copper		0.035 mm		1	0
Dielectric	prepreg	FR4	0.1785 mm	Not specified	4.56	0.02
L4_BottomLayer.Cu	copper		0.035 mm		1	0
BottomLayer_Mask.Mask	Bottom Solder Mask	Not specified	0.01 mm	Green	3.3	0
BottomLayer_Paste.Paste	Bottom Solder Paste		0 mm		1	0
BottomLayer_Silk.Silkscreen	Bottom Silk Screen	Not specified	0 mm	White	1	0



BOARD CHARACTERISTICS

Board Thickness: 1.5470 mm Copper Layer Count:

Board overall dimensions: 54.9766 mm x 79.9766 mm

Min hole diameter: 0.2000 mm 0.1500 mm / 0.1500 mmMin track/spacing:

HAL lead-free Copper Finish: Impedance Control: Yes

Plated Board Edge: No οИ Castellated pads:

οИ Edge card connectors:

Layer Name	Туре	Material	Thickness (mm)	Color	Epsilon R	Loss Tangent
TopLayer_Silk.Silkscreen	Top Silk Screen	Not specified	mm 0	White	1	0
TopLayer_Paste.Paste	Top Solder Paste		mm 0		1	0
TopLayer_Mask.Mask	Top Solder Mask	Not specified	0.01 mm	Green	3.3	0
L1_TopLayer.Cu	copper		0.035 mm		1	0
Dielectric	prepreg	FR4	0.1785 mm	Not specified	4.56	0.02
L2_InnerLayer1.Cu	copper		0.035 mm		1	0
Dielectric	core	FR4	1.03 mm	Not specified	4.6	0.02
L3_InnerLayer1.Cu	copper		0.035 mm		1	0
Dielectric	prepreg	FR4	0.1785 mm	Not specified	4.56	0.02
L4_BottomLayer.Cu	copper		0.035 mm		1	0
BottomLayer_Mask.Mask	Bottom Solder Mask	Not specified	0.01 mm	Green	3.3	0
BottomLayer_Paste.Paste	Bottom Solder Paste		mm 0		1	0
BottomLayer_Silk.Silkscreen	Bottom Silk Screen	Not specified	mm 0	White	1	0

Board2Pdf: Black-And-White-BOT - Page 2/2

SJFOM

Sheet: Black-And-White-BOT

File: StepUp.kicad_pcb

Title: StepUp

Size: A4 Date: 2024-05-06 Rev: v0.2 KiCad E.D.A. 8.0.6 ld: 1/1