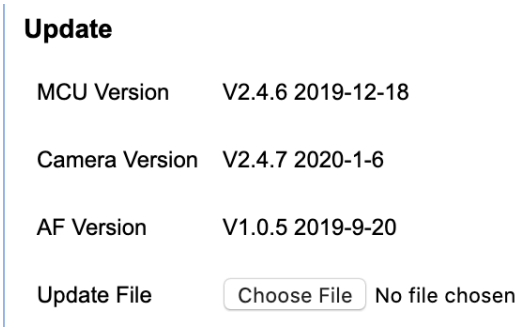
Device: Avonic CM71-IP

Introduction

A large number of parameters can be controlled on the Avonic CM71-IP camera. Control is via VISCA over IP. The Firmware on the camera have the following details



Please see the "PTZ Manual" at https://www.skaarhoj.com/support/manuals/ to learn more about PTZ control in general from SKAARHOJ controllers and in particular network recommendations.

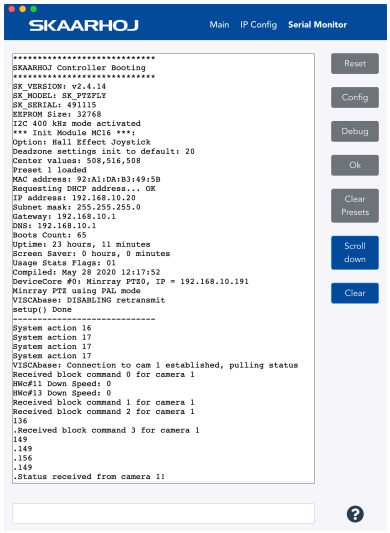
In this manual it is worth noticing that one should not add *additional* Device Cores to control multiple cameras. This is possible from the same Device Core but proper steps should be ensured (consecutive IP addresses on the cameras) for a good user experience.

Number of Cameras possible to control

Please notice from the Avonic Core it is possible to control up 7 cameras. In general this is the limit for our VISCA over IP Device Cores and our integration have not been tested above 7 cameras. If you want to control more than 7 cameras you will need to add an additional Device Core and configure the controller accordingly. None of our default configuration utilities 2 x Avonic Device Cores. As we have never tested with more than 7 cameras, we do not know how well performance and stability will be in such a configuration setup. We recommend only having 1 x Avonic Device Core installed per controller.

Confirm Connection

The Serial Monitor from the Firmware Application can be used to monitor connection status.



Device Configurations

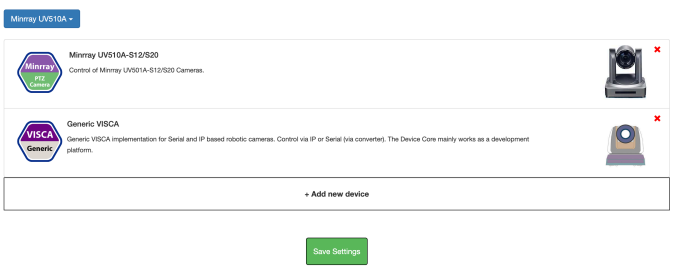
Device configuration options exist:

- Index 0: **VISCA over IP/Serial**
  - If “0” = VISCA over IP
  - If “1” = VISCA over TCP
  - If “2” = VISCA serial over IP
- Index 1: **Video Mode**
  - If “0” = PAL mode (default)
  - If “1” = NTSC mode
- Index 2: **Number of Cameras**
  - If “0” = 7 (default)
  - If “1-7” = the defined number of cameras

Example:

Enabling VISCA over serial could look like this device configuration code: “D0:0=1” where the general form would be “Dx:y=z” where “x” is the number of the device core as installed on the controller (starting with zero for the first device core), “y” the index number and “z” the value for that index.

If the Avonic device core is the first like below:

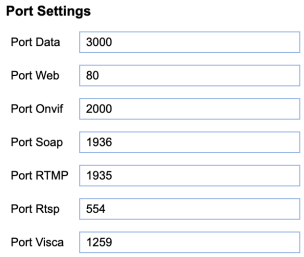


To confirm that a device configuration is in fact detected by the controller, please check it out on the serial monitor where it will be mentioned:

```
Memory A-D restored
Compiled: Mar 12 2020 12:29:09
D0[0] = 1
DeviceCore #0: Minrray PTZ0, IP = 192.168.10.114
ClientVISCAoverTCP:  __deviceIdx: 0
Minrray PTZ using PAL mode
VISCAbase: DISABLING retransmit
setup() Done
```

Port Settings

Our implementation was made with the Avonic CM71-IP default Port Visca setting of 1259.



Actions

An excerpt of the actions in the Device Core