

RELEASED 07-APR-2015

Page	Document	Page	Document	Page	Document	Page	Document
01	COVER PAGE	09	17	25
02	REVISION HISTORY	10	18	26
03	BLOCK DIAGRAM	11	19	27
04	SCHEMATIC_SHEET_1	12	20	28
05	SCHEMATIC_SHEET_2	13	21	29
06	SCHEMATIC_SHEET_3	14	22	30
07	15	23	31
08	16	24	32

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational
design notes.

DESIGN NOTE:
Example text for cautionary
design notes.

DESIGN NOTE:
Example text for debug notes.

DESIGN NOTE:
Example text for critical
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

APPROVED

REV: 1.2 / 13.02.2018

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REVISION HISTORY


Rev.	Date	Author	Description
0.1P	01/02/2017	COVER PAGE	COVER PAGE

TODOs, PROPOSALS & IMPROVEMENTS

Text

APPROVED

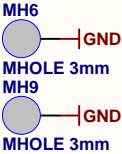
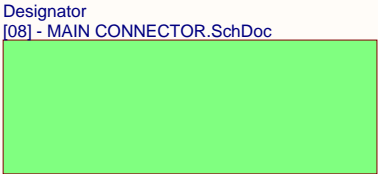
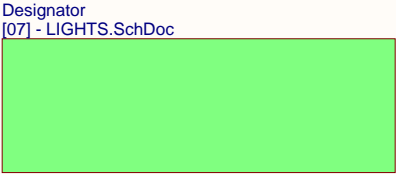
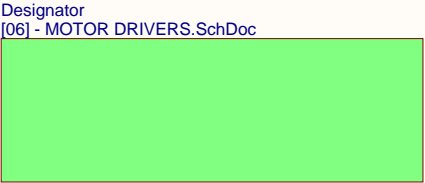
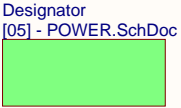
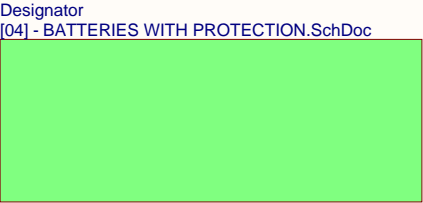
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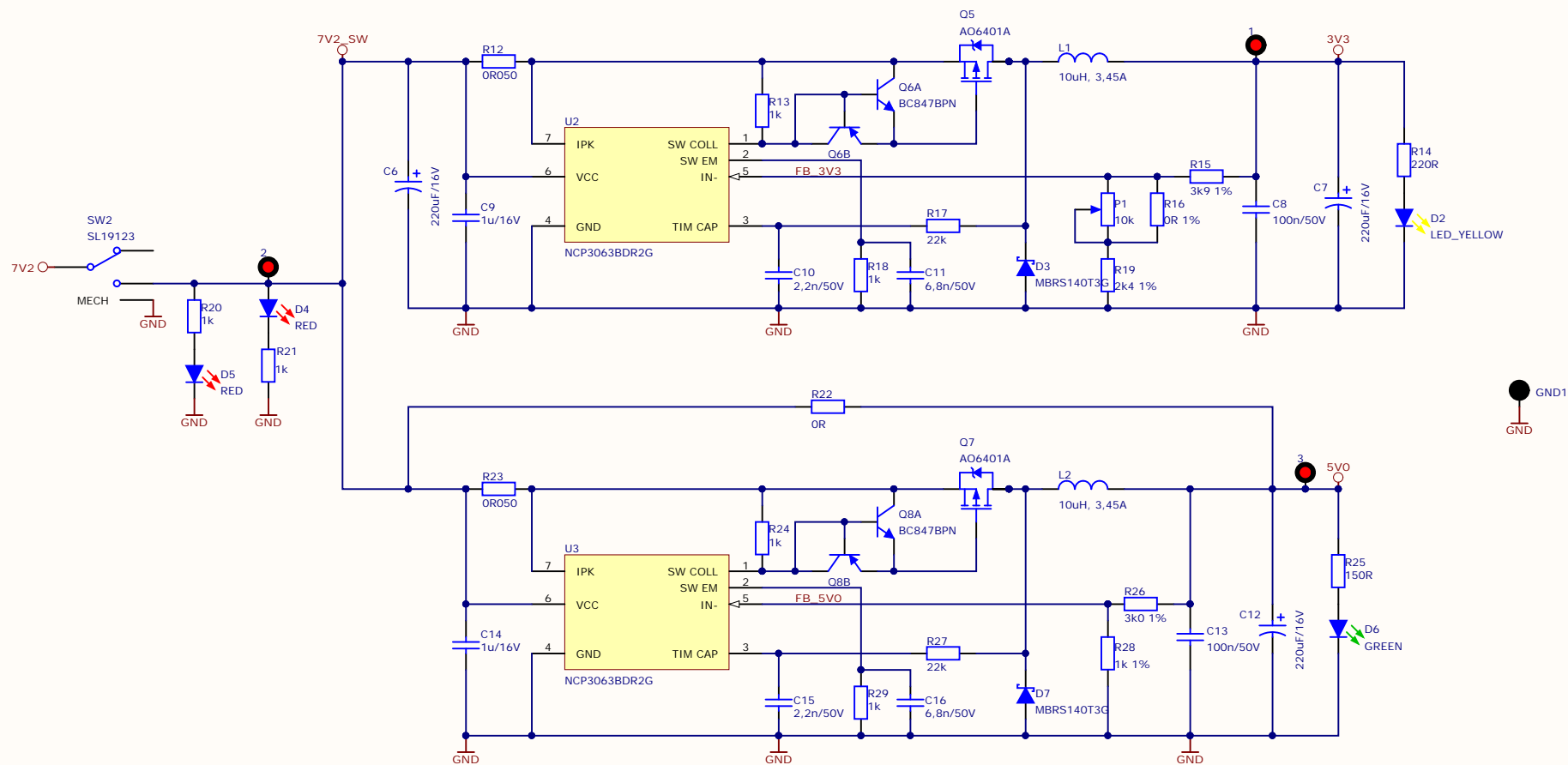


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ie-track-kit-pwr-1.PrjPcb

(Block Diagram)



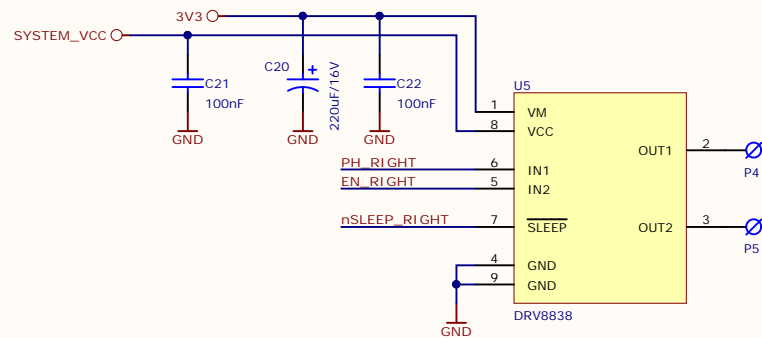
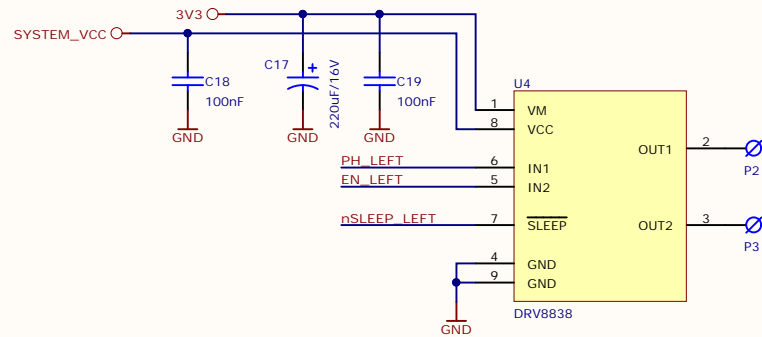


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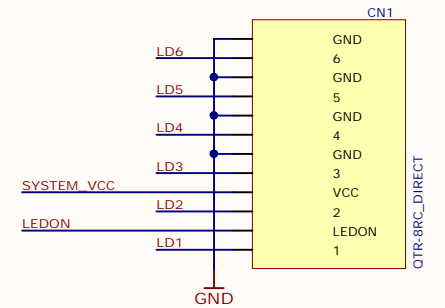
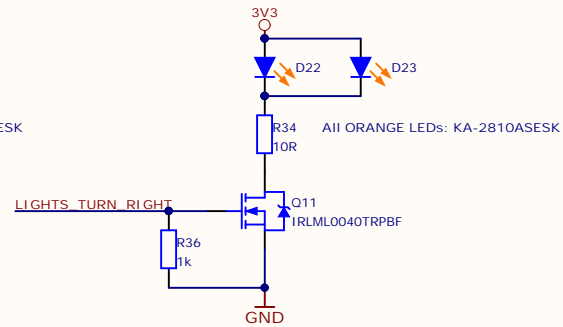
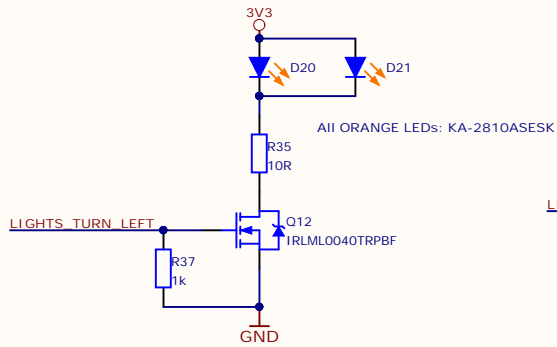
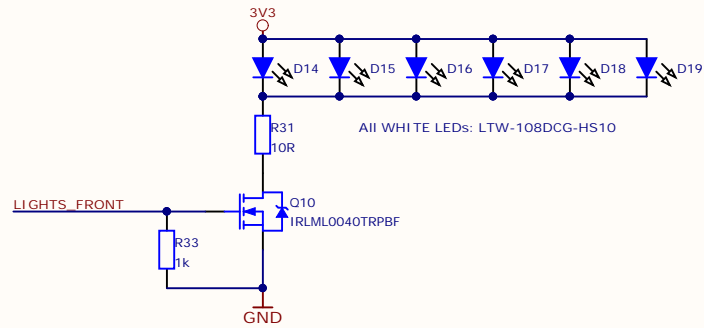
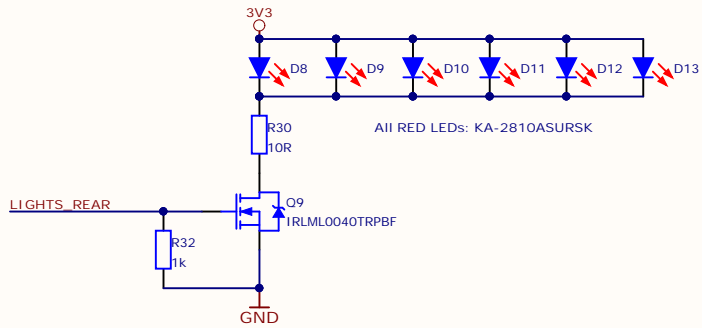


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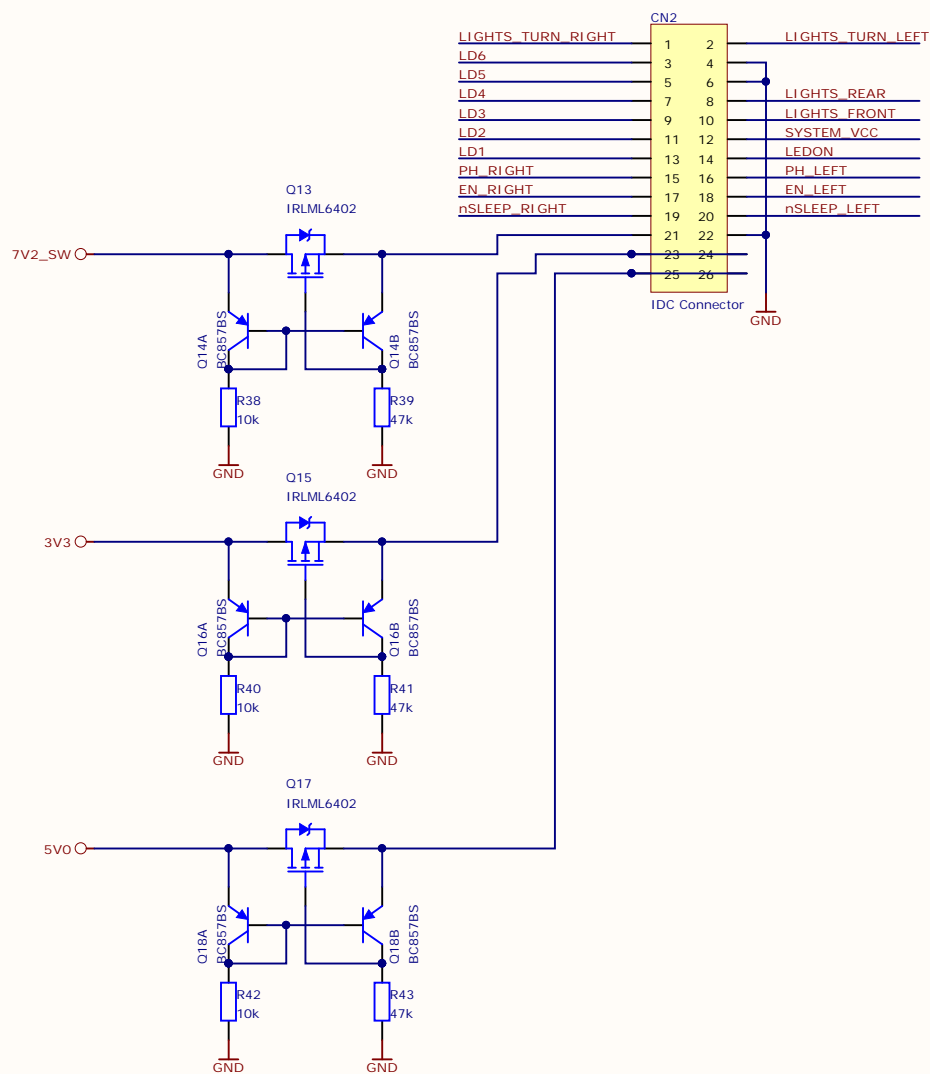


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