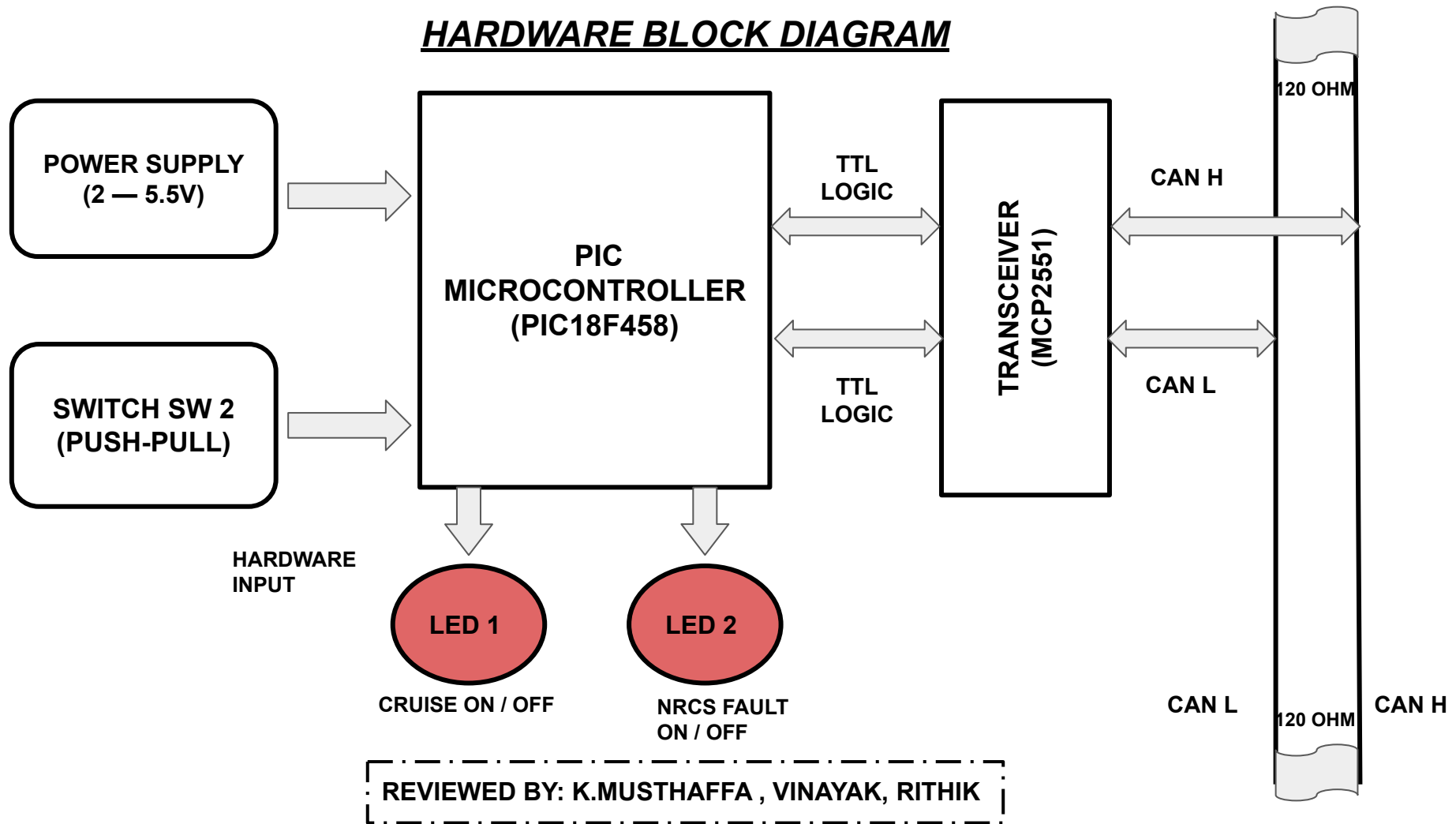
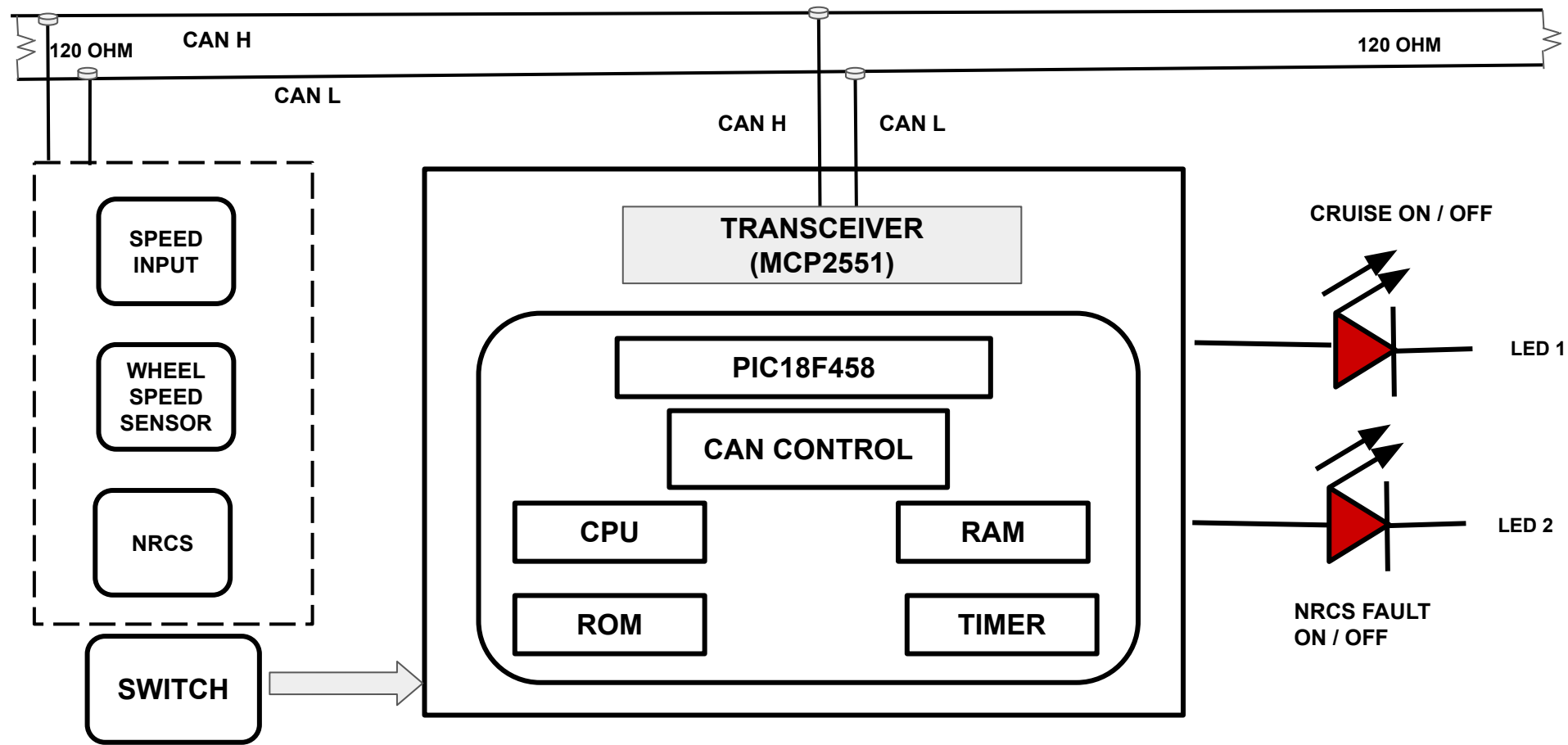


HARDWARE BLOCK DIAGRAM

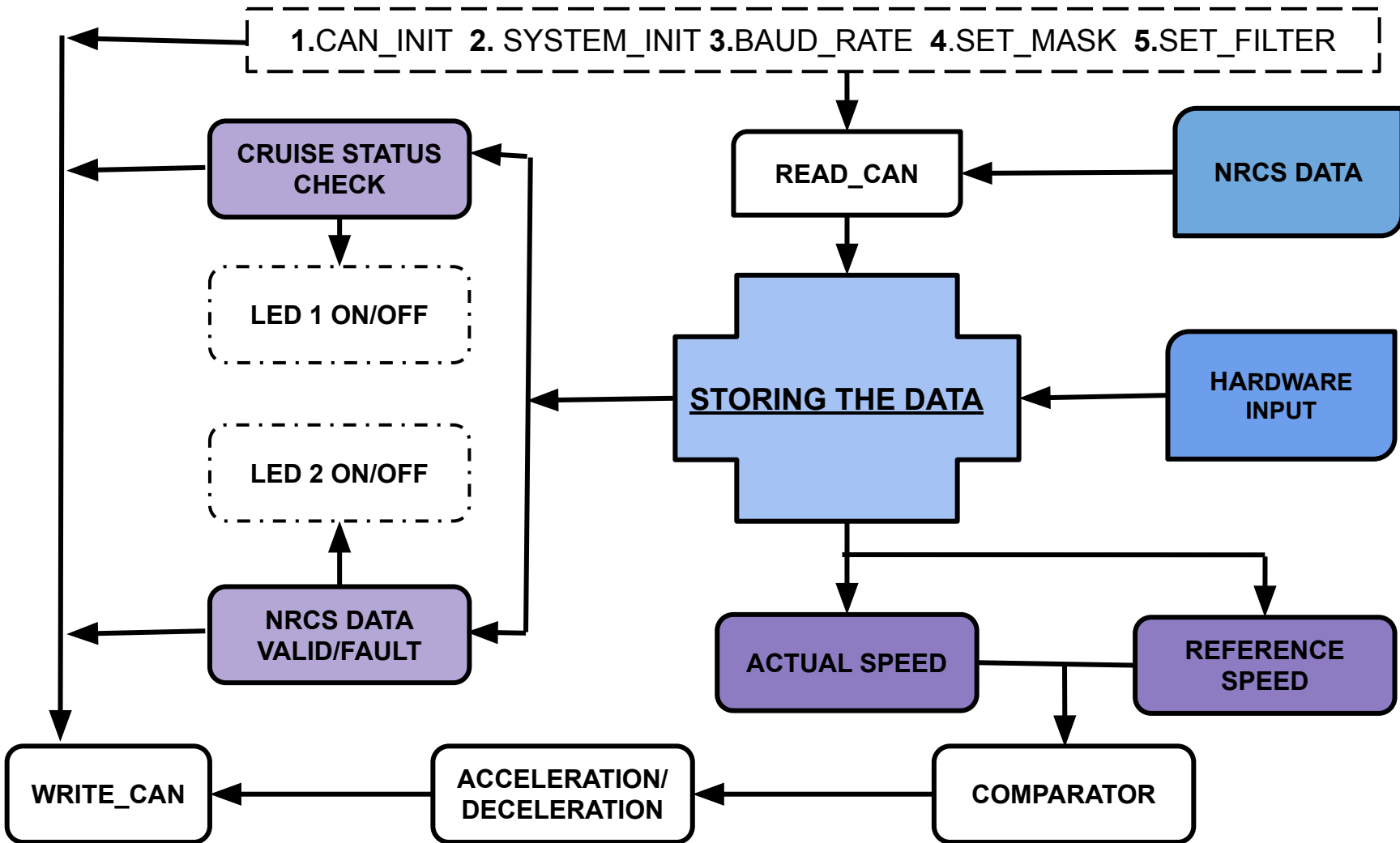




SYSTEM BLOCK DIAGRAM

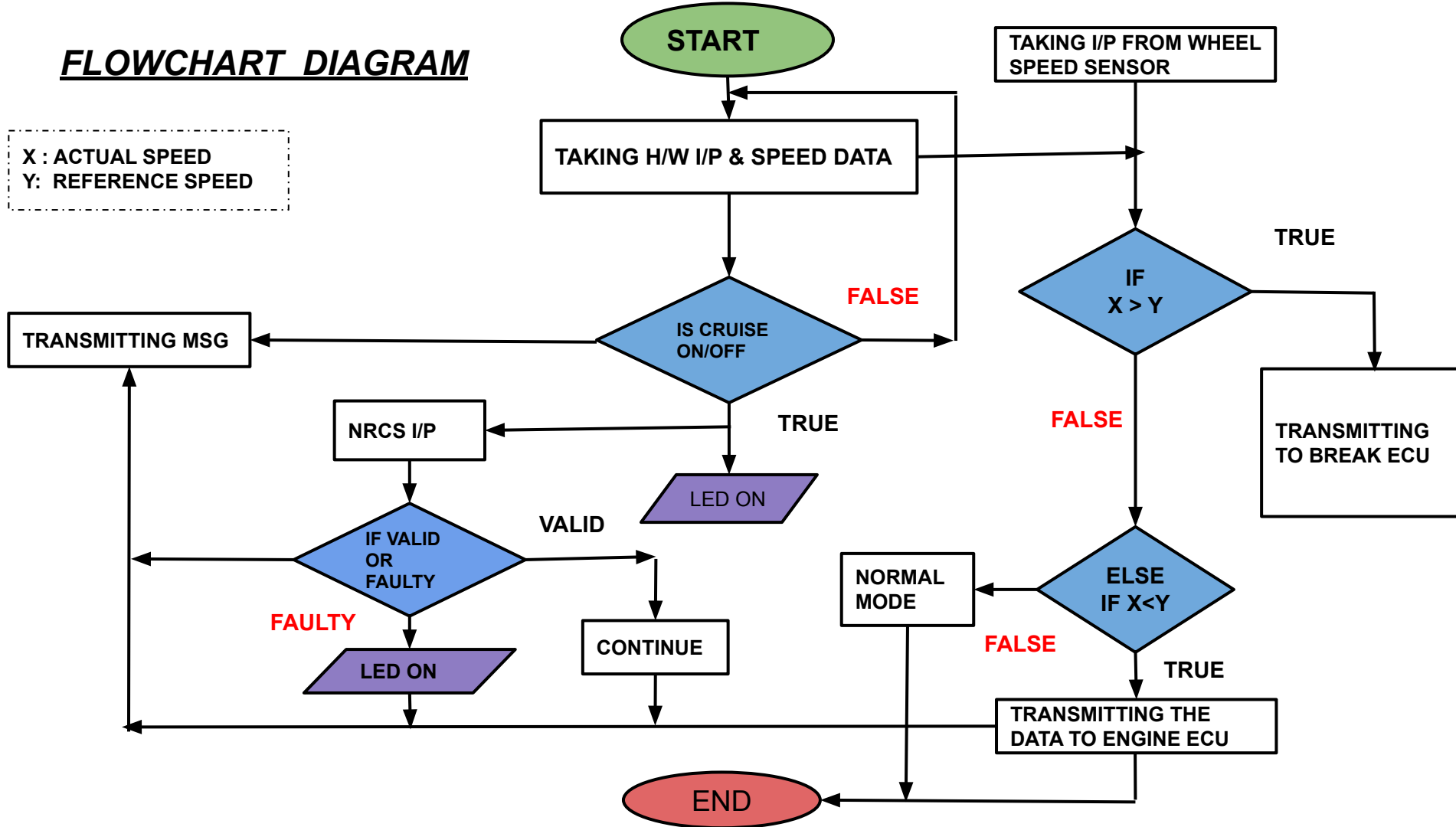
REVIEWED BY: K.MUSTHAFFA , VINAYAK, RITHIK

FUNCTIONAL BLOCK DIAGRAM

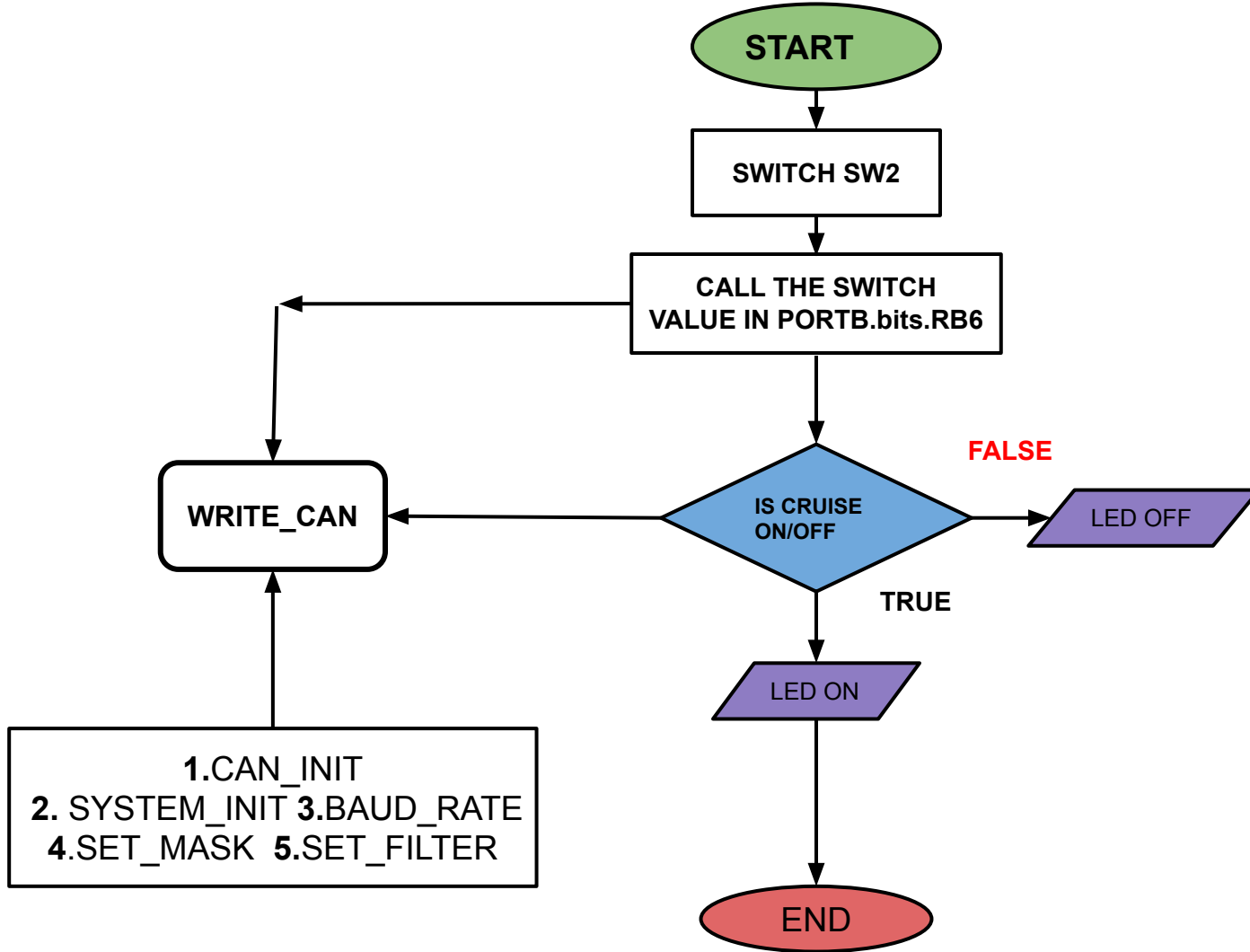


FLOWCHART DIAGRAM

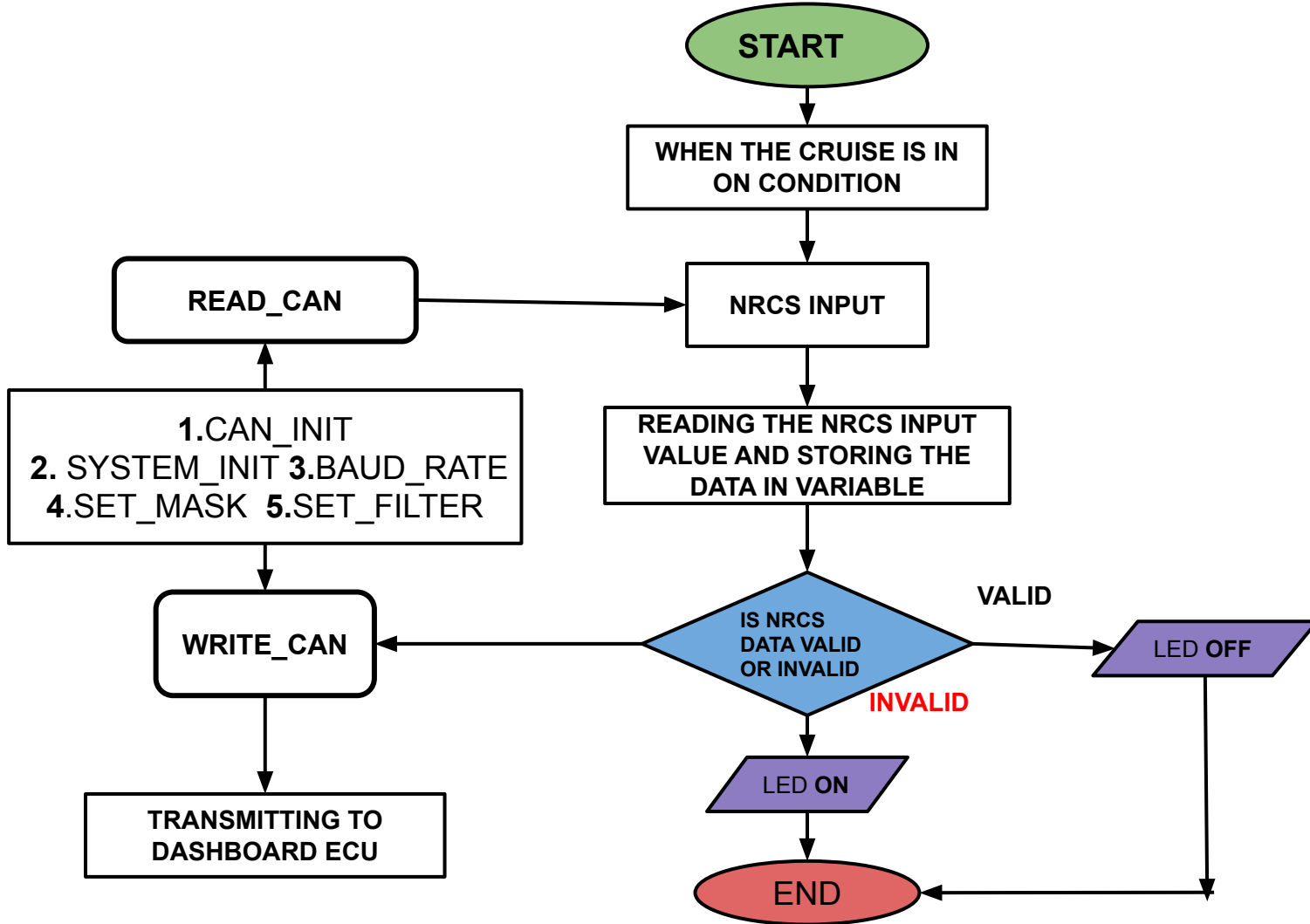
X : ACTUAL SPEED
Y: REFERENCE SPEED



HARDWARE FUNCTION FLOWCHART DIAGRAM



NRCS INPUT FUNCTION FLOWCHART
DIAGRAM



ACCELERATION AND DECELERATION FUNCTION FLOWCHART DIAGRAM

X : ACTUAL SPEED
Y: REFERENCE SPEED

