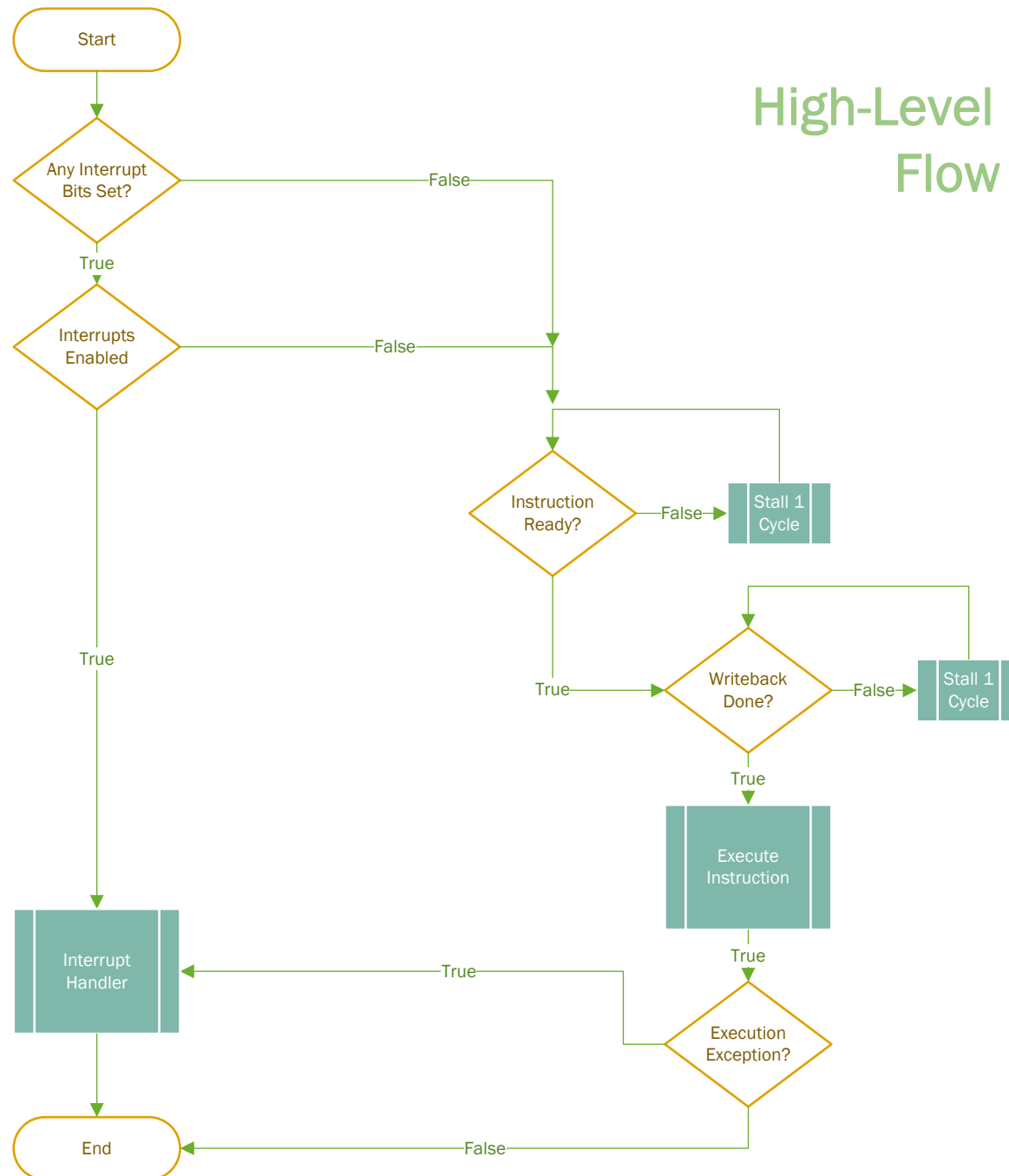
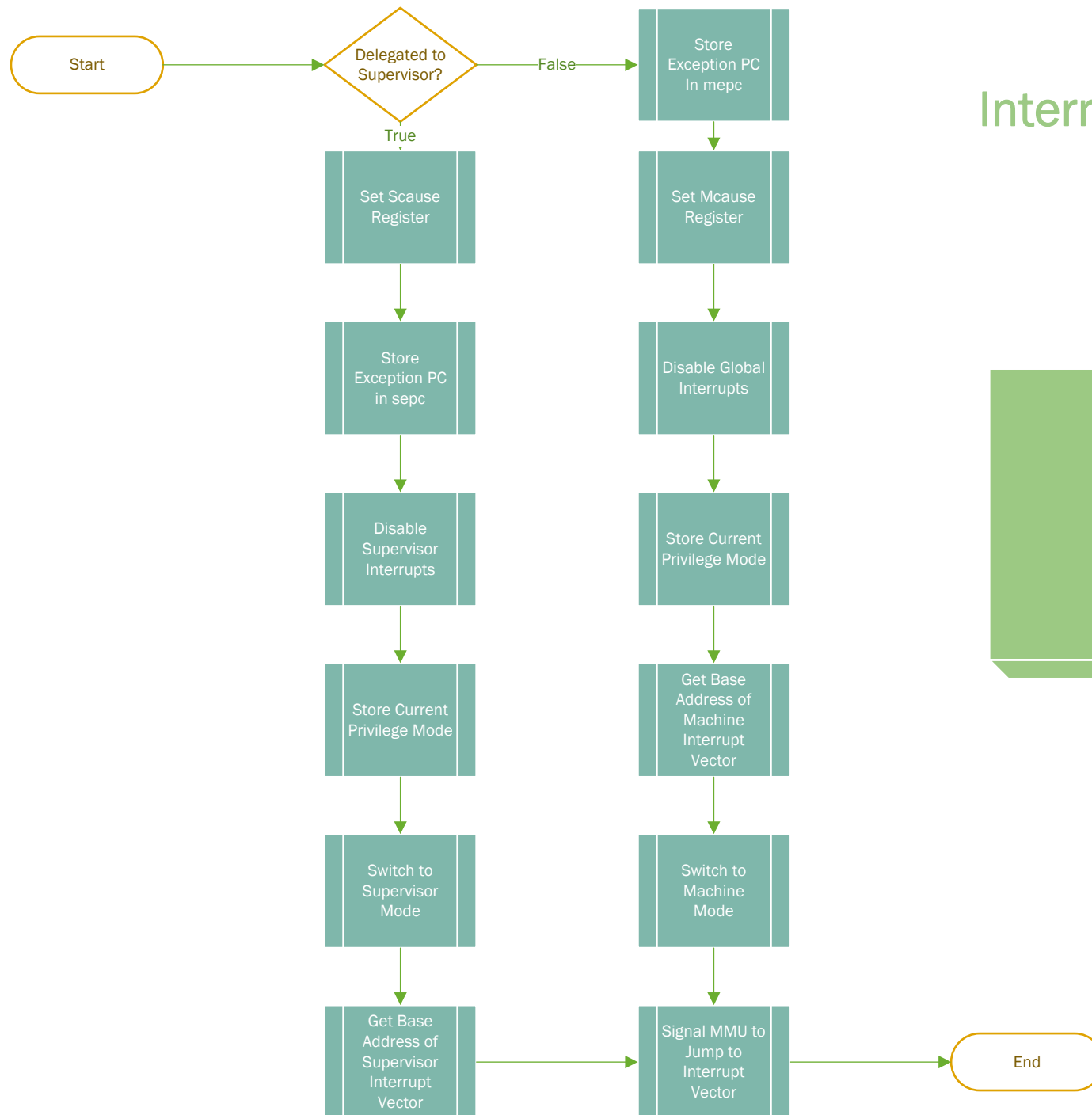


High-Level Control Unit Flow Chart



Interrupt Handler Flow Chart



Visible Data:
Masked Interrupt Bits
PC
Status Registers