Theory of Computation

> Introduction to the Theory of Computation, Michael Sipser

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➤ Operating Systems: Internals and Design Principles, William Stallings

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Data Communications and Networking

> Data Communications and Networking, Behrouz A. Forouzan

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Distributed System

> Designing Distributed Systems: Patterns and Paradigms for Scalable, Reliable Services, Brendan Burns

Designing Distributed Systems: Patterns and Paradigms for Scalable, Reliable Services
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Parallel Computing

> Algorithms and Parallel Computing, Fayez Gebali

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Algorithms
Parallel Computing Design Considerations
Parallel Algorithms and Parallel Architectures
Relating Parallel Algorithm and Parallel Architecture
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Measuring Benefits of Parallel Computing
Amdahl's Law for Multiprocessor Systems
Gustafson-Barsis's Law
Applications of Parallel Computing
Enhancing Uniprocessor Performance
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Increasing Processor Clock Frequency
Parallelizing ALU Structure
Using Memory Hierarchy
Pipelining
Very Long Instruction Word (VLIW) Processor
Instruction-Level Parallelism (ILP) and Superscalar Processors
Multithreaded Processor
Parallel Computers
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Systolic Processor
Cluster Computing
Grid (Cloud) Computing
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Concurrency Platforms

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Summary of Work Done in this Chapter

Case Study: One-Dimensional IIR Digital Filters
Introduction
The 1-D IIR Digital Filter Algorithm
The IIR Filter Dependence Graph
z-Domain Analysis of 1-D IIR Digital Filter Algorithm
Case Study: Two and Three Dimensional Digital Filters
Introduction
Line and Frame Wraparound Problems
2-D Recursive Filters
3-D Digital Filters
Case Study: Multi-rate Decimators and Interpolators
Introduction
Decimator Structures
Decimator Structures Decimator Dependence Graph
Decimator Scheduling
Decimator Scrieduling Decimator DAG for s1 = [1 0]
Decimator DAG for s2 = [1-1]
Decimator DAG for s3 = [1-1] Decimator DAG for s3 = [1 1]
Poly-phase Decimator Implementations
Interpolator Structures
Interpolator Structures Interpolator Dependence Graph
Interpolator Scheduling
Interpolator DAG for s1 = [1 0]
Interpolator DAG for s2 = [1 -1]
Interpolator DAG for s3 = [1 1]
Poly-phase Interpolator Implementations
Case Study: Pattern Matching
Introduction
Expressing the Algorithm as a Regular Iterative Algorithm (RIA)
Obtaining the Algorithm Dependence Graph
Data Scheduling
DAG Node Projection
DESIGN 1: Design Space Exploration When s = [1 1]
DESIGN 2: Design Space Exploration When s = [1 -1]
DESIGN 3: Design Space Exploration When s = [1 0]
Case Study: Motion Estimation for Video Compression
Introduction
FBMAs
Data Buffering Requirements
Formulation of the FBMA
Hierarchical Formulation of Motion Estimation
Hardware Design of the Hierarchy Blocks
Case Study: Multiplication Over GF(2m)
Introduction
The Multiplication Algorithm in GF(2m)
Expressing Field Multiplication as an RIA
Field Multiplication Dependence Graph
Data Scheduling
DAG Node Projection
Design 1: Using d1 = [1 0]
Design 2: Using d2 = [1 1]

Design 2. Using d2 = [1 1]
Design 3: Using d3 = [1 -1]
Applications of Finite Field Multipliers
Case Study: Polynomial Division Over GF(2)
Introduction
The Polynomial Division Algorithm
The LFSR Dependence Graph
Data Scheduling
DAG Node Projection
Design 1: Design Space Exploration When s1 = [1 -1]
Design 2: Design Space Exploration When s2 = [1 0]
Design 3: Design Space Exploration When s3 = [1 -o.5]
Comparing the Three Designs
The Fast Fourier Transform
Introduction
Decimation-in-Time FFT
Pipeline Radix-2 Decimation-in-Time FFT Processor
Decimation-in-Frequency FFT
Pipeline Radix-2 Decimation-in-Frequency FFT Processor
Solving Systems of Linear Equations
Introduction
Special Matrix Structures
Forward Substitution (direct Technique)
Back Substitution
Matrix Triangularization Algorithm
Successive Over Relaxation (SOR) (Iterative Technique)
Problems
Solving Partial Differential Equations Using Finite Difference Method
Introduction
FDM for 1-D Systems

Discrete Mathematics

Mathematical Structure for Computer Science: A Modern Approach to Discrete Mathematics, Judith L. Gersting

Mathematical Structure for Computer Science: A Modern Approach to Discrete Mathematics
Formal Logic
Statements, Symbolic Representation, and Tautologies – Topics 4 and Exercises
Propositional Logic – Topics 4 and Exercises
Quantifiers, predicates, and Validity – Topics 3 and Exercises
Predicate Logic – Topics 8 and Exercises
Logic Programming – Topics 4 and Exercises
Proof of Correctness – Topics 3 and Exercises
Review on the Computer
Proofs, Recursion, and Analysis of Algorithms
Proof Techniques – Topics 8 and Exercises
Induction – Topics 3 and Exercises
More on Proof of Correctness – Topics 2 and Exercises
Recursive Definitions – Topics 4 and Exercises
Recurrence Relations – Topics 5 and Exercises
Analysis of Algorithms – Topics 3 and Exercises
Sets, Combinatory, Probability and Number Theory
Sets – Topics 7 and Exercises
Counting – Topics 4 and Exercises
Principle of Inclusion and Exclusion; Pigeonhole Principle – Topics 2 and Exercises
Permutations and Combinations – Topics 4 and Exercises
Probability – Topics 5 and Exercises
Binomial Theorem – Topics 3 and Exercises
Number Theory – Topics 3 and Exercises
Review on the Computer
Relations, Functions, and Matrices
Relations – Topics 5 and Exercises
Topological Sorting – Exercises
Relations and Databases – Topics 5 and Exercises
Functions – Topics 12 and Exercises
The Mighty Mod Function – Topics 8 and Exercises
Matrices – Topics 3 and Exercises
Review on the Computer
Graphs and Trees
Graphs and Their Representations – Topics 8 and Exercises
Trees and Their Representations – Topics 5 and Exercises
Decision Trees – Topics 4 and Exercises
Huffman Codes – Topics 4 and Exercises
Review on the Computer
Graph Algorithms
Directed Graphs and Binary Relations; Warshall's Algorithm – Topics 3 and Exercises
Euler Path and Hamiltonian Circuit – Topics 2 and Exercises
Shortest Path and Minimal Spanning Tree – Topics 2 and Exercises
Traversal Algorithms – Topics 4 and Exercises
Articulation Points and Computer Networks – Topics 3 and Exercises
Review on the Computer

Boolean Algebra and Computer Logic
Boolean Algebra Structure – Topics 5 and Exercises
Logic Networks – Topics 11 and Exercises
Minimization – Topics 5 and Exercises
Review on the Computer
Modeling Arithmetic, Computation, and Languages
Algebraic Structures – Topics 4 and Exercises
Finite-State Machines – Topics 8 and Exercises
Turing Machines – Topics 8 and Exercises
Formal Languages
Classes of Grammars
Formal Languages and Computational Devices
Context-Free Grammars
Exercises
Review on the Computer
Appendix A: Derivation Rules for Propositional and Predicate Logic
Appendix B: Summation Notation
Appendix C: The Logarithm Function

Schedule of Practice

	System and Communication	
01		
	Total Lab Topics	
02		
03		
04		
	Total Theoretical Topics	
05		
06		
	Total Elective Topics	
	4 Months: (4 Weeks x 4 = 16 Weeks x 15 Hours) = 240 Hours	
	Per Day : (1000 Topics / 240 = 5 Topics x 3 Hours) = 15 Topics	

Practice Hours Per Week	
Theory	5 Days x 3 Hours (Evening) = 15 Hours
Lab Practice	5 Days x 3 Hours (Morning) = 15 Hours
Total Lesson Hours	5 Days x 6 Hours (Full Day) = 30 Hours
Review and Test	1 Days x 6 Hours (Full Day) = 06 Hours
Industrial Project	1 Days x 6 Hours (Full Day) = 06 Hours
Total Review Hours	2 Days x 6 Hours (Full Day) = 12 Hours
Week Hours	7 Days x 6 Hours (Full Day) = 42 Hours

Resources:-

- 1. Listed Books
- 2. GPT: Concept and Questions
- 3. YouTube: Professional and Industrial Practice Example
- 4. Websites: Additional Resources
- 5. Certification: MIT, Harvard, Oxford, Microsoft, Google, OpenAI, IBM, Etc

Practice Recommendation:-

- Clear Each Concept Step by Step
- Apply to Get Output
- Manipulate
- > Then Summarize the Lesson
- Make the Connected and Related List of Summarized Concept

Review Recommendation:-

- Make Quiz, Interview Question (Google, Microsoft), Competitive Questions Using GPT
- Make Projects using GPT and Community According to Market and Client Needs