



National University of Computer & Emerging Sciences, Karachi Spring 2022 CS-Department



Quiz No: 03-AAA

Course Code: EE-1005	Course Name: Digital Logic Design	
Instructor Name: Hamza A	hmed	
Student Roll No:	Section:	
Date:	Time:	

Total Time: 20 min

Max Marks: 15 points

Question # 01:

(05 Marks)

A 555-timer configured to run in the astable mode (oscillator). Determine the frequency of the output and the duty cycle. (R₁= 4.5K Ω , R₂= 2.7K Ω and C₁= 43 pF)

$$R_{1} = \frac{1.44}{(4.5 \times 10^{3})} \times 43 \times 10^{17}$$

$$R_{2} = \frac{(5)}{(1)} IHRES \quad OUT \quad (5)$$

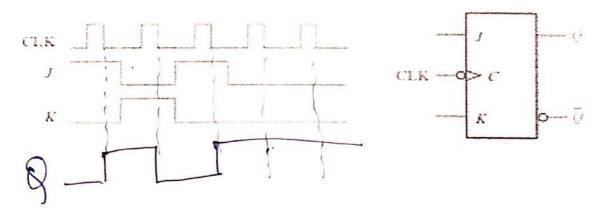
$$C_{1} \quad GND \quad tw = 1.1 \times 6.1 \times C.1$$

$$AAA \quad T_{1} \quad DL = tw$$

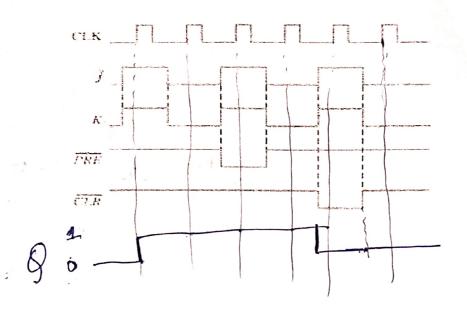
Question # 02

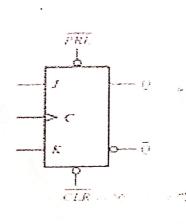
(05 Marks)

An edge-triggered J-K flip-flops are shown in Figure below. If the inputs are as shown, draw the Q output of flip-flop relative to the clock.



Determine the Q waveform relative to the clock if the signals shown in Figure below are applied to the inputs of the J-K flip-flop. Assume that Q is initially LOW.







National University of Computer & Emerging Sciences, Karachi Spring 2022 CS-Department



Quiz No: 03BBB

Course Code: EE-1005	Course Name: Digital Logic Design	
Instructor Name:		
Student Roll No:	Section:	
Date:	Time:	

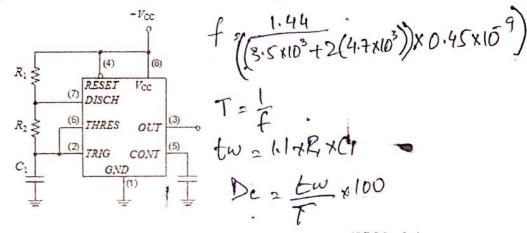
Total Time: 20 min

Max Marks: 15 points

Question #01:

(05 Marks)

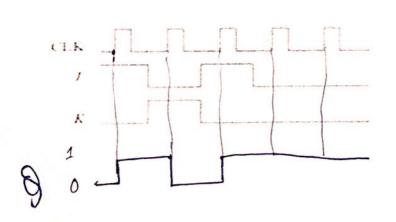
A 555-timer configured to run in the astable mode (oscillator). Determine the frequency of the output and the duty cycle. (R₁= 3.5K Ω , R₂= 4.7K Ω and C₁= 0.45 nF)

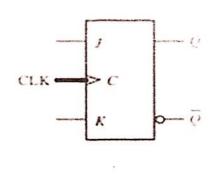


Question # 02

(05 Marks)

An edge-triggered J-K flip-flops are shown in Figure below. If the inputs are as shown, draw the Q output of flip-flop relative to the clock.





Determine the Q waveform relative to the clock if the signals shown in Figure below are applied to the inputs of the J-K flip-flop. Assume that Q is initially HIGH.

