Bitwuzla at the SMT-COMP'21

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Tracks/Divisions

Single Query:	QF_{A,BV,FP,FPLRA,UF}$^+$
Incremental:	QF_{A,BV,FP,FPLRA,UF}$^+$
Unsat Core:	QF_{A,BV,FP,FPLRA,UF}$^+$
Model Validation:	QF_BV, QF_UFBV

News

- Code now available at https://github.com/bitwuzla/bitwuzla
- New API for C, Python, and OCaml[1]
- Floating-points: Real to FP support (for FPLRA logics)
- Bit-vectors: CaDiCaL version sc2021 as default SAT backend for all logics
- Lots of improvements/refactoring going on behind the scenes

https://bitwuzla.github.io

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[1] Thanks to Frédéric Recoules for the OCaml bindings