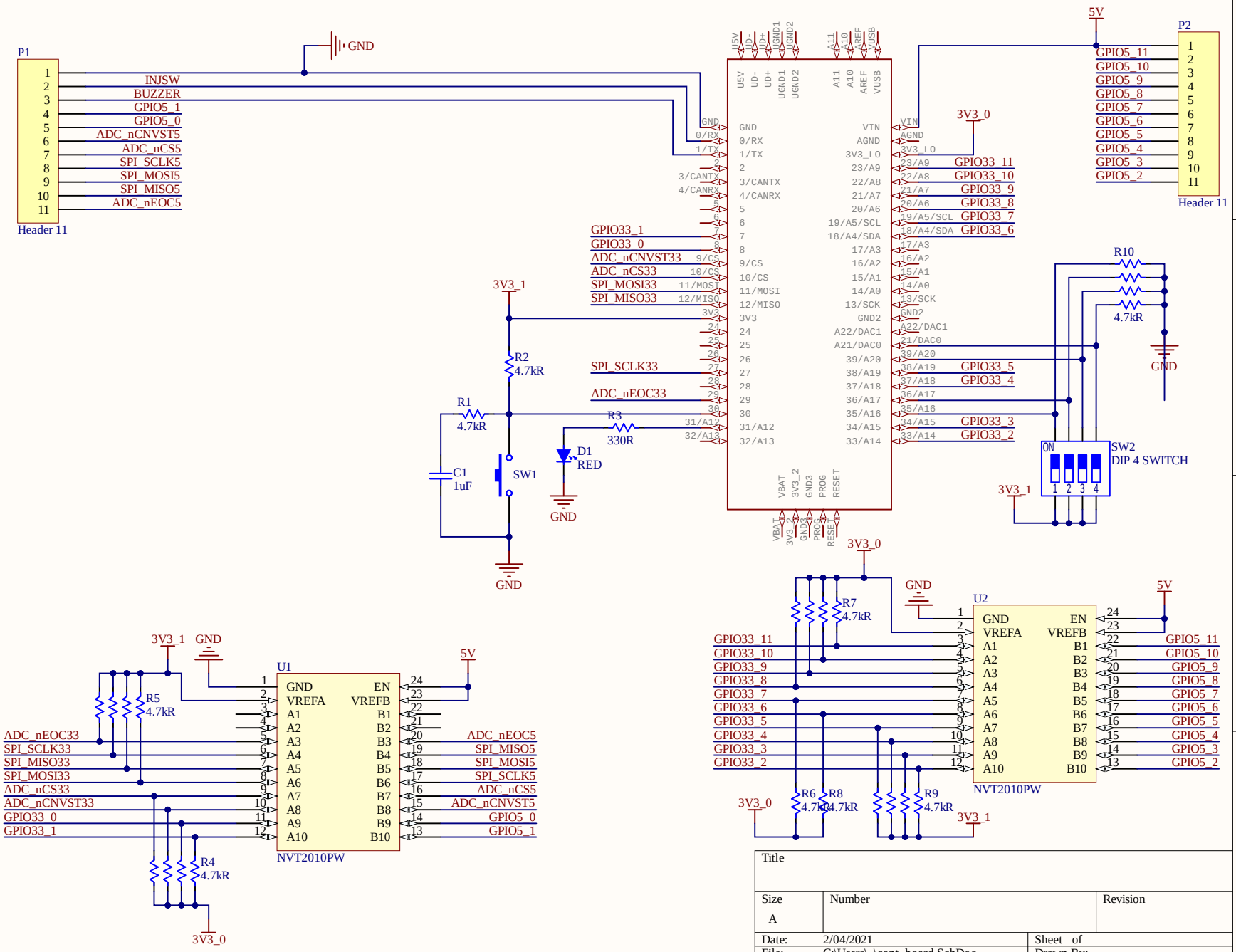
Line #	Name	Description	Designator	Quantity	Manufacturer 1	Manufacturer Part Number 1	Manufacturer Lifecycle 1	Supplier 1	Supplier Part Number 1	Supplier Unit Price 1	Supplier Subtotal 1
	TEENSY_3.5/3.6		1	1							
	1uF	Cap Ceramic 1uF 16V Y5V -20% to 80% Pad SMD 0805 85C T/R	C1	1	KEMET	C0805C105Z4VACTU	Volume Production	Digi-Key	399-8011-1-ND	0.11	0.11
	RED	WURTH ELEKTRONIK 150080SS75000 LED, 0805, SUP RED, 60MCD, 630NM	D1	1	Wurth Electronics	150080SS75000	Volume Production	Digi-Key	732-4985-1-ND	0.18	0.18
	Header 11	Header, 11-Pin	P1, P2	2							
	4.7kR	Res Thin Film 0805 4.7K Ohm 0.5% 0.125W(1/8W) ±25ppm/C Molded SMD Automotive Punched T/R	R1, R2, R6, R8	4	Panasonic	ERA-6AED472V	Volume Production	Digi-Key	P123914CT-ND	0.1	0.4
	330R	Res Thin Film 0805 330 Ohm 0.5% 0.125W(1/8W) ±25ppm/C Molded SMD Automotive Punched T/R	R3	1	Panasonic	ERA-6AED331V	Volume Production	Digi-Key	P123837CT-ND	0.1	0.1
	4.7kR	RES ARRAY 4 RES 4.7K OHM 1206	R4, R5, R7, R9, R10	5	Bourns	CAY16-472J4LF	Volume Production	Digi-Key	CAY16-472J4LFCT-ND	0.1	0.5
	B3FS-1000P	OMRON - B3FS1000P - SWITCH, FLAT, 6X6X3.1, 100GF	SW1	1	Omron	B3FS-1000P BY OMZ	Unknown	Digi-Key	SW423CT-ND	0.59	0.59
	DIP 4 SWITCH		SW2	1	CUI Devices	DS04-254-2-04BK-SMT-TR	Unknown	Digi-Key	2223-DS04-254-2-04BK-SMT-CT-ND	0.66	0.66
	NVT2010 PW	10 pin Bidirectional Level Shifter IC	U1, U2	2	NXP Semiconductors	NVT2010PW,118	Volume Production	Digi-Key	568-5299-1-ND	0.97	1.94


P1
Header 11
INJSW
BUZZER
GPIO5_1
GPIO5_0
ADC_nCNVST5
ADC_nCS5
SPI_SCLK5
SPI_MOSI5
SPI_MISO5
ADC_nEOC5
GND
P2
Header 11
5V
GPIO5_11
GPIO5_10
GPIO5_9
GPIO5_8
GPIO5_7
GPIO5_6
GPIO5_5
GPIO5_4
GPIO5_3
GPIO5_2
3V3_0
GPIO33_11
GPIO33_10
GPIO33_9
GPIO33_8
GPIO33_7
GPIO33_6
GPIO33_1
GPIO33_0
ADC_nCNVST33
ADC_nCS33
SPI_MOSI33
SPI_MISO33
3V3_1
R2
4.7kR
SPI_SCLK33
ADC_nEOC33
R1
4.7kR
R3
330R
D1
RED
C1
1uF
SW1
GPIO33_5
GPIO33_4
GPIO33_3
GPIO33_2
R10
4.7kR
SW2
DIP 4 SWITCH
U1
NVT2010PW
R5
4.7kR
R4
4.7kR
ADC_nEOC5
SPI_MISO5
SPI_MOSI5
SPI_SCLK5
ADC_nCS5
ADC_nCNVST5
GPIO5_0
GPIO5_1
U2
NVT2010PW
R7
4.7kR
R6
4.7kR
R8
4.7kR
R9
4.7kR
Title
Size
A
Number
Revision
Date: 2/04/2021
Sheet of
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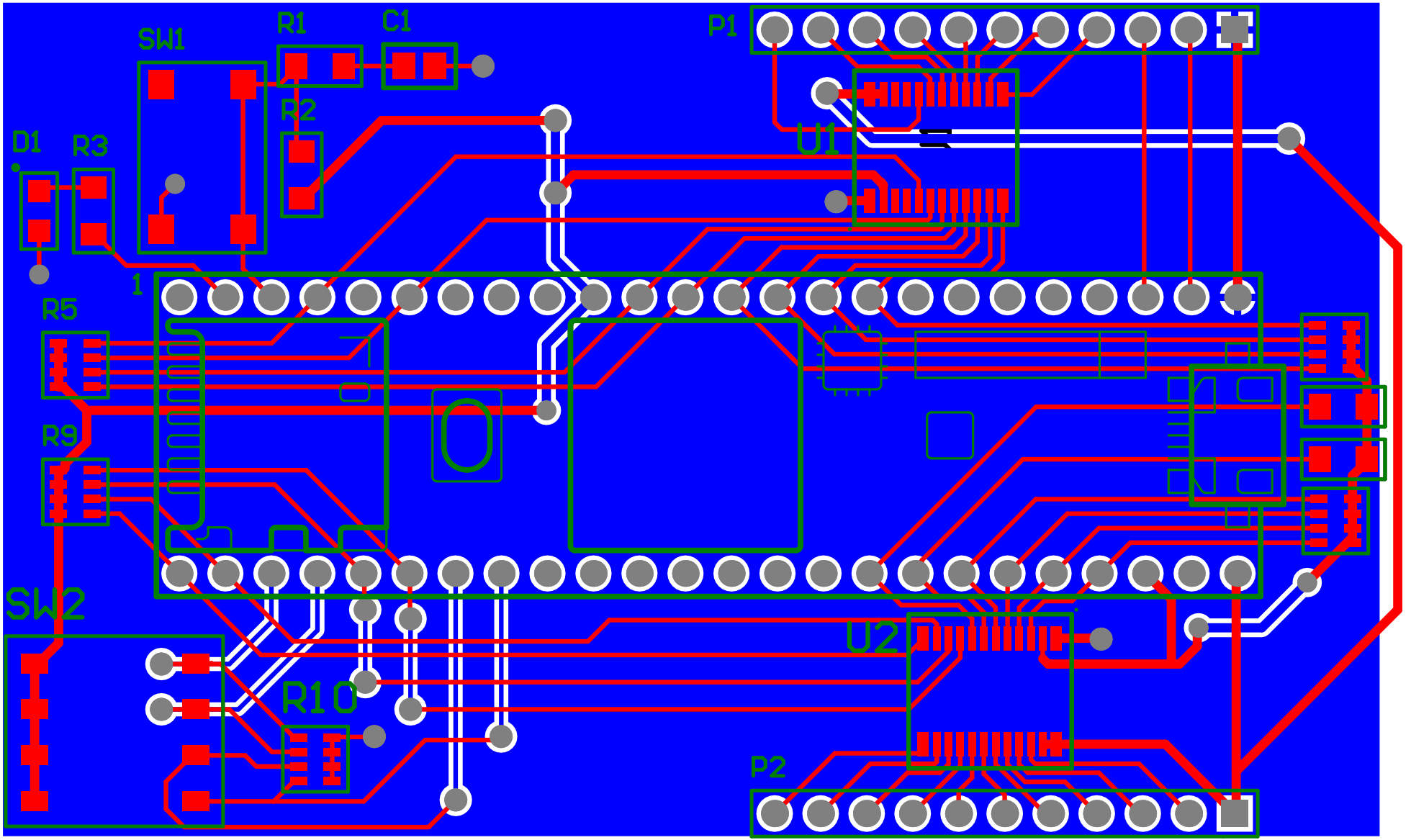
Drawn By:


SW1
R1
C1
P1
R2
D1
R3
U1
1
R5
R9
SW2
U2
R10
P2

R1
C1
SW1
R2
D1
R3
U1
R5
R9
TEENSY
3.6
R4
R8
R6
R7
SW2
R10
U2

Design Rules Verification Report

Filename : C:\Users\Public\Documents\Altium\Projects\EFIPCB_2020\cont_board\cont_board. Warnings 0

Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.254mm) (All),(All)	0
Clearance Constraint (Gap=0.254mm) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.127mm) (Max=1mm) (Preferred=0.254mm) (All)	0
Width Constraint (Min=0.254mm) (Max=0.254mm) (Preferred=0.254mm) (All)	0
Width Constraint (Min=1mm) (Max=5.5mm) (Preferred=3.3mm) (InNetClass('HighPower'))	0
Width Constraint (Min=0.127mm) (Max=5.5mm) (Preferred=0.254mm) (InNet('GND'))	0
Width Constraint (Min=0.127mm) (Max=1mm) (Preferred=0.254mm) (InNetClass('Signal'))	0
Width Constraint (Min=0.254mm) (Max=2mm) (Preferred=1.5mm) (InNetClass('LowPower'))	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Minimum Annular Ring (Minimum=0.15mm) (All)	0
Minimum Annular Ring (Minimum=0.23mm) (IsVia)	0
Hole Size Constraint (Min=0.025mm) (Max=2.54mm) (All)	0
Hole Size Constraint (Min=0.3mm) (Max=6.3mm) (IsVia)	0
Hole Size Constraint (Min=0.7mm) (Max=6.35mm) (IsPad)	0
Hole Size Constraint (Min=0.25mm) (Max=6.3mm) (All)	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Hole To Hole Clearance (Gap=0.5mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.15mm) (All),(All)	0
Silk To Solder Mask (Clearance=0.15mm) (IsPad),(All)	0
Silk to Silk (Clearance=0.254mm) (All),(All)	0
Net Antennae (Tolerance=0mm) (All)	0
Board Clearance Constraint (Gap=0mm) (All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Prefered=12.7mm) (All)	0
Total	0