

APPENDIX E. EUROPA CLIPPER FSW FCPL MODULES LOGICAL SLOC STATISTICS

FSW CORE PRODUCT LINE (FCPL)											
	Actual Logical SLOC per Release								Projected Logical SLOC		
Modules	R4.1.1	R5.0.0	R6.0.0	R7.0.0	R7.0.0	R9.0.0	R10.0.0	Growth Factor	R10.5.0	R11.0.0	WM (CURRENT TOTAL)
ADC_SVC	83	83	659	657	657	741	750	1.4	1082.4	1562.1	2.8
ADL_SVC	634	680	700	716	716	688	615	1.0	611.9	608.8	2.7
BC_MGR	2466	2571	2599	2622	2622	3241	2975	1.0	3069.5	3167.0	11.6
CBM_CTL	0	0	0	920	920	2697	2731	1.4	3924.9	5640.8	11.1
CFDP_SVC	5340	6168	8061	7521	7580	7735	7615	1.1	8079.0	8571.3	49.1
CMD_SVC	1629	1838	2060	2062	2062	2284	2185	1.1	2294.6	2409.7	16.0
CPU_DRV	1094	1094	1110	1145	1145	2237	2328	1.1	2640.2	2994.4	6.5
CRC_MGR	1556	1577	1546	1560	1560	1664	1744	1.0	1777.5	1811.6	4.2
CSTR_MGR	0	0	0	2097	2097	2978	3166	1.1	3632.0	4166.6	18.0
DATA_PTM	295	305	329	466	464	558	559	1.1	621.8	691.7	4.3
DMA_DRV	206	206	226	226	226	226	230	1.0	234.3	238.6	3.6
DPC_SVC	1101	1104	1117	1276	1276	1702	1749	1.1	1889.3	2040.8	18.2
DPM_SVC	1711	1945	1992	1993	1993	3661	3682	1.1	4183.7	4753.7	10.8
DSS_MGR	106	686	850	877	851	1007	1027	1.5	1499.5	2189.4	6.5
DWN_MGR	6002	6000	4967	5091	5063	5167	5472	1.0	5388.3	5305.9	14.9
ECM_MGR	0	252	952	1032	1032	1392	1411	1.4	1991.4	2810.5	3.7
ETHEMIS_MGR	0	252	152	846	846	1056	1147	1.4	1553.1	2102.9	4.3
EVR_SVC	2739	2555	2361	2378	2352	2624	2661	1.0	2648.2	2635.5	23.7
FILE_SVC	2980	3360	3842	4401	4401	4857	5009	1.1	5461.9	5955.7	11.8
FPM_SVC	176	299	308	334	334	335	335	1.1	372.9	415.2	4.5
GNC_PTM	247	246	263	395	395	443	447	1.1	493.4	544.7	1.5
HEALTH_SVC	2268	2269	2273	2311	2311	2649	2639	1.0	2706.5	2775.7	11.1
INST_PTM	7	1303	1445	1582	1582	1841	1843	2.5	4665.8	11812.3	3.9
IO_PTM	184	187	239	350	350	410	411	1.1	469.9	537.3	2.1

MC_MGR	0	0	0	137	137	116	116	0.9	109.7	103.8	4.5
NAND_MGR	2725	2702	3072	3252	3252	3147	3252	1.0	3349.3	3449.4	10.8
NVDS_SVC	4478	5043	4711	4900	4902	5913	6023	1.1	6328.0	6648.5	10.6
NVFS_SVC	3076	3154	3214	3466	3418	3681	3718	1.0	3837.3	3960.5	6.4
OSAL_SVC	393	2197	2251	2265	2265	2556	2558	1.4	3495.3	4776.1	2.5
PIMSU_MGR	0	252	1015	1138	1138	1278	1305	1.4	1813.2	2519.4	1.9
RADMON_MGR	0	0	152	157	157	949	972	1.6	1545.7	2458.0	0.0
REASON_MGR	0	252	1680	1818	1818	2313	2405	1.6	3776.2	5929.3	4.0
REU_SVC	587	173	239	355	355	538	539	1.0	531.4	523.9	4.4
RM_CTL	0	57	1246	1434	1434	1342	1440	1.9	2747.0	5240.3	3.9
RT_MGR	0	2027	2046	2053	2053	2073	2057	1.0	2063.1	2069.1	8.3
SARB_SVC	0	0	0	0	0	359	355	1.0	351.0	347.1	1.6
SCFG_SVC	0	0	0	997	997	1062	1079	1.0	1107.8	1137.4	3.1
SFP_EXE	1101	24	900	4543	4543	11692	12376	1.5	18523.0	27723.3	16.5
SHELL_CONFIG_SVC	0	0	0	0	0	0	0	0.0	0.0	0.0	0.0
SHELL_CTL	15363	15364	15379	15527	15527	15734	15804	1.0	15878.7	15953.8	4.3
SHELL_PTM	129	131	126	1049	1049	1070	168	1.0	175.6	183.5	1.3
THRM_PTM	0	7	101	156	156	176	176	1.9	335.4	639.3	0.0
TIME_SVC	1457	1543	1866	2329	2329	2958	3161	1.1	3596.5	4092.1	14.7
TYPES_SVC	67	70	74	74	74	56	56	1.0	54.4	52.8	1.0
UPL_MGR	4038	4058	4074	4084	4074	4255	4304	1.0	4350.0	4396.5	9.4
UPL_PTM	174	190	205	306	305	351	349	1.1	391.9	440.1	1.9
TOTAL	64412	72224	80402	92898	92818	113812	114944		135653	168386	357.6