# Appendix Data-driven Mutation Testing: LuxSpace Case Study

This Appendix describe the procedures adopted to execute data-driven mutation testing on the LuxSpace case study system. Section 1 provide a detailed overview of the case study and the function targeted by data-driven mutation testing. Section 2 describes the fault models defined for the case study. Section 3 describes the integration of mutation probes into ADCS IF SW.

# 1. Overview of the case study

In the case of LXS, data-driven mutation testing is applied to assess the quality of the test cases that exercise the ADCS software interface of the ESAIL system (hereafter, ADCS\_IF\_SW). In ESAIL, the ADCS\_IF\_SW is used to manage and collect data from hardware devices (e.g., sensors). Detailed specifications for the ADCS interface appear in the document *ESAIL-LXS-ICD-P-0184 ADCS IF SW External ICD*.

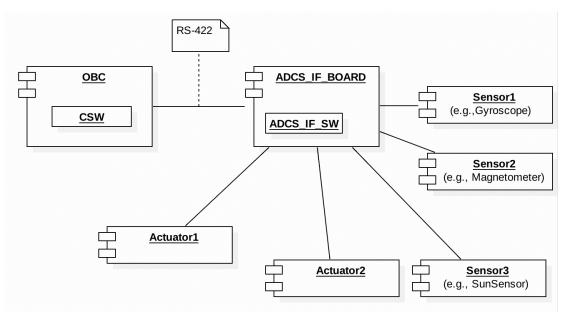


Figure 1: OBC-ADCS integration in ESAIL

Figure 1 provides an overview of the integration between ESAIL OBC and the ADCS board. ESAIL CSW (central software) runs on an onboard controller (OBC) with a Leon 3 microprocessor. The OBC is connected to ADCS interface boards (ADCS\_IF\_BOARD) through RS-422. The ADCS\_IF\_BOARD runs its own controller (ADCS\_IF\_SW). Each board processes data received from sensors and controls actuators. The ADCS\_IF\_SW is the

target of data-driven mutation testing and is the software layer where mutation probes are installed.

The ADCS\_IF\_SW implements functions used by the OBC to send data to devices (i.e., set their configuration) and functions that send devices data to the OBC. Since the functions that send data to the devices are typically tested with hardware in the loop, in the context of FAQAS, we will apply data-driven mutation testing only to verify the functions used by the ADCS to send data to the OBC.

The function of the ADCS\_IF\_SW that manages the communication between the ADCS and the OBC, i.e., *ObcRecvBlockCb*. The function is implemented in file *AdcsIf.c*.

The implementation of function *ObcRecvBlockCb* is shown in Section 1.1. It mainly consists of a switch command (line 138) that generates a response for the OBC after invoking a *data generation method* selected according to the request received on the data link. For example, Line 146 invokes method *GetIfStatus*, which prepares a response packet containing the information about the ADCS status.

Each data generation method receive as input an object of type std::vector (i.e., the object newBlock) that will be used to store the data to be sent to the OBC. The vector newBlock acts as a buffer; it contains elements of type UInt8, the length of the vector matches the size of the response message indicated in ESAIL-LXS-ICD-P-0184 (one element per byte). Table 1 reports, for each feature targeted by data-driven mutation testing, the page in ESAIL-LXS-ICD-P-0184 that describes the data format, the ADCS\_IF\_SW function that fill the content of message, and the size of the response message (i.e., the length of std::vector).

ADCS Feature	Page	ADCS_IF_SW function	Message size (bytes)
ADCS IF Status	19	GetIfStatus	6
ADCS IF HK	22	GetIfHk	37
GYTM - Gyroscope TM	34	GetGyroTm	21
MMTX - Magnetometer TX	41	GetMgtmTm	2
Sun Sensor TM	42	GetSsTm	48
SSTP - Sun Sensor Temperature	45	GetSsTemp	12
Reaction Wheel TX	50	GetRwTm	2
SpaceCroft UV	60	CatlfCaUlr	10

GetMgtqTm

39

Table 2: Features targeted by data-driven mutation testing and message size

57

Each invocation of a *data generation method* generates a response (i.e., the vector *newBlock*) that may either contain the desired result or an error code. The response generated in the first case is referred to as *nominal response message*, the response generated in the second case is an *error response message*. The reponse message is sent to the OBC through the invocation of function SendResponse (Lines 298 and 312). When an error code is generated, the data generation method returns *CR\_Failure*. The response code is read by function *ObcRecvBlockCb* to determine if it is necessary to trim the buffer before sending back to OBC; this behaviour is handled by the parameter *true* passed to SendResponse (Line 312).

#### 1.1 Function ObcRecvBlockCb

Magnetorquer Set PWM RSP

```
89 // -- OPENING ELEMENT--AdcsIf:: ObcRecvBlockCb--
 90 /// Function that is called when a block of data is received from the data link layer.
 91 /// @param block The received data block.
 92 void AdcsIf::ObcRecvBlockCb(const std::vector<Smp::UInt8>& block)
93 {
         // MARKER: OPERATION BODY: START
 95
         Trace(4, "Received command: 0x%s", OhbCommon::ByteUtils::BinToHex(block));
96
 97
        if(!CheckRxEnabled())
 98
        {
99
             return;
        }
100
101
102
        std::vector<Smp::UInt8> newBlock(block);
103
        Smp::UInt8 cmdId = block[0];
Smp::UInt8 subcmdId = block[1];
104
105
106
        if(forcedResponse && (forcedResponseCmdId == cmdId || forcedResponseCmdId < 0)
    && (forcedResponseSubcmdId == subcmdId || forcedResponseSubcmdId < 0))</pre>
107
108
109
110
             newBlock.resize(2);
111
             if(forcedResponseErrorCode >= 0)
112
113
             {
114
                  // Generate forced error response
115
                  Trace(2, "Generating forced response message with error code 0x%02X", forcedResponseErrorCode);
116
                  newBlock.push_back(forcedResponseErrorCode);
                  SendResponse(newBlock, true);
117
118
119
             else
120
                  // Generate forced valid response
121
                  Trace(2, "Generating forced response message with data 0x%s", forcedResponseData.c_str()); for(unsigned int i = 0; i < forcedResponseData.length(); i += 2)
122
123
124
                      std::string byteString = forcedResponseData.substr(i, 2);
125
126
                      newBlock.push_back(strtol(byteString.c_str(), NULL, 16));
127
128
                 SendResponse(newBlock, false);
129
             }
130
             return;
131
132
        bool processed = true:
133
134
        CommandResult cr = CR_Failure;
135
        if(Status->ADRD || ((cmdId == 1) && (subcmdId < 3)))</pre>
136
137
138
             switch(cmdId)
139
             {
140
             case 1:
141
                 switch(subcmdId)
142
143
144
                 case 0:
145
                 {
146
                      cr = GetIfStatus(newBlock);
147
148
                 break;
149
                 case 1:
150
                 {
                     cr = GetIfHk(newBlock);
151
153
                 break;
154
                 case 2:
155
156
                      cr = SetIfPower(newBlock);
                 }
157
                 break;
                 case 3:
159
160
                      cr = SetUnitStatus(newBlock);
162
                 1
163
                 break;
164
                 case 4:
165
                 {
166
                      cr = SetConfiguration(newBlock);
167
168
                 break:
169
                 case 5:
170
171
                      cr = LclRetrigger(newBlock);
172
173
                 break;
```

```
174
                default:
175
                    Log(Smp::Services::LMK_Warning, "Sub-command %u not implemented", subcmdId);
176
                    processed = false;
177
178
179
            break;
180
            case 4:
181
182
                switch(subcmdId)
183
                {
                case 0:
184
185
                {
                    cr = GetGyroTm(newBlock);
186
                }
187
                break;
188
189
                default:
190
                    Log(Smp::Services::LMK_Warning, "Sub-command %u not implemented", subcmdId);
191
                    processed = false;
192
193
194
            break;
195
            case 5:
196
                switch(subcmdId)
197
198
                {
199
                case 0:
200
                {
201
                    cr = GetMgtmTm(newBlock);
202
203
                break;
204
                default:
205
                    Log(Smp::Services::LMK_Warning, "Sub-command %u not implemented", subcmdId);
206
                    processed = false;
207
208
            }
209
            break;
210
            case 6:
211
            {
                switch(subcmdId)
212
213
                {
                case 0:
214
215
                {
                    cr = GetSsTm(newBlock);
216
217
218
                break;
219
                case 1:
                {
221
                    cr = GetSsTemp(newBlock);
222
223
                break;
```

```
224
                default:
225
                    Log(Smp::Services::LMK_Warning, "Sub-command %u not implemented", subcmdId);
226
                    processed = false;
227
228
            }
229
            break;
230
            case 7:
231
            {
                switch(subcmdId)
232
233
                {
234
                case 0:
235
                {
236
                    cr = GetRwTm(newBlock);
                }
237
238
                break;
239
                default:
240
                    Log(Smp::Services::LMK_Warning, "Sub-command %u not implemented", subcmdId);
241
                    processed = false;
242
243
            }
244
            break;
245
            case 8:
            {
246
247
                switch(subcmdId)
248
                {
249
                case 0:
250
                {
251
                    cr = SetMgtqPwm(newBlock);
252
                    if(cr == CR_Success)
253
254
                        if(newBlock[2] == 0x55)
255
256
                             // Bypass Magnetometer response
257
                            newBlock.resize(2);
258
                             cr = GetMgtqTm(newBlock);
                        }
259
260
                        else
261
                        {
262
                            cr = BuildMgtmDataRequestCmd(newBlock);
263
                            cr = GetMgtmTm(newBlock);
264
265
                    }
266
                }
267
                break;
268
                default:
269
                    Log(Smp::Services::LMK_Warning, "Sub-command %u not implemented", subcmdId);
270
                    processed = false;
271
                }
            }
272
```

```
274
            case 9:
275
276
                switch(subcmdId)
277
                {
278
                case 0:
279
                {
280
                    cr = GetIfScHk(newBlock);
                }
281
282
                break;
283
284
                   Log(Smp::Services::LMK_Warning, "Sub-command %u not implemented", subcmdId);
285
                    processed = false;
286
                }
287
            }
288
            break;
289
            default:
                Log(Smp::Services::LMK_Warning, "Command %u not implemented", cmdId);
290
291
                processed = false;
292
293
        }
294
        switch(cr)
295
        {
296
        case CR_Success:
297
        {
298
            SendResponse(newBlock, false);
299
300
        break;
301
        case CR_InProgress:
302
303
            Trace(5, "Operation in progress");
304
305
        break;
        case CR_Failure:
306
307
308
            if(!processed)
309
310
                newBlock.push_back(0x56);
311
312
            SendResponse(newBlock, true);
313
314
        break;
315
        default:
            Log(Smp::Services::LMK_Error, "Command result %u not supported", cr);
316
317
        // MARKER: OPERATION BODY: END
318
319 }
320 // --CLOSING ELEMENT--AdcsIf::ObcRecvBlockCb--
```

#### 2. Fault Model

In the case of ADCS\_IF\_SW we have defined a total of 18 fault models, two for each feature listed in Table 1. For each feature, one fault model captures the fault that might affect a nominal response message, one fault model captures the faults that might affect an error response message.

In the following sections we describe the fault models by providing for each byte of the response message (column *Byte*), the relevant bits (column *Bit*), a description of the information that is supposed to be transmitted by the byte (column *Description*), the type of data written on the byte (column *Type*), the fault classes that might affect the byte (column *Fault class*). We do not report the span of the item since it can be deducted from the table; indeed, descriptions that span over multiple rows correspond to data types that, to be loaded, require the readin of multiple data items. Concerning data types, the type DOUBLE is used for data items that internally to ESAIL are represented using the type Smp::Float64. On the channel, Smp:Float64 is transmitted as <*PTC*=3, *PCF*=6> Unsigned Integer 10bits, which in the code is represented with Smp::Int16.

For each fault class, we indicate the value of the parameters required to configure the corresponding mutation operator (see Table 2.1 of D2). We use the keyword @MIB to indicate that the parameter value should be derived from the MIB database for ESAIL, more precisely from the file OCP.dat. In the database, the min and max range value for the nominal cases are reported. For example, Figure 2 shows a portion of the OBC.dat from which we can determine that MIN and the MAX values for AIFN031U are 3 and 3.6, respectively. The delta (i.e., parameter D) is coincides with the lowest positive number that can be represented with the number of decimals appearing in the rage (e.g., 0.1 for AIFN031U and 0.01for AIFN031U). for For some of the data items in the table we report also the corresponding identifier in OBC.dat. Missing identifiers will be reported in the coming months while refining the approach; indeed, decisions on the data items to be addressed by the approach may change after the first preliminary tests.

AIFN030U	1	Н	24	33.53	AAA_OL80	1
AIFN031U	1	Н	3	3.6	AAA_OL80	1
AIFN032U	1	Н	3	3.6	AAA OL80	1
		Figure	2: Portion of Ol	BC.dat		

In column Fault class, the label NONE indicates that we are not interested into performing data-driven mutation testing for that specific byte. In general, we do not target with data-driven mutation those data items that do not concern features covered by the test suite. These are typically data items that do not cause a crash of the on board software or data items used only for self-testing of the board.

Columns Byte, Bit, and Description match the columns of corresponding tables in *ESAIL-LXS-ICD-P-0184*.

#### 2.1 ADCS IF Status

Byte	Bit	Description	Type	Fault class

		Dagat Course	DIN	DE(MINI-2:MA
		Reset Source	BIN	BF(MIN=3;MA
		Provides information about last reset.		X=3)
		The bit is cleared after the first read of		BF(MIN=4;MA
		the status		X=4)
		0 = No reset		BF(MIN=5;MA
		1 = Power-on Reset		X=7)
	20	2 = External Reset (released by JTAG		
	20	adapter)		
		3 = Watchdog Reset		
		4 = Brown-out Reset		
		5 = JTAG AVR Reset (logic reset by		
		JTAG)		
		6 = Not used		
		7 = Not used		
		ADCS IF ready		
		This bit is set when ADCS is ready to		
		read/write to units. In the boot of the		
		ADCS IF shall be a time to initialize all		
		modules and units. After initialization of		
		the ADCS IF, modules and units, shall go		
1	3	to a ready state.		
		While ADCS IF is not ready, the		
		available commands are:		
		• ASST		
		ASHK		
		• ASCT		
		OBC communication error		
		This bit is set if a communication error		
	4	between OBC and ADCS IF occurred in		
	4	the last command. The bit is cleared after		
		the first reading of the status		
		0 = No error		
		1 = Communication error		
		Unit communication error		
		This bit is set if a communication error		
		between ADCS IF and ADCS unit		
	75	occurred. The bit is cleared after the first		
		read of the status		
		0 = No error		
		1 = Communication error		
		Unit in error	BIN	BF(MIN=0;MA
		Provides a list of units in error.		X=4)
		0 = No error		
		1 = Unit error		
2	70			
	/	Each bit is assigned to one unit:		
		Bit 0 = Gyroscope unit		
		Bit 1 = Reaction Wheel		
		Bit 2 = Magnetorquer		
		Bit 3 = Magnetometer		

		Bit 4 = Sun Sensor		
3	70	Watchdog Reset Counter Watchdog Reset counter value. Increment in every watchdog reset. Value is stored in non-volatile memory To clear watchdog reset counter, shall be used the ASCF command.	INT	None: ESAIL OBC does not deal with anomalous values of reset counters. Thus we do not expect ESAIL test suite to fail in case of a high reset counter
4	70	Overall Reset Counter Overall reset counter value. Increment in every device reset. Value is stored in non-volatile memory To clear overall reset counter, shall be used the ASCF command.	INT	None: same as above.
	10	Gyroscope enable Enable/Disable status of nominal or redundant bus transceiver.  0 = Disabled both transceivers  1 = Enabled nominal transceiver only  2 = Enabled redundant transceiver only  3 = not existing (reserved for future needs)	BIN	BF(MIN=0;MA X=2) BF(MIN=2;MA X=4) BF(MIN=5;MA X=7)
5	42	Reaction Wheel enable Enabled/Disabled status of bus transceiver. 0 = Disabled transceiver 1 = Enabled transceiver 72 = not existing (reserved for future needs)		
	75	3 axis Magnetorquer enable General Enable/Disable status of the Magnetorquer Driver for all three axis. 0 = Disabled 1 = Enabled  Bit assignement: Bit 0 = Enabled/Disabled Driver Bit 1 = 0 not used (reserved for future needs)		

		Bit $2 = 0$ not used (reserved for future	]	
		needs)		
		Magnetometer enable	BIN	BF(MIN=0;MA
		Enable/Disable status of nominal or		X=1)
		redundant bus transceiver.		BF(MIN=2;MA
	10	0 = Disabled both transceivers		X=7)
	10	1 = Enabled nominal transceiver only		
		2 = Enabled redundant transceiver only		
		3 = not existing (reserved for future		
		needs)		
		Sun Sensor board ADC enable		
		Enabled/Disabled Sun Sensor board		
6		ADC, see Note 3)		
		0 = Disabled		
		1 = Enabled		
	72	Each bit is assigned to one ADC:		
		Bit $0 = \text{Enabled/Disabled ADC2}$		
		Bit 1 = Enabled/Disabled ADC3		
		Bit 2 = Enabled/Disabled ADC4		
		Bit 3 = Enabled/Disabled ADC5		
		Bit 4 = Enabled/Disabled ADC6		
		Bit 5 = Enabled/Disabled ADC7		

Byte	Bit	Description	Type	Fault class
			HEX	IV(VALUE=0x51)
				IV(VALUE=0x52)
				IV(VALUE=0x53)
		Error type		IV(VALUE=0x54)
		The possible errors are		IV(VALUE=0x55)
1	70	described in the Error!		IV(VALUE=0x57)
		Reference source not		IV(VALUE=0x58)
		found.		IV(VALUE=0x59)
				IV(VALUE=0x5A)
				IV(VALUE=0x5B)
				IV(VALUE=0x5C)

### 2.2 ASHK - ADCS IF HK

Byte	Bit	Description	Type	Fault class
1	70	VCC1N		NONE
2	70	OBC Nominal transceiver circuit voltage		
3	70	VCC1R		NONE
4	70	OBC Redundant transceiver circuit voltage		NONE
5	70	VCC2		NONE
6	70	Gyroscope transceiver/UART circuit voltage		NONE
7	70	VCC3		NONE
8	70	Magnetometer transceiver/UART circuit voltage		NONE
9	70	VCC4		NONE
10	70	Reaction Wheel transceiver/UART circuit voltage		NONE
11	70	VCCa		NONE
12	70	Internal power supply (5.5V), measured with ADC0		NONE
13	70	VCCb		
14	70	Internal power supply (5.5V), measured with ADC1	DOUBLE	VAT(T=@MIB;D=@MIB) ID: AIFN031U
15	70	VBUS Unit input bus voltage	DOUBLE	VAT(T=@MIB;D=@MIB)
16	70			
17	70	VCC5		NONE
18	70	Supply voltage for ADC2, ADC3, ADC4 and VCCB1. Sun-sensor PCB		NONE
19	70	VCC6		NONE
20	70	Supply voltage for ADC5, ADC6, ADC7 and VCCB2. Sun-sensor PCB		NONE
21	70	VCC5 IN		NONE
22	70	LDO input voltage for ADC2, ADC3, ADC4 and VCCB1. Sun-sensor PCB		NONE
23	70	VCC6_IN		NONE
24	70	LDO input voltage for ADC5, ADC6, ADC7 and VCCB2. Sun-sensor PCB		NONE
25	70	VCC_SW1 SSB internal switched power supply, measured by ADC3		VAT(T=@MIB;D=@MIB)
26	70	Remark: the voltage VCC_SW is measured 2 times with two different ADC. This allows to compare the results and conclude for a drift in the ADC's.		

		TYGG GYYYA		NONE
27	7.0	VCC_SW2		NONE
27	70	SSB internal switched power supply,		
		measured by ADC6		NONE
		Remark: the voltage VCC_SW is measured 2		NONE
28	70	times with two different ADC. This allows to		
	28 /0	compare the results and conclude for a drift in		
		the ADC's.		
29	70	T_PCB_TEMP1	DOUBLE	VOR(MIN=@MIB;
	70	Main Board PCB Temperature, sensor 1		MAX=@MIB;D=@MIB)
		Temperature of VCC DC/DC regulator.		
30	70	Remark: 1/2 is measured on the same place,		
30	70	it's to compare the values to discover a		
		measurement failure		
31	70	T_PCB_TEMP2		
		Main Board PCB Temperature, sensor 2	DOUBLE	VOR(MIN=@MIB;
		Temperature of VCC DC/DC regulator.		MAX=@MIB;D=@MIB)
32	70	Remark: 1/2 is measured on the same place,		
		it's to compare the values to discover a		
		measurement failure		
33	70	T_PCB_TEMP3a	DOUBLE	VOR(MIN=@MIB;
33	70	Sun Sensor Board PCB Temperature, sensor		MAX=@MIB;D=@MIB)
		3a.	DOUBLE	
		Temperature of VCC5 LDO regulator.		
34	70	Remark: 3a/b is measured on the same place,		
		it's to compare the values to discover a		
		measurement failure		
35	70	T_PCB_TEMP3b	DOUBLE	VOR(MIN=@MIB;
33	70	Sun Sensor Board PCB Temperature, sensor		MAX=@MIB;D=@MIB)
		3b.	DOUBLE	
		Temperature of VCC5 LDO regulator.		
36	70	Remark: 3a/b is measured on the same place,		
		it's to compare the values to discover a		
		measurement failure		
		T_PCB_TEMP4	DOUBLE	VOR(MIN=@MIB;
37	70	Sun Sensor Board PCB Temperature, sensor		MAX=@MIB;D=@MIB)
		4. Temperature of VCC6 LDO regulator.		
-		<del>-</del>		•

Byte	Bit	Description	Type	Fault class
			HEX	IV(VALUE=0x51)
				IV(VALUE=0x52)
				IV(VALUE=0x53)
		Error type		IV(VALUE=0x54)
		The possible errors are		IV(VALUE=0x55)
1	70	described in the Error!		IV(VALUE=0x57)
		Reference source not		IV(VALUE=0x58)
		found.		IV(VALUE=0x59)
				IV(VALUE=0x5A)
				IV(VALUE=0x5B)
				IV(VALUE=0x5C)

# 2.3 GYTM - Gyroscope TM

Byte	Bit	Description	Type	Fault class
		Unit identifier	INT	BF(MIN=0,MAX=0)
		Identification of the unit that addresses the		
1	70	message		
		0 = Nominal		
		1 = Redundant		
		Gyroscope Telemetry	HEX	NONE: the type of
		All telemetry data from Gyroscope.		data transmitted appear
		Message is the same sent from Gyroscope unit		to bee too much
212	70	without adding/removing data		complicate to be
212	70			mutated in such a way
				of triggering a test
				failure. Could be
				trageted in the future.

Byte	Bit	Description	
1	70	Error type The possible errors are described in the Error! Reference source not found.	IV(VALUE=0x51) IV(VALUE=0x52) IV(VALUE=0x53) IV(VALUE=0x54) IV(VALUE=0x56)

# 2.4 MMTX - Magnetometer TX

Byte	Bit	Description	Type	Fault class
		Unit identifier	BIN	BF(MIN=0;MAX=0)
		Identification of the unit that		
1	70	addresses the message		
		0 = Nominal		
		1 = Redundant		
		Self Test Result	BIN	BF(MIN=0;MAX=0;STA
2	70	0 = no error		TE=0)
	70	1 = error detected during TX self test		
		2-255 = reserved		

Byte	Bit	Description	Type	Fault class
			HEX	IV(VALUE=0x51)
		Error type		IV(VALUE=0x52)
1	70	The possible errors are described in		IV(VALUE=0x53)
1	70	the Error! Reference source not		IV(VALUE=0x54)
		found.		IV(VALUE=0x56)
				IV(VALUE=0x5D)

### 2.5 Sun Sensor TM

Byte	Bit	Description	Type	Fault class
1	70	Photodiode Q1	DOUBLE	VAT(T=@MIB;D=@MIB)
2	70	current ADC #3		
3	70	Photodiode Q2	DOUBLE	VAT(T=@MIB;D=@MIB)
4	70	current ADC #3		
5	70	Photodiode Q3	DOUBLE	VAT(T=@MIB;D=@MIB)
6	70	current ADC #3		
7	70	Photodiode Q4	DOUBLE	VAT(T=@MIB;D=@MIB)
8	70	current ADC #3		
9	70	Photodiode Q1	DOUBLE	VAT(T=@MIB;D=@MIB)
10	70	current ADC #2		
11	70	Photodiode Q2	DOUBLE	VAT(T=@MIB;D=@MIB)
12	70	current ADC #2		
13	70	Photodiode Q3	DOUBLE	VAT(T=@MIB;D=@MIB)
14	70	current ADC #2		
15	70	Photodiode Q4	DOUBLE	VAT(T=@MIB;D=@MIB)
16	70	current ADC #2		
17	70	Photodiode Q1	DOUBLE	VAT(T=@MIB;D=@MIB)
18	70	current ADC #6		
19	70	Photodiode Q2	DOUBLE	VAT(T=@MIB;D=@MIB)
20	70	current ADC #6		
21	70	Photodiode Q3	DOUBLE	VAT(T=@MIB;D=@MIB)
22	70	current ADC #6		
23	70	Photodiode Q4	DOUBLE	VAT(T=@MIB;D=@MIB)
24	70	current ADC #6		
25	70	Photodiode Q1	DOUBLE	VAT(T=@MIB;D=@MIB)
26	70	current ADC #5		
27	70	Photodiode Q2	DOUBLE	VAT(T=@MIB;D=@MIB)
28	70	current ADC #5		

29	70	Dhatadiada O2	DOUBLE	VAT(T=@MIB;D=@MIB)
		Photodiode Q3 current ADC #5		
30	70		DOLIDI E	VATER OLDER DOLDER
31	70	Photodiode Q4	DOUBLE	VAT(T=@MIB;D=@MIB)
32	70	current ADC #5		
33	70	Photodiode Q1	DOUBLE	VAT(T=@MIB;D=@MIB)
34	70	current ADC #4		
35	70	Photodiode Q2 current ADC #4	DOUBLE	VAT(T=@MIB;D=@MIB)
36	70			
37	70	Photodiode Q3 current ADC #4	DOUBLE	VAT(T=@MIB;D=@MIB)
38	70			
39	70	Photodiode Q4	DOUBLE	VAT(T=@MIB;D=@MIB)
40	70	current ADC #4		
41	70	Photodiode Q1	DOUBLE	VAT(T=@MIB;D=@MIB)
42	70	current ADC #7		
43	70	Photodiode Q2	DOUBLE	VAT(T=@MIB;D=@MIB)
44	70	current ADC #7		
45	70	Photodiode Q3	DOUBLE	VAT(T=@MIB;D=@MIB)
46	70	current ADC #7		
47	70	Photodiode Q4	DOUBLE	VAT(T=@MIB;D=@MIB)
48	70	current ADC #7		

Byte	Bit	Description	Type	Fault class
		Error type	HEX	IV(VALUE=0x51)
		The possible errors are		IV(VALUE=0x54)
1	70	described in the Error!		IV(VALUE=0x56)
		Reference source not		
		found.		

# **2.6 SSTP - Sun Sensor Temperature**

Byte	Bit	Description	Type	Fault class
1	70	Temperature reading from	DOUBLE	VOR(MIN=@MIB;MAX=
2	70	ADC #3		@MIB;D=@MIB)
3	70	Temperature reading from	DOUBLE	VOR(MIN=@MIB;MAX= @MIB;D=@MIB)
4	70	ADC #2		
5	70	Temperature reading from	DOUBLE	VOR(MIN=@MIB;MAX= @MIB;D=@MIB)
6	70	ADC #6		
7	70	Temperature reading from	DOUBLE	VOR(MIN=@MIB;MAX= @MIB;D=@MIB)
8	70	ADC #5		
9	70	Temperature reading from	DOUBLE	VOR(MIN=@MIB;MAX= @MIB;D=@MIB)
10	70	ADC #4		
11	70	Temperature reading from	DOUBLE	VOR(MIN=@MIB;MAX= @MIB;D=@MIB)
12	70	ADC #7		

Byte	Bit	Description	Type	Fault class
		Error type The possible errors are		IV(VALUE=0x51) IV(VALUE=0x54)
1	70	described in the Error!		IV(VALUE=0x54) IV(VALUE=0x56)
		Reference source not		
		found.		

### 2.7 Reaction Wheel TX

Byte	Bit	Description	Type	Fault class
		Unit identifier	BIN	BF(MIN=0,MAX=0)
		Identification of the unit that		
1	70	addresses the message		
		0 = Nominal		
		1 = Redundant		
		Self Test Result	BIN	BF(MIN=0,MAX=0)
		0 = no error		
2	70	1 = error detected during TX		
		self test		
		2-255 = reserved		

Byte	Bit	Description	Type	Fault class
			Hex	IV(VALUE=0x51)
		Error type		IV(VALUE=0x52)
1	70	The possible errors are described in		IV(VALUE=0x53)
1	70	the Error! Reference source not		IV(VALUE=0x54)
		found.		IV(VALUE=0x56)
				IV(VALUE=0x5D)

# 2.8 SpaceCraft HK

Byte	Bit	Description	Type	Fault class
1	70	TMTC_SW1 Identifies the switching position of the TMTC switch 1: the voltage is ~1.1V for position A and 2.2V for position B.	DOUBLE	VAT(T=3.3;D=0) VBT(T=0;D=0) ID: AIFN086X
2	70	0V or 3.3V will indicate a short or an interruption.		
3	70	TMTC_SW2 Identifies the switching position of the	DOUBLE	VOR(MIN=@MIB;MA X=@MIB;D=@MIB)
4	70	TMTC switch 2: the voltage is ~1.1V for position A and 2.2V for position B. 0V or 3.3V will indicate a short or an interruption.		ID: AIFN087X
5	70	SC_TEMP1 Temperature SC-TEMP1 of a sensor in	DOUBLE	VOR(MIN=@MIB;MA X=@MIB;D=@MIB)
6	70	the S/C structure		
7	70	SC_TEMP2 Temperature SC-TEMP2 of a sensor in	DOUBLE	VOR(MIN=@MIB;MA X=@MIB;D=@MIB)
8	70	the S/C structure		
9	70	SC_TEMP3 Temperature SC-TEMP3 of a sensor in	DOUBLE	VOR(MIN=@MIB;MA X=@MIB;D=@MIB)
10	70	the S/C structure		
11	70	SC_TEMP4 Temperature SC-TEMP4 of a sensor in	DOUBLE	VOR(MIN=@MIB;MA X=@MIB;D=@MIB)
12	70	the S/C structure		
13	70	SC_TEMP5 Temperature SC-TEMP5 of a sensor in	DOUBLE	VOR(MIN=@MIB;MA X=@MIB;D=@MIB)
14	70	the S/C structure		
15	70	SC_TEMP6 Temperature SC-TEMP6 of a sensor in	DOUBLE	VOR(MIN=@MIB;MA X=@MIB;D=@MIB)
16	70	the S/C structure		
17	70	SC_TEMP7 Temperature SC-TEMP7 of a sensor in	DOUBLE	VOR(MIN=@MIB;MA X=@MIB;D=@MIB)
18	70	the S/C structure		

Byte	Bit	Description		
1	70	Error type The possible errors are described in the Error! Reference source not found.	HEX	IV(VALUE=0x56)

# 2.9 Magnetorquer Set PWM RSP

Byte	Bit	Description	Type	Fault class
1	70	Unit identifier Magnetometer Identification of the Magnetometer unit that addresses the message 0 = Nominal 1 = Redundant	BIN	BF(MIN=0;MAX=0)
2	70	Magnetometer Data request reply Byte1 Sync(LSB) (Note 1)		NONE: Not to address with the approach because the effect of a mutation is not predictable (the trasferred data is complex, e.g., signal).
3	70	Magnetometer Data request reply Byte2 Sync(MSB) (Note 1)		NONE: same as above.
4	70	Magnetometer Data request reply Byte3 RAdr (Note 1)		NONE: same as above.
5	70	Magnetometer Data request reply Byte4 Sadr (Note 1)		NONE: same as above.
6	70	Magnetometer Data request reply Byte5 ReplyMsg (Note 1)		NONE: same as above.
7	70	Magnetometer Data request reply Byte6 Bx Low (Note 1)		NONE: same as above.
8	70	Magnetometer Data request reply Byte7 Bx Middle		NONE: same as above.
9	70	Magnetometer Data request reply Byte8 CS error + Average + pos Clip X + neg Clip X + BX High (Note 1)		NONE: same as above.
10	70	Magnetometer Data request reply Byte9 By Low (Note 1)		NONE: same as above.
11	70	Magnetometer Data request reply Byte10 By Middle (Note 1)		NONE: same as above.
12	70	Magnetometer Data request reply Byte11 spare + pos Clip Y + neg Clip Y + BY High (Note 1)		NONE: same as above.
13	70	Magnetometer Data request reply Byte12 Bz Low (Note 1)		NONE: same as above.
14	70	Magnetometer Data request reply Byte13 Bz Middle (Note 1)		NONE: same as above.
15	70	Magnetometer Data request reply Byte14 spare + pos Clip Z + neg Clip Z + BZ High (Note 1)		NONE: same as above.
16	70	Magnetometer Data request reply Byte15 CS (Note 1)		NONE: same as above.
17	70	Magnetorquer nX Current - on Current MTXA_N when powered	DOUBLE	VOR(MIN=@MIB,MA X= @MIB,D=@MIB)

18	70			
19	70	Magnetorquer nX Current - off	DOUBLE	VOR(MIN=0.14,MAX= 0.21,D=0.01)
20	70	Current MTXA_N when unpowered		ID: AIFR074I
21	70	Magnetorquer pX Current - on	DOUBLE	VOR(MIN=@MIB,MA X= @MIB,D=@MIB)
22	70	Current MTXA_P when powered		
23	70	Magnetorquer pX Current - off	DOUBLE	VOR(MIN=@MIB,MA X= @MIB,D=@MIB)
24	70	Current MTXA_P when unpowered		ID: AIFR075I
25	70	Magnetorquer nY Current - on Current MTYA N when powered	DOUBLE	VOR(MIN=@MIB,MA X= @MIB,D=@MIB)
26	70	Current WIT TA_IV when powered		ID: AIFR076I
27	70	Magnetorquer nY Current - off	DOUBLE	VOR(MIN=@MIB,MA X= @MIB,D=@MIB)
28	70	Current MTYA_N when unpowered		ID:AIFR077I
29	70	Magnetorquer pY Current - on		VOR(MIN=@MIB,MA X= @MIB,D=@MIB)
30	70	Current MTYA_P when powered	DOUBLE	ID: AIFR078I
31	70	Magnetorquer pY Current - off	DOUBLE	VOR(MIN=@MIB,MA X= @MIB,D=@MIB)
32	70	Current MTYA_P when unpowered		ID: AIFR079I
33	70	Magnetorquer nZ Current - on	DOUBLE	VOR(MIN=@MIB,MA X= @MIB,D=@MIB)
34	70	Current MTZA_N when powered		ID:AIFR080I
35	70	Magnetorquer nZ Current - off	DOUBLE	VOR(MIN=@MIB,MA X= @MIB,D=@MIB)
36	70	Current MTZA_N when unpowered		
37	70	Magnetorquer pZ Current - on	DOUBLE	VOR(MIN=@MIB,MA X= @MIB,D=@MIB)
38	70	Current MTZA_P when powered		
39	70	Magnetorquer pZ Current - off Current MTZA_P when unpowered	DOUBLE	VOR(MIN=@MIB,MA X= @MIB,D=@MIB)

Byte	Bit	Description	Type	Fault class
1	70	Error type The possible errors are described in the Error! Reference source not found.	Hex	IV(VALUE=0x51) IV(VALUE=0x52) IV(VALUE=0x53) IV(VALUE=0x54) IV(VALUE=0x56) IV(VALUE=0x5D)
1	70	The possible errors are described in the Error! Reference source not		IV(VALUE=0x54 IV(VALUE=0x56

#### 3. Mutation Probes

Mutation probes are manually integrated into the source code of function *ObcRecvBlockCb*. Figure 3 shows an example of how we integrate mutation probes. All the probes are integrated following the same pattern; more precisely, for each data generation function we manually insert two invocations to the FAQAS mutation probe API, one to perform mutation of the nominal response message (Line 154, in Figure 3) the other one to mutate an error response message (Line 149). The choice of the data model to pass to the FAQAS mutation probe API is based on the value of *cr*, the variable that captures the return status of the specific data generation function invoked (function *GetIfHk* in Figure 3).

The function \_FAQAS\_mutate takes as input the fault model to be used to drive the mutation. Fault models are automatically generated from template files matching to the tables reported in Section 2.

```
144:
          case 0:
145:
146:
            cr = GetIfStatus(newBlock);
            if (cr == CR Failure){
147:
                 FaultModel *dm = FAQAS GetIfStatus FM Error ()
148:
                 _FAQAS_mutate( newBlock, dm );
149:
150:
                 FAQAS delete DM(dm)
             } else {
151:
                  FaultModel *dm = FAQAS GetIfStatus FM ()
152:
                   FAQAS mutate( newBlock, dm);
153:
154:
                 FAQAS delete DM(dm)
155:
             }
156:
          }
```

Figure 3: Mutation probe for GetIfStatus