Flexible Runtime Security Enforcement with Tagged C

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Abstract. Today's computing infrastructure is built atop layers of legacy C code, often insecure, poorly understood, and/or difficult to maintain. These foundations may be shored up with dynamic security enforcement, which spares legacy code owners from having to modify their code. Tagged C is a C variant with a built-in *tag-based reference monitor* for use in expressing a variety of dynamic security policies and enforcing them with compiler and hardware support.

Tagged C is comprehensive in the policies that it can support. In this paper we will discuss *memory safety*, *compartmentalization*, and *secure information flow* (SIF) policies. It is flexible in covering the tradeoff between conservative policies that may halt too many programs and more permissive ones. And as a source language, it should be more accessible to C programmers than existing assembly-level tag-based reference monitors.

1 Introduction

Many essential technologies rely on new and old C code. Operating systems (Linux, Windows, OSX, BSD), databases (Oracle, sqlite3), the internet & web (Apache, NGNIX, NetBSD, Cisco IOS), the Internet of Things (IoT), and the embedded devices that run our homes and hospitals are built in and on C [11]. C is not a relic; more than a third of professional programmers report active developing in C today [12]. The safety of public and private systems we depend on every day in turn depends on the security of their underlying C codebases. Insecurity might take the form of undefined behavior such as memory errors, of logic errors such as sql injection or other input-sanitization flaws, or of larger-scale architectural flaws that over-provision components of the system with privilege.

Although static analyses can detect and mitigate many C insecurities, the last line of defense against undetected or unfixable vulnerabilities is dynamic enforcement at runtime. Ideally an engineer can tune enforcement to the security needs of the system, rather than apply one-size-fits-all conservative restrictions. To allows developers to define flexible security policies in terms of a familiar source language, we introduce Tagged C: a general-purpose dynamic tag-based enforcement language. Applications of Tagged C include giving precise definition to undefined behaviors (UBs) involving memory, specifying detailed information flow policies, and enforcing arbitrary mandatory access control tables.

Importantly, a security policy can be modified without recompilation unless it is optimized as described in section 5.

The novelty of Tagged C is that it is a *general-purpose* scheme for specifying security policies at the source level, using a tag-based reference monitor. This style of monitor associates a metadata tag with the data in the underlying system, and throughout execution it updates these tags according to a set a predefined rules, or halts if the program would violate a rule. This is the underlying concept of PIPE, an ISA extension that implements a reference monitor in hardware, as well as similar systems such as ARM MTE and [that thing from Binghamton]. While our scheme is general and could be implemented in software, we are motivated by PIPE and aim for compatibility with it as a likely hardware target.

Tagged C consists of an underlying semantics that establishes the baseline concrete behavior of programs with no policies, and a set of *control points* at which the semantics consult a user defined set of *tag rules*. For convenience we build our underlying semantics on the CompCert C semantics, which are formalized as part of the CompCert verified compiler [8]. We provide a reference interpreter also based on that of CompCert, for use executing prototype policies.

Contributions We offer the following contributions:

- A full formal semantic definition for Tagged C, formalized in Coq
- The design of a comprehensive set of *control points* at which the language interfaces with the policy
- A Tagged C interpreter, implemented in Coq and extracted to Ocaml
- Tagged C policies implementing (1) compartmentalization, (2) a realistic, permissive memory model from the literature (PVI), and (3) Secure Information Flow (SIF)

In the next section, we give a high-level introduction of metadata-tagging: how it works, and how its use can improve security. Then in **??**, we briefly discuss the language as a whole, before moving into policies in section 4. Finally, in **??** we discuss the degree to which the design meets our goals of flexibility and applicability to realistic security concerns.

2 What is Metadata Tagging?

Consider a very simple security requirement: "do not leak the password." For simplicity, we will suppose that `pwd` is an integer in this case, and consider a number of ways that it might be leaked (fig. 1). In example (a), we store it in a local variable, then pass it to `printf`. In (b), we perform some arithmetic on it before printing it, but an observer could easily determine the original value. In (c), we store it to a volatile variable representing an mmapped region. And in (d), we loop over it, and store the loop counter in the same mmapped variable, indirectly revealing the value.

To prevent example (a), we need to keep track of the value of the input as it moves from its initial temporary variable to the stack and then is passed, and we need to check the origins of any value we pass to `printf`. Example (b) further requires that we track the provenance of the value as we perform arithmetic on it.

Example (c) reveals that we need to separately track some information about memory locations in addition to the values that they hold, and example (d) asks us to track how the overall state has been influenced by `pwd`.

```
void f(int pwd) {                    volatile int mm;
  int x = pwd;
  printf("%d", x);                   void g(int pwd) {
}                                      if (pwd > 128) {
                                         mm = i;
                                       }
              (a)                    }
```
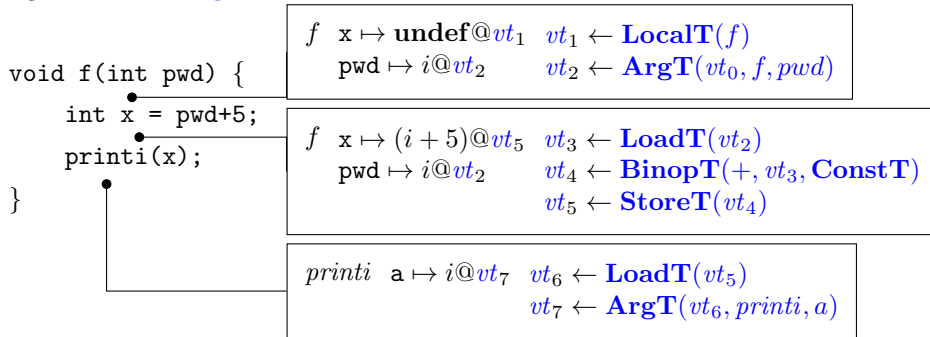
$$(b)$$

Fig. 1: Examples of leaking `pwd`

Lets examine the program state at various points as we execute example 1a. Let the value of pwd be represented by the variable i. We consider this value to be high-security, and wish to track it as it moves through the system, distinguishing it from other, low-security values. These security levels, which we will write Hand L, form our set of *tags* that represent important metadata about a value in the program. On entry to `f`, we assign tags to both the parameter `pwd` and the local variable `x`, based on the **ArgT** and **LocalT** tag rules, respectively. We will call these vt_1 and vt_2, as they are tags on values. **LocalT** takes as its parameter the identity of the function being entered, and **ArgT** takes the tag vt_0 on the value being passed into `f` as well as the identities of both the function and the argument. Next, we load i from `pwd` and store it in `x`, and we need to consult tag rules, **LoadT** and **StoreT**. Finally, when we make the call to `printi`, we once again consult **ArgT**.

```
void f(int pwd) {
```
$$
\begin{array}{lll}
f & \texttt{x} \mapsto \mathbf{undef}@vt_1 & vt_1 \leftarrow \mathbf{LocalT}(f) \\
  & \texttt{pwd} \mapsto i@vt_2 & vt_2 \leftarrow \mathbf{ArgT}(vt_0, f, pwd)
\end{array}
$$
```
    int x = pwd+5;
```
$$
\begin{array}{lll}
f & \texttt{x} \mapsto (i+5)@vt_5 & vt_3 \leftarrow \mathbf{LoadT}(vt_2) \\
  & \texttt{pwd} \mapsto i@vt_2 & vt_4 \leftarrow \mathbf{BinopT}(+, vt_3, \mathbf{ConstT}) \\
  & & vt_5 \leftarrow \mathbf{StoreT}(vt_4)
\end{array}
$$
```
    printi(x);
}
```
$$
\begin{array}{lll}
printi & \texttt{a} \mapsto i@vt_7 & vt_6 \leftarrow \mathbf{LoadT}(vt_5) \\
  & & vt_7 \leftarrow \mathbf{ArgT}(vt_6, printi, a)
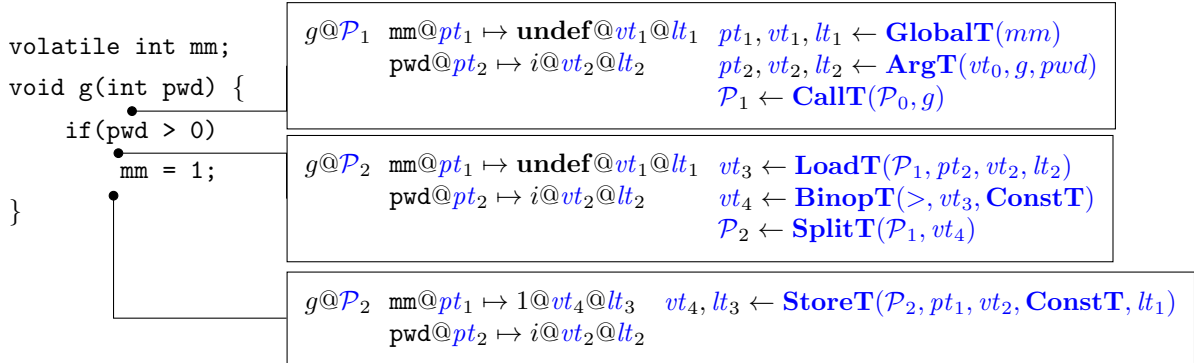\end{array}
$$

These points at which the tags are checked and either propagated or updated by tag rules are termed *control points*. Collectively, the tag rules form a *policy*. Now we define a "don't print `pwd`" policy that tracks the influence of `pwd` and failstops if it would leak. The tag set, as previously noted, consists of the high and low tags, and we define our critical tag rules as follows.

$$\tau ::= \text{H}|\text{L}$$
$$\mathbf{ConstT} = \text{L}$$
$$\mathbf{LocalT}(f) = \text{L}$$
$$\mathbf{LoadT}(vt) = vt$$
$$\mathbf{StoreT}(vt) = vt$$

$$\mathbf{BinopT}(\oplus, vt_1, vt_2) = \begin{cases} \text{L} & \text{if } vt_1 = vt_2 = \text{L} \\ \text{H} & \text{otherwise} \end{cases}$$

$$\mathbf{ArgT}(vt, f, id) = \begin{cases} \mathbf{fail} & \text{if } f = \texttt{printi} \text{ and } vt = \text{H} \\ \text{H} & \text{if } f = \texttt{f} \text{ and } id = \texttt{pwd} \\ vt & \text{otherwise} \end{cases}$$

As we see, most of the tagrules simply move tags around. The most interesting ones are $\mathbf{BinopT}$, which combines two tags, setting the result of a binary operation H if either of its arguments are, and $\mathbf{ArgT}(\mathcal{P}, vt, \text{A}_{f,x}, \text{T}_{ty})$. $\mathbf{ArgT}(\mathcal{P}, vt, \text{A}_{f,x}, \text{T}_{ty})$ checks whether the function being called is `printi` and fails if it is and the value being passed is tagged H. It also always tagged `pwd` H, and otherwise it just maintains the security level of the input it is given. So, in this example, we will be unable to generate a tag vt_7, and the tag processor will throw a failstop rather than allow execution to continue.

Example 1b adds two new wrinkles: we need to keep track of metadata associated with addresses and with the program's control-flow state. If the volatile variable `mm` represents mmapped memory that can be seen publicly, we want to avoid storing the password there. And, by branching on the password, we risk leaking information that could eventually compromise it even if we never print it directly (an *implicit flow*.) So we extend our state with additional "location tags" on memory locations, which we will range over with lt by convention. Meanwhile, pointers, including those that are the static addresses of local or global variables, will be given tags ranged over by pt. Finally, to track metadata associated with the program's control flow, we maintain a special global tag called the PC Tag, ranged over by $\mathcal{P}$.

```
volatile int mm;

void g(int pwd) {

    if(pwd > 0)

        mm = 1;

}
```

$$g@\mathcal{P}_1 \quad \begin{aligned} mm@pt_1 &\mapsto \mathbf{undef}@vt_1@lt_1 \\ pwd@pt_2 &\mapsto i@vt_2@lt_2 \end{aligned} \quad \begin{aligned} pt_1, vt_1, lt_1 &\leftarrow \mathbf{GlobalT}(mm) \\ pt_2, vt_2, lt_2 &\leftarrow \mathbf{ArgT}(vt_0, g, pwd) \\ \mathcal{P}_1 &\leftarrow \mathbf{CallT}(\mathcal{P}_0, g) \end{aligned}$$

$$g@\mathcal{P}_2 \quad \begin{aligned} mm@pt_1 &\mapsto \mathbf{undef}@vt_1@lt_1 \\ pwd@pt_2 &\mapsto i@vt_2@lt_2 \end{aligned} \quad \begin{aligned} vt_3 &\leftarrow \mathbf{LoadT}(\mathcal{P}_1, pt_2, vt_2, lt_2) \\ vt_4 &\leftarrow \mathbf{BinopT}(>, vt_3, \mathbf{ConstT}) \\ \mathcal{P}_2 &\leftarrow \mathbf{SplitT}(\mathcal{P}_1, vt_4) \end{aligned}$$

$$g@\mathcal{P}_2 \quad \begin{aligned} mm@pt_1 &\mapsto 1@vt_4@lt_3 \\ pwd@pt_2 &\mapsto i@vt_2@lt_2 \end{aligned} \quad vt_4, lt_3 \leftarrow \mathbf{StoreT}(\mathcal{P}_2, pt_1, vt_2, \mathbf{ConstT}, lt_1)$$

Here, we initialize the tags on `mm` with the **GlobalT** rule. The PC Tag at the point of call, $\mathcal{P}_0$, is fed to the **CallT** rule to determine a new PC Tag inside of `g`. And the if-statement consults the **SplitT** rule to update the PC Tag inside of its branch based on the value-tag of the expression `pwd < 0`. Once inside the loop, we perform the store, taking into account all of the additional tags.

To upgrade our "don't print the password" policy to "don't leak the password," we can keep most of our rules the same, or extend them in natural ways. Crucially, we will tag memory locations H by default, indicating that they are allowed to contain H-tagged values, but `mm` will be tagged L. The most interesting rules are:

$$\textbf{GlobalT}(id) = \begin{cases} \text{L, L, L} & \text{if } id = \texttt{mm} \\ \text{L, L, H} & \text{otherwise} \end{cases} \quad \textbf{SplitT}(\mathcal{P}, vt) = \begin{cases} \text{L} & \text{if } vt = \mathcal{P} = \text{L} \\ \text{H} & \text{otherwise} \end{cases}$$

$$\textbf{StoreT}(\mathcal{P}, pt, vt_1, vt_2, \overline{lt}) = \begin{cases} \textbf{fail} & \text{if } \mathcal{P} = \text{H or } vt_2 = \text{H and } lt = \text{L} \\ \text{L, H} & \text{if } \mathcal{P} = vt_2 = \text{L} \\ \text{H, H} & \text{otherwise} \end{cases}$$

$$\textbf{ArgT}(vt, f, id) = \begin{cases} \textbf{fail} & \text{if } f = \texttt{printi} \text{ and } vt = \text{H} \\ \text{H, H} & \text{if } f = \texttt{g} \text{ and } id = \texttt{pwd} \\ vt, \text{H} & \text{otherwise} \end{cases}$$

In this case, **SplitT** will set the PC Tag to H, as it branches on a value derived from `pwd`. Then, when it comes time to write to `mm`, **StoreT** will fail rather than write to a low address in a high context.

3 The Language, Informally

Tagged C uses the full syntax of CompCert C [8] with minimal modification (fig. 7). There are two notable syntactical differences in the language, relative to CompCert C: conditionals and loops take an optional *join point* label, and parenthetical expressions an optional "context tag."

Our semantics are a small-step reduction semantics which differ from CompCert C's in two key respects. These are given in full in the appendix. First, Tagged C's semantics contain *control points*: hooks within the operational semantics at which the tag policy is consulted and either tags are updated, or the system failstops. (Control points resemble "advice points" in aspect-oriented programming, but narrowly focused on the manipulation of tags.) A control point consists of the name of a *tag rule* and the bindings of its inputs and outputs; a tag rule is a partial function. The names and signatures of the tag rules, and their corresponding control points, are listed in Table 1.

Second, there is no memory-undefined behavior: the source semantics reflect a concrete target-level view of memory as a flat address space. Without memory safety, programs that exhibit memory-undefined behavior will act as their compiled equivalents would, potentially corrupting memory; we expect that a memory safety policy will be a standard default, but that the strictness of the policy may need to be tuned for programs that use low-level idioms.

The choice of control points and their associations with tag rules, as well as the tag rules' signatures, are a crucial design element. Our proposed design is sufficient for the three classes of policy that we explore in this paper, but it may not be complete.

4   Tags and Policies

Tagged C can enforce a wide range of policies, as follows. A policy consists of a tag type τ, a default tag inhabiting that type, and an instantiation of each tag rule identified in table 1. Tags in gray boxes are optional, as discussed in section 5.

Name Tags When we want to define a per-program policy, we need to be able to attach tags to the program's functions, globals, and so on. We do this by automatically embedding their identifiers in tags, which are available to all policies. These are called *name tags*. We give name tags to the following constructions and identify them as follows:

- Function identifiers, F_f
- Function arguments, $\mathrm{A}_{f,x}$
- Global variables, G_x
- Labels, L_L
- Types, T_{ty}

4.1   PVI Memory Safety

Our first policy is a form of memory safety that uses the "provenance via integer" (PVI) memory model of Memarian et al. [9]. Variations of memory safety have been enforced in PIPE already, but usually using an ad hoc memory model. PVI has the virtue of giving definition to many memory UBs in which a pointer is cast to an integer, subjected to various arithmetic operations, and cast back to a pointer. Their second memory model, *PNVI* (provenance not via integer), is even more permissive. We can also enforce it in Tagged C, though its security value is questionable, and we will not describe it in this paper.

When we say that we want to enforce this specific memory model, we mean that our policy should not failstop on any program that is defined in PVI, and that it should failstop if and when a program reaches undefined behavior. So, in the following examples, we would like `mark` to proceed without failing, while `overflow` should failstop.

Rule Name	Inputs	Outputs	Control Points
LoadT	$\mathcal{P}, pt, vt, \overline{lt}$	vt'	Memory Loads
StoreT	$\mathcal{P}, pt, vt_1, vt_2, \overline{lt}$	$\mathcal{P}', vt', \overline{lt}'$	Memory Stores
UnopT	$\odot, \mathcal{P}, vt$	vt	Unary Operation
BinopT	$\oplus, \mathcal{P}, vt_1, vt_2$	vt'	Binary Operation
ConstT		vt	Applied to Constants/Literals
ExprSplitT	$\mathcal{P}, vt$	$\mathcal{P}'$	Control-flow split points in expressions
ExprJoinT	$\mathcal{P}, \mathcal{P}', vt$	$\mathcal{P}'', vt'$	Join points in expressions
SplitT	$\mathcal{P}, vt, \boxed{L}$	$\mathcal{P}'$	Control-flow split points in statements)
LabelT	$\mathcal{P}, LN(L)$	$\mathcal{P}'$	Labels/arbitrary code points
CallT	$\mathcal{P}, \mathrm{F}_f, \mathrm{F}_{f'}$	$\mathcal{P}'$	Call
ArgT	$\mathcal{P}, vt, \mathrm{A}_{f,x}, \mathrm{T}_{ty}$	$\mathcal{P}', pt, vt', \overline{lt}$	Call
RetT	$\mathcal{P}_{CLE}, \mathcal{P}_{CLR}, vt, \mathrm{F}_f$	$\mathcal{P}', vt'$	Return
GlobalT	$GN(x), TN(ty)$	$pt, vt, \overline{lt}$	Program initialization
LocalT	$\mathcal{P}, \mathrm{T}_{ty}$	$\mathcal{P}', pt, vt, \overline{lt}$	Call
DeallocT	$\mathcal{P}, \mathrm{T}_{ty}$	$\mathcal{P}', vt, \overline{lt}$	Return
ExtCallT	$\mathcal{P}, \mathrm{F}_f, \mathrm{F}_{f'}, \overline{vt}$	$\mathcal{P}'$	Call to linked code
MallocT	$\mathcal{P}, \mathrm{F}_f, \mathrm{F}_{f'}, vt$	$\mathcal{P}', pt, \boxed{vt, \overline{lt}}$	Call to `malloc`
FreeT	$\mathcal{P}, vt$	$\mathcal{P}', pt, \boxed{vt, \overline{lt}}$	Call to `free`
FieldT	$pt, TN(ty), GN(x)$	pt'	Field Access
PICastT	$\mathcal{P}, pt, \boxed{vt, \overline{lt}}$	vt	Cast from pointer to scalar
IPCastT	$\mathcal{P}, vt_1, \boxed{vt_2, \overline{lt}}$	pt	Cast from scalar to pointer
PPCastT	$\mathcal{P}, pt, \boxed{vt, \overline{lt}}$	pt'	Cast between pointers
IICastT	$\mathcal{P}, vt_1$	pt	Cast between scalars

Table 1: Full list of tag-rules in control points

```
int mark(uintptr ptr) {                     void overflow() {
  if (!(ptr & 0x00000001))                    int[3] x;
    *(int*) (ptr | 0x11111110) = 0;           int y;
                                              *x = 222222;
  return ((uintptr) ptr | 0x00000001);        *(x+10) = 333333;
}                                           }
```

Both code snippets are undefined behavior in C, but `mark` is a common low-level idiom [10], seen for example in implementations of Cheney's garbage collection algorithm. Taking advantage of the fact that objects in memory are four-aligned, it uses the lower-order bits of the pointer to store a flag. In this case, it first checks the flag, and if it is not set, it zeroes the memory and then sets it. This is generally considered to be harmless, in contrast to `overflow`, where `y` is being overwritten due to being adjacent in memory to `x`. Here we show the state of the local environment (mapping identifiers to addresses and pointer tags) and memory:

```
void overflow() {
    int[3] x; int y;
    *x = 222222;
    *(x+3) = 333333;
}
```

$$\overline{overflow@\mathcal{P}_2 \quad \text{x} \mapsto \underline{84}@pt_1)}$$
$$\text{y} \mapsto \underline{96}@pt_2$$

84				88				92				96			
undef @ vt_1				**undef** @ vt_1				**undef** @ vt_1				**undef** @ vt_2			
lt_1	lt_1	lt_1	lt_1	lt_1	lt_1	lt_1	lt_1	lt_1	lt_1	lt_1	lt_1	lt_2	lt_2	lt_2	lt_2

84				88				92				96			
222222 @ vt_3				**undef** @ vt_1				**undef** @ vt_1				333333 @ vt_4			
lt_3	lt_3	lt_3	lt_3	lt_1	lt_1	lt_1	lt_1	lt_1	lt_1	lt_1	lt_1	lt_4	lt_4	lt_4	lt_4

We now show separate location tags on each of the four bytes in each word. In the inputs and outputs of a tag rule, we write this $\overline{lt}$, indicating that the rule consumes and produces multiple locations tags. If the PC Tag is $\mathcal{P}_0$ when we enter the function, our initial tags come from $\mathcal{P}_1, pt_1, vt_1, \overline{lt}_1 \leftarrow \mathbf{LocalT}(\mathcal{P}_0, \mathrm{T}_{int})$ followed by $\mathcal{P}_2, pt_2, vt_2, \overline{lt}_2 \leftarrow \mathbf{LocalT}(\mathcal{P}_1, \mathrm{T}_{int})$.

The write to *(x+3) is of particular interest. We tag the result of x+3 with $pt' \leftarrow \mathbf{BinopT}(\mathcal{P}_2, pt_1, \mathbf{ConstT})$; then for the store itself, we use the rule $vt_4, \overline{lt}_4 \leftarrow \mathbf{StoreT}(\mathcal{P}_2, pt', vt_2, \mathbf{ConstT}, \overline{lt}_2)$.

We can prevent overflows like this using a *memory safety* policy. In brief, whenever an object is allocated, it is assigned a unique "color," and its memory locations as well as its pointer are tagged with that color. Pointers maintain their tags under arithmetic operations, and loads and stores are legal if the pointer matches the target memory location. The rules for the *PVI* memory safety policy are given in fig. 2. In this case, we will have $pt_1 = lt_1 = 0$ and $pt_2 = lt_2 = 1$. When we try to write to x+3, we compare $pt' = 0$ with lt_2, and failstop because they differ.

[SNA: Talk about globals here?]

$$\tau ::= clr \qquad\qquad clr \in \mathbb{N}$$
$$N$$

BinopT$(\oplus, \mathcal{P}, vt_1, vt_2)$
case (vt_1, vt_2) of
$(t, N) \qquad \Rightarrow vt' := t$
$(N, t) \qquad \Rightarrow vt' := t$
$(clr_1, clr_2) \Rightarrow vt' := N$
{ vt' }

UnopT$(\odot, \mathcal{P}, vt)$
$vt' := vt$
{ vt }

LocalT$(\mathcal{P}, \mathrm{T}_{ty})$
$\mathcal{P}' := \mathcal{P} + 1; pt := \mathcal{P}$
$vt := N; \overline{lt} := [\mathcal{P}]$
{ $\mathcal{P}', pt, vt, \overline{lt}$ }

MallocT$(\mathcal{P}, \mathrm{F}_f, \mathrm{F}_{f'}, vt)$
$\mathcal{P}' := \mathcal{P} + 1; pt := \mathcal{P}$
$vt := N; \overline{lt} := [\mathcal{P}]$
{ $\mathcal{P}', pt, vt, \overline{lt}$ }

LoadT$(\mathcal{P}, pt, vt, \overline{lt})$
assert $\forall lt \in \overline{lt}.pt = lt$
$vt' := vt$
{ vt' }

StoreT$(\mathcal{P}, pt, vt_1, vt_2, \overline{lt})$
assert $\forall lt \in \overline{lt}.pt = lt$
$\mathcal{P}' := \mathcal{P}; vt' := vt_2; \overline{lt}' := \overline{lt}$
{ $\mathcal{P}', vt', \overline{lt}'$ }

Fig. 2: PVI Memory Safety Policy

The cast rules, meanwhile, have no effect on the tag of the value being cast at all. So, we can cast a pointer to a scalar value, perform any operation that is defined on that type on it, and cast it back, and it will retain its pointer tag. As long as it ends up pointing at the same object, loads and stores will be successful. Function pointers are an exception: Tagged C's underlying control-flow protections prevent them from being tampered with.

4.2 Compartmentalization

In a perfect world, all C programs would be memory safe. But it is unfortunately common for a codebase to contain undefined behavior that will not be fixed, including memory undefined behavior. This may occur because developers intentionally use low-level idioms that are UB [10], or because the cost and risk of regressions may make it undesirable to fix bugs in older code, as opposed to code under active development that is held to a higher standard [3].

A compartmentalization policy isolates potentially risky code, such as code with known UB, from safety-critical code, minimizing the damage that can be done if a vulnerability is exploited. It may also contain restricts on how code in one part of the system may interact with another even in the absence of language-level errors—ideally, restricting each component to the *least privilege* necessary to complete its task. This common form of protection can be implemented at many levels. It is often built into a system's fundamental design, like a web browser sandbox untrusted javascript. But for our use-case, we consider a compartmentalization scheme being added to the system after the fact.

Let's assume that we have a set of compartment identifiers, ranged over by C, and a mapping from function identifiers to compartments, $comp(f)$. This mapping must be provided by a security engineer.

Coarse-grained Protection The core of a compartmentalization scheme is once again memory protection. For the simplest version, we will enforce that memory allocated by a function is only accessible by functions that share its compartment. To do that, we need to keep track of which compartment we're in, using the PC Tag.

Calls and returns each take two steps: first to an intermediate call or return state, and then to the normal execution state, as shown in fig. 3 with some function f calling f'. In the initial call step, **CallT** uses the name-tags of the caller and callee to update the PC Tag. Then, in the step from the call state, we place the function arguments in the temp environment, tagging their values with the results of **ArgT**, and we allocate our stack locals, tagging their values and locations with the results of **LocalT**. And on return, we deallocate locals and update their location tags with **DeallocT** when stepping to a return state, and from there **RetT** updates both the PC Tag and the tag on the returned value.

In our compartmentalization policy (fig. 4), we define a tag to be a compartment identifier or the default N tag. At any given time, the PC Tag carries the
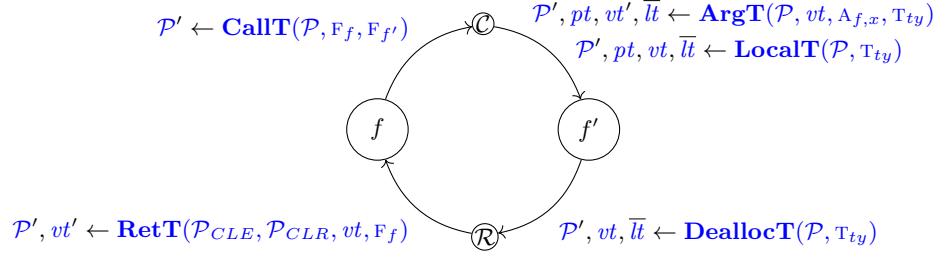
$$\mathcal{P}' \leftarrow \mathbf{CallT}(\mathcal{P}, \mathrm{F}_f, \mathrm{F}_{f'}) \qquad \mathcal{P}', pt, vt', \overline{lt} \leftarrow \mathbf{ArgT}(\mathcal{P}, vt, \mathrm{A}_{f,x}, \mathrm{T}_{ty})$$
$$\mathcal{P}', pt, vt, \overline{lt} \leftarrow \mathbf{LocalT}(\mathcal{P}, \mathrm{T}_{ty})$$

$\mathcal{C}$ f f'

$$\mathcal{P}', vt' \leftarrow \mathbf{RetT}(\mathcal{P}_{CLE}, \mathcal{P}_{CLR}, vt, \mathrm{F}_f) \qquad \mathcal{R} \qquad \mathcal{P}', vt, \overline{lt} \leftarrow \mathbf{DeallocT}(\mathcal{P}, \mathrm{T}_{ty})$$

Fig. 3: Structure of a function call

compartment of the active function, kept up to date by the **CallT** and **RetT** rules.

$$\tau ::= C | N$$

MallocT$(\mathcal{P}, \mathrm{F}_f, \mathrm{F}_{f'},$ **LoadT**$(\mathcal{P}, pt, vt, \overline{lt})$
$\mathcal{P}' := \mathcal{P}; pt := N;$ **assert** $\forall lt \in \overline{lt}.\mathcal{P} = lt$
$vt := N; \overline{lt} := [\mathcal{P}]$ $vt' := vt$
$\{\ \mathcal{P}', pt, vt, \overline{lt}\ \}$ $\{\ vt'\ \}$

CallT$(\mathcal{P}, \mathrm{F}_f, \mathrm{F}_{f'})$
$\mathcal{P}' := comp(f')$
$\{\ \mathcal{P}'\ \}$

LocalT$(\mathcal{P}, \mathrm{T}_{ty})$ **StoreT**$(\mathcal{P}, pt, vt_1, vt_2, \overline{lt})$
$\mathcal{P}' := \mathcal{P}; pt := N;$ **assert** $\forall lt \in \overline{lt}.lt = \mathcal{P}$
$vt := N; \overline{lt} := [\mathcal{P}]$ $\mathcal{P}' := \mathcal{P}; vt' := N; \overline{lt}' := \overline{lt}$
$\{\ \mathcal{P}', pt, vt, \overline{lt}\ \}$ $\{\ \mathcal{P}', vt', \overline{lt}'\ \}$

RetT$(\mathcal{P}_{CLE}, \mathcal{P}_{CLR}, vt, \mathrm{F}_f)$
$\mathcal{P}' := \mathcal{P}_{CLR}; vt' := vt$
$\{\ \mathcal{P}', vt'\ \}$
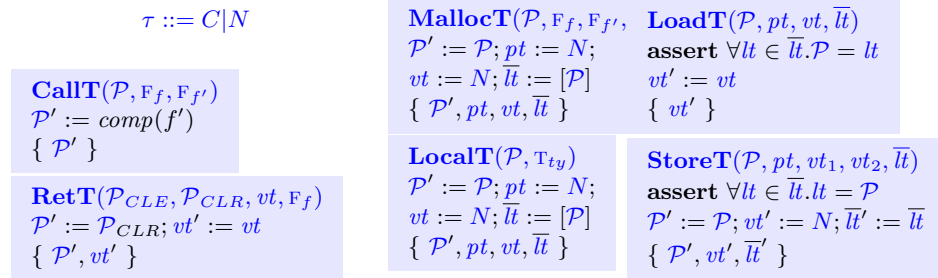
Fig. 4: Compartmentalization Policy

The remainder of the policy works much like memory safety, except that coarse-grained protection means that the "color" we assign to an allocation is the active compartment, and during a load or store, we compare the location tags to the PC Tag, not the pointer.

Sharing Memory The above policy works if our compartments only ever communicate by passing non-pointer values. In practice, this is far too restrictive! Many library functions take pointers and operate on memory shared with the caller. External libraries are effectively required for most software to function yet represent a threat. Isolating external libraries from critical code prevents vulnerabilities in the library from compromising critical code and deprives potential attackers of ROP gadgets and other tools if there is an exploit in the critical code.

To allow intentional sharing of memory across compartments, a more flexible policy is needed. Suppose for example the hostname needs to conform to an expected pattern, such as in an enterprise network, to differentiate between different classes of computers (employee, server, contractor, etc). The standard

library, over in its own compartment, has helpful functions, provided the caller provides the buffers from which to set or get the hostname.

```
void configure_enterprise(char* intended_name) {
  int ret = 0;
  char* curr_name = malloc(HOST_NAME_MAX + 1);
  ret = gethostname( &curr_name, HOST_NAME_MAX + 1 );
  if (! ret && !(strcmp(curr_name, intended_name))) {
    ret = sethostname(intended_name), strlen(intended_name);
    ....
  }
  ....
}
```

The literature contains two main approaches to this problem: *mandatory access control* and *capabilities*. The former explicitly enumerates the access rights of each compartment, while the latter turns passed pointers into unforgeable tokens of privilege, so that the act of passing one implicitly grants the recipient access.

Tagged C can enforce either; here we will demonstrate a capability approach in which we delineate allocations that may be passed and those that must not. At the syntactic level we separate these by creating a variant identifier for `malloc`, `malloc_share`. This identifier maps to the same address (i.e., it is still calling the same function) but its name tag differs and can therefore parameterize the tag rule. The source must have the malloc name changed for every allocation that might be shared. The annotation could be performed manually, or perhaps automatically using some form of escape analysis.

Seen in fig. 5, the policy works by gluing compartmentalization and memory safety together. The PC Tag carries the current compartment and the next color for shared allocations, and **MallocT** uses the function tag to determine which to attach to the pointer and allocated region. During loads and stores, the location tag of the target address determines whether access is restricted via the identity of the active compartment or the validity of the pointer.

4.3 Secure Information Flow

Memory safety and compartmentalization both either prevent or mitigate memory errors. But programs can be memory safe and still do insecure things! Consider the following code, in which we have some error-handling code that writes to a log. [TODO: more realistic example]

```
int checked_div(int a, int b) {
  if (a % b == 0) {
    return a / b;
  } else {
    fprintf(log, "%d should divide %d but doesn't\n", b, a);
```
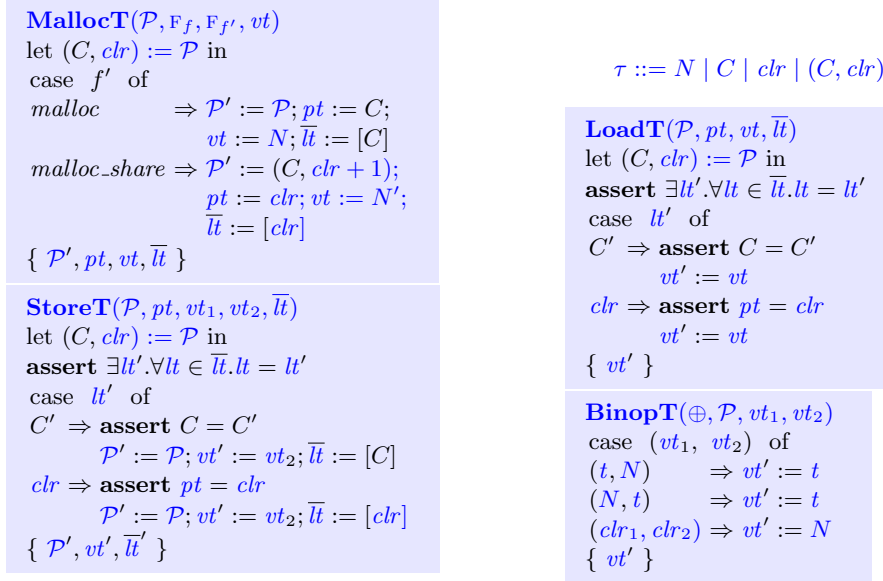
$$\textbf{MallocT}(\mathcal{P}, \mathrm{F}_f, \mathrm{F}_{f'}, vt)$$
let $(C, clr) := \mathcal{P}$ in
case f' of
$malloc \qquad \Rightarrow \mathcal{P}' := \mathcal{P}; pt := C;$
$\qquad\qquad\qquad vt := N; \overline{lt} := [C]$
$malloc\_share \Rightarrow \mathcal{P}' := (C, clr + 1);$
$\qquad\qquad\qquad pt := clr; vt := N';$
$\qquad\qquad\qquad \overline{lt} := [clr]$
$\{\ \mathcal{P}', pt, vt, \overline{lt}\ \}$

$$\textbf{StoreT}(\mathcal{P}, pt, vt_1, vt_2, \overline{lt})$$
let $(C, clr) := \mathcal{P}$ in
assert $\exists lt'.\forall lt \in \overline{lt}.lt = lt'$
case lt' of
$C' \Rightarrow$ **assert** $C = C'$
$\qquad \mathcal{P}' := \mathcal{P}; vt' := vt_2; \overline{lt} := [C]$
$clr \Rightarrow$ **assert** $pt = clr$
$\qquad \mathcal{P}' := \mathcal{P}; vt' := vt_2; \overline{lt} := [clr]$
$\{\ \mathcal{P}', vt', \overline{lt}'\ \}$

$$\tau ::= N \mid C \mid clr \mid (C, clr)$$

$$\textbf{LoadT}(\mathcal{P}, pt, vt, \overline{lt})$$
let $(C, clr) := \mathcal{P}$ in
assert $\exists lt'.\forall lt \in \overline{lt}.lt = lt'$
case lt' of
$C' \Rightarrow$ **assert** $C = C'$
$\qquad vt' := vt$
$clr \Rightarrow$ **assert** $pt = clr$
$\qquad vt' := vt$
$\{\ vt'\ \}$

$$\textbf{BinopT}(\oplus, \mathcal{P}, vt_1, vt_2)$$
case $(vt_1,\ vt_2)$ of
$(t, N) \qquad \Rightarrow vt' := t$
$(N, t) \qquad \Rightarrow vt' := t$
$(clr_1, clr_2) \Rightarrow vt' := N$
$\{\ vt'\ \}$

Fig. 5: Compartmentalization with Shared Capabilities

```
    return 0;
  }
}

void main(int factor) {
  int key = read_and_parse(keyfile);
  int dividend = checked_div(key, factor);
  if (!dividend) { ... } else { ... }
}
```

The checked_div function sometimes writes its arguments to a log, which is reasonable enough, except when it's called with a key as an argument! Suddenly we have keys being written to an unexpected and probably unprotected file.

On the flip side, we have this code, where a malformed user input is accidentally appended to an sql query without sanitization:

```
void sanitize(buf);
char* sql_query(char* query);

void get_data() {
  char[20] name;
  char[100] query = "select address where name =";

  scanf("%19f", name);
```

```
    sanitize(name);
    strncat(query, name, strlen(name));

    sprintf(buf, sql_query(query));
    return;
}
```

These are both instances of problematic "information-flow" that can be pre-
vented using a *secure information flow* (SIF) policy in Tagged C, similar to our
very first example. SIF is described in the venerable Denning and Denning [5],
and part of a larger family of policies known as *information flow control* (IFC.) At
its simplest, if we classify inputs and outputs to the program into secure ("high")
and public ("low") classifications, then the high inputs do not influence the low
outputs. This generalizes to an arbitrary set of security classes, but out first
example is concerned with just two: the value returned from read_and_parse
and the output to the log. In our treatment of this example, we will describe a
policy tailored to this particular set of security classes.

Basic SIF Confidentiality Let's assume that read_and_parse is an *external*
function—that is, we will not model its internal behavior, so we know noth-
ing about the value it returns. We can therefore treat that value as an input,
and track its influence through the system.

For this initial, simplified policy, we will assume that it is the only input that
we care about, so we have four classes of tags. The default tag N represents val-
ues that are not tainted by the sensitive input, the tag *vtaint* represents values
that have been influenced by read_and_parse, and the tag $pc\ f\ ets\ \overline{L}$ carries a
set of labels representing that the current control-flow of the program is tainted
(we will discuss this in detail below.) Lastly, the tag *vol* marks the memory lo-
cations of *volatile* global variables. Volatile variables represent mmapped regions
or memory that for other reasons is accessible outside the process.

Initially, the PC Tag is $pc\ f\ \emptyset\ \emptyset$, and all values and memory locations are
tagged N. The taint tags are introduced at the external call to read_and_parse.
At the same time, all external calls must check that they aren't leaking a tainted
value!

$$\tau ::= N$$
$$vtaint$$
$$pc\ f\ ets\ \overline{L}$$
$$vol$$

When two values are combined with a binary operation, the resulting value
is tainted if either of them was. We define this as the *join* or *least-upper-bound*
operator, $\sqcup$. We will then compare tags according to a partial order, the *no-*

higher-than operator, $\sqsubseteq$. In this case, $a \sqsubseteq b$ means that a does not have higher privilege than b, and so information is allowed to flow from a to b.

$$t_1 \sqcup t_2 \triangleq \begin{cases} vtaint & \text{if } t_1 = vtaint \\ vtaint & \text{if } t_2 = vtaint \\ N & \text{otherwise} \end{cases} \qquad t_1 \sqsubseteq t_2 \triangleq \begin{cases} \textbf{false} & \text{if } t_1 = vtaint \text{ and } t_2 = vol \\ \textbf{true} & \text{otherwise} \end{cases}$$

The policy needs to failstop if a tainted value becomes visible to the outside world. That can happen when the value is passed as an argument to an external function, as we saw above, or when it is stored to volatile memory (typically representing a file or external device that might be read or might transfer.

$$\textbf{BinopT}(\oplus, \mathcal{P}, vt_1, vt_2) \qquad vt_1 \sqcup vt_2$$
$$\{\ vt'\ \}$$

$$\textbf{StoreT}(\mathcal{P}, pt, vt_1, vt_2, \overline{lt})$$
$$\textbf{assert } \forall lt \in \overline{lt}.\mathcal{P} \sqcup pt \sqcup vt_2 \sqsubseteq lt \qquad \mathcal{P}\ \mathcal{P} \sqcup pt \sqcup vt_2\ \overline{lt}$$
$$\{\ \mathcal{P}', vt', \overline{lt}'\ \}$$

Things become trickier when we consider that the program's control-flow itself can be tainted. This can occur in any of our semantics' steps that can produce different statements and continuations depending on the tained value. At that point, any change to the machine state constitutes an information flow. This is termed an *implicit flow*.

Implicit flows become much more complex outside of expressions, when we have more complex control flow. This time the taint is carried on the PC Tag itself. When the PC Tag is tainted, all stores to memory and all updates to environments must also be tainted until all branches eventually rejoin, which might be at any point. We term the point at which it is safe to remove taint a *join point*. In terms of the program's control-flow graph, the join point of a branch is its immediate post-dominator []. [TODO: this is the Denning cited in Bay and Askarov]

In many simple programs, the join point of a conditional or loop is obvious: the point at which the chosen branch is complete, or the loop has ended. Such a simple example can be seen in fig. 6a; `public1` must be tagged with the taint tag of `secret`, while it is safe to tag `public2` N, because that is after the join point, J. The same goes for fig. 6b, if we are in a *termination-insensitive* setting [2]. In termination-insensitive noninterference, we allow an observer to glean information by the termination or non-termination of the program. So, it is safe to assume that the post-dominator J of the while loop is reached.

[TODO: implicit flow rules for statements]

But in the presence of unrestricted go-to statements, a join point may not be local (and sometimes may not exist within the function, assuming that we have not consolidated return points.) Consider **??**, which uses go-to statements to create an approximation of an if-statement whose join-point is far removed

```
int f(bool secret) {                     int f(bool secret) {
    int public1, public2;                    int public1=1;
                                             int public2;
S:  if (secret) {
b1:     public1 = 1;                     S:  while (secret) {
    } else {                             b1:     public1 = 1;
b2:     public1 = 0;                             secret = false;
    }                                        }

J:  public2 = 42;                        J:  public2 = 42;

    return public2;                          return public2;
}                                        }
```

      (a) Leaking via if statements   (b) Leaking via while statements

from the for-loop. The label J now has nothing to do with the semantics of any particular statement.

Luckily this can be determined statically from a function's full control-flow graph, so we can implement it as long as we're willing to deviate from our purely syntax-based tag rules by performing a code transformation. This can be done completely automatically; for each split point in the code, the control-flow graph identifies its join point statement, and the transformation must wrap that statement in a fresh label.

To implement the policy, we must first transform our program by adding labels at the join point of each conditional. Every statement that branches carries an optional label indicating its corresponding join point, if it has one—a function with multiple returns might not, in which case once the PC Tag is tainted, it must remain so until a return.

Intransitive SIF Our second example involves information from outside of the system ending up somewhere it isn't supposed to.

This function sanitizes its input `name`, then appends the result to an appropriate SQL query, storing the result in `buf`. But, in the default case, the programmer has accidentally used the unsanitized string! This creates the opportunity for an SQL injection attack: a caller to this function could (presumably at the behest of an outside user) call it with `field` of 3 and `name` of "Bobby; drop table;".

In this example, we want to implement an *intransitive integrity* SIF policy: we wish to allow `name` to influence the result of `sanitize`, naturally, and the result of `sanitize` to influence the value passed to `sql_query`, but we do not wish for `name` to influence `sql_query` directly.

In this context, we consider the function `scanf` to be our information source, `sql_query` as the "sink" that we don't want it to flow to, and `sanitize` as clearing the taint off of data it touches.

Tags are similar to our previous example, except for removing *vol*. The PC Tag tracks the current function identifier, a stack of expression taints *ets*, and a set of labels, *sts*. *ets* tracks how many defered expression evaluations depend on tainted values. [TODO: slightly off]. Each label L in *sts* indicates that until reaching label L, the state itself has been influenced by `scanf`.

$$\tau ::= vtaint$$
$$pc \ f \ ets \ \overline{L} \quad ets \in \mathbb{N}$$
$$N$$

We define the *join* operation again.

$$t_1 \sqcup t_2 \triangleq \begin{cases} vtaint & \text{if } t_1 = vtaint \ \text{ or } t_2 = vtaint \\ vtaint & \text{if } t_1 = pc \ f \ ets \ sts \text{ and either } ets > 0 \text{ or } |sts| > 0 \\ vtaint & \text{if } t_2 = pc \ f \ ets \ sts \text{ and either } ets > 0 \text{ or } |sts| > 0 \\ N & \text{otherwise} \end{cases}$$

And once again we wish to define the "no-higher-than" relation. In this case, we want to avoid user input from `scanf` flowing to `sql_query`. So we will define that a PC Tag in the function `sql_query`, is strictly higher security than anything tainted.

$$t_1 \sqsubseteq t_2 \triangleq \begin{cases} \mathbf{f} & \text{if } t_1 = vtaint \ \text{ and } t_2 = pc \ \texttt{query} \ \_ \ \_ \\ \mathbf{t} & \text{otherwise} \end{cases}$$

Tainting and Cleaning Memory In this version of the policy we care primarily about loads and stores. External data are first tainted by `scanf` when they are stored into memory, then (ideally) `sanitize` processes them and stores a clean version into its destination buffer. Finally, if `sql_query` is exposed to tainted data, it will be by loading from memory.

$$\mathbf{StoreT}(\mathcal{P}, pt, vt_1, vt_2, \overline{lt})$$
let $vt' = \mathcal{P} \sqcup vt \sqcup pt$ in
$\{ \mathcal{P}', vt', \overline{lt}' \}$
$$\mathcal{P} \begin{cases} vtaint & \text{if } f = \texttt{scanf} \\ N & \text{if } f = \texttt{sanitize} \ \overline{lt} \\ vt' & \text{otherwise} \end{cases}$$

$$\mathbf{LoadT}(\mathcal{P}, pt, vt, \overline{lt})$$
assert $\mathcal{P} \sqcup vt \sqcup pt \sqsubseteq \mathcal{P}$
$vt' := vt$
$\{ vt' \}$

The remainder of the policy resembles our first SIF example.

Realizing IFC In order to implement an IFC policy, we need to specify the rules that it needs to enforce. The positive here is that the rules are not dependent on one another (with the exception of declassification rules), and default to permissiveness when no rule is given. We assume that the user would supply a

separate file consisting of a list of triples: the source, the sink, and the type of rule. This is then translated into the policy.

The other implementation detail to consider are the label tags. These resemble instruction tags, and that is exactly how they would be implemented: as a special instruction tag on the appropriate instruction, which might be an existing instruction or a specially added no-op, that the processor handles by introducing a tag corresponding to that label.

It remains to generate those labels. For purposes of an IFC policy, we first generate the program's control flow graph. Then, for each if, while, do-while, for, and switch statement, we identify the immediate post-dominator in the graph, and wrap it in a label statement with a fresh identifier. That identifier is also added as a field in the original conditional statement. The tags associated with the labels are initialized at program state—in the case of IFC, these defaults declare that there are no secrets to lower when it is reached.

5 Implementing Tagged C with PIPE

Chhak et al. [4] introduce a verified compiler from a toy high-level language with tags to a control-flow-graph-based intermediate representation of a PIPE-based ISA. It is a proof-of-concept of compilation from a source language's tag policy to realistic hardware. Everything in a PIPE system carries tags, including instructions. Instruction tags are statically determined at compile-time, so they can carry data about source-level control points in the corresponding assembly. This means that PIPE can emulate any given Tagged C policy by running two policies in parallel: a basic stack-and-function-pointer-safety policy to mimic Tagged C's high-level control-flow, and the source-level policy as written. AN: automatically run? [SNA: I don't understand the question, but perhaps this edit helps?]

Chhak et al.'s general strategy for mapping Tagged C's tag rules sometimes requires adding extra instructions to the generated code. A Tagged-C control point may require a tag from a location that is not read under a normal compilation scheme, or must update tags in locations that would otherwise not be written. Such instructions are unnecessary overhead if the policy doesn't meaningfully use the relevant tags.

To mitigate this, control points whose compilation would add potentially extraneous instructions take optional parameters or return optional results. We will explain how the rule should be implemented in the target if the options are used. [SNA: I think we have been leaning toward doing so in more detail here, not threaded through the rest of the paper. So that's a TODO.] Optional inputs and outputs are marked with $\boxed{\text{boxes}}$. If a policy does not make use of the options, it will be sound to compile without the extra instructions.

6 Evaluation

Tagged C aims to combine the flexibility of tag-based architectures with the abstraction of a high-level language. How well have we achieved this aim?

[Here we list criteria and evaluate how we fulfilled them]

– Flexibility: we demonstrate three policies that can be used alone or in conjunction
– Applicability: we support the full complement of C language features and give definition to many undefined C programs
– Practical security: our example security policies are based on important security concepts from the literature

6.1 Limitations of the Tag Mechanism

By committing to a tag-based mechanism, we do restrict the space of policies that Tagged C can enforce. In general, a reference monitor can enforce any policy that constitutes a *safety property*—any policy whose violation can be demonstrated by a single finite trace. This class includes such policies as "no integer overflow" and "pointers are always in-bounds," which depend on the values of variables. Tag-based monitors cannot enforce any policy that depends on the value of a variable rather than its tags.

7 Related Work

Reference Monitors The concept of a reference monitor was first introduced fifty years ago in [1]: a tamper-proof and verifiable subsystem that checks every security-relevant operation in a system to ensure that it conforms to a security *policy* (a general specification of acceptable behavior; see [7].)

A reference monitor can be implemented at any level of a system. An *inline reference monitor* is a purely compiler-based system that inserts checks at appropriate places in the code. Alternatively, a reference monitor might be embedded in the operating system, or in an interpreted language's runtime. A *hardware reference monitor* instead provides primitives at the ISA-level that accelerate security and make it harder to subvert.

Programmable Interlocks for Policy Enforcement (PIPE) [6] is a hardware extension that uses *metadata tagging*. Each register and each word of memory is associated with an additional array of bits called a tag. The policy is decomposed into a set of *tag rules* that act in parallel with each executing instruction, using the tags on its operands to decide whether the instruction is legal and, if so, determine which tags to place on its results. PIPE tags are large relative to other tag-based hardware, giving it the flexibility to implement complex policies with structured tags, and even run multiple policies at once.

Other hardware monitors include Arm MTE, [Binghamton], and CHERI. Arm MTE aims to enforce a narrow form of memory safety using 4-bit tags, which distinguish adjacent objects in memory from one another, preventing buffer overflows, but not necessarily other memory violations. [TODO: read the Binghamton paper, figure out where they sit here.]

CHERI is capability machine [TODO: cite OG CHERI]. In CHERI, capabilities are "fat pointers" carrying extra bounds and permission information,

and capability-protected memory can only be accessed via a capability with the appropriate privilege. This is a natural way to enforce spatial memory safety, and techniques have been demonstrated for enforcing temporal safety [14], stack safety [13], and compartmentalization [TODO: figure out what to cite], with varying degrees of ease and efficiency. But CHERI cannot easily enforce notions of security based on dataflow, such as Secure Information Flow.

In this paper, we describe a programming language with an abstract reference monitor. We realize it as an interpreter with the reference monitor built in, and envision eventually compiling to PIPE-equipped hardware. An inlining compiler would also be plausible. As a result of this choice, our abstract reference monitor uses a PIPE-esque notion of tags.

Aspect Oriented Programming [TODO: do forward search from original AOP paper]

8 Future Work

We have presented the language and a reference interpreter, built on top of the CompCert interpreter [8], and three example policies. There are several significant next-steps.

Compilation An interpreter is all well and good, but a compiler would be preferable for many reasons. A compiled Tagged C could use the hardware acceleration of a PIPE target, and could more easily support linked libraries, including linking against code written in other languages. The ultimate goal would be a fully verified compiler, but that is a very long way off.

Language Proofs There are a couple of properties of the language semantics itself that we would like to prove. Namely (1) that its behavior (prior to adding a policy) matches that of CompCert C and (2) that the behavior of a given program is invariant under all policies up to truncation due to failstop.

Policy Correctness Proofs For each example policy discussed in this paper, we sketched a formal specification for the security property it ought to enforce. A natural continuation would be to prove the correctness of each policy against these specifications.

Policy DSL Currently, policies are written in Gallina, the language embedded in Coq. This is fine for a proof-of-concept, but not satisfactory for real use. We plan to develop a domain-specific policy language to make it easier to write Tagged C policies.

References

1. Anderson, J.P.: Computer Security Technology Planning Study. Tech. rep., U.S. Air Force Electronic Systems Division (10 1972)

2. Askarov, A., Hunt, S., Sabelfeld, A., Sands, D.: Termination-insensitive noninterference leaks more than just a bit. In: Jajodia, S., Lopez, J. (eds.) Computer Security - ESORICS 2008. pp. 333–348. Springer Berlin Heidelberg, Berlin, Heidelberg (2008)

3. Bessey, A., Block, K., Chelf, B., Chou, A., Fulton, B., Hallem, S., Henri-Gros, C., Kamsky, A., McPeak, S., Engler, D.: A few billion lines of code later: Using static analysis to find bugs in the real world. Commun. ACM **53**(2), 66–75 (feb 2010). `https://doi.org/10.1145/1646353.1646374`, `https://doi.org/10.1145/1646353.1646374`

4. Chhak, C., Tolmach, A., Anderson, S.: Towards formally verified compilation of tag-based policy enforcement. In: Proceedings of the 10th ACM SIGPLAN International Conference on Certified Programs and Proofs. p. 137–151. CPP 2021, Association for Computing Machinery, New York, NY, USA (2021). `https://doi.org/10.1145/3437992.3439929`, `https://doi.org/10.1145/3437992.3439929`

5. Denning, D.E., Denning, P.J.: Certification of programs for secure information flow. Commun. ACM **20**(7), 504–513 (jul 1977). `https://doi.org/10.1145/359636.359712`, `https://doi.org/10.1145/359636.359712`

6. Dhawan, U., Vasilakis, N., Rubin, R., Chiricescu, S., Smith, J.M., Knight Jr., T.F., Pierce, B.C., DeHon, A.: PUMP: A Programmable Unit for Metadata Processing. In: Proceedings of the Third Workshop on Hardware and Architectural Support for Security and Privacy. p. 8:1–8:8. HASP '14, ACM, New York, NY, USA (2014). `https://doi.org/10.1145/2611765.2611773`, `http://doi.acm.org/10.1145/2611765.2611773`

7. Goguen, J.A., Meseguer, J.: Security policies and security models. In: IEEE Symposium on Security and Privacy. pp. 11–20. IEEE Computer Society (1982), `http://dblp.uni-trier.de/db/conf/sp/sp1982.html#GoguenM82a`

8. Leroy, X.: Formal verification of a realistic compiler. Commun. ACM **52**(7), 107–115 (jul 2009). `https://doi.org/10.1145/1538788.1538814`, `https://doi.org/10.1145/1538788.1538814`

9. Memarian, K., Gomes, V.B.F., Davis, B., Kell, S., Richardson, A., Watson, R.N.M., Sewell, P.: Exploring c semantics and pointer provenance. Proc. ACM Program. Lang. **3**(POPL) (jan 2019). `https://doi.org/10.1145/3290380`, `https://doi.org/10.1145/3290380`

10. Memarian, K., Matthiesen, J., Lingard, J., Nienhuis, K., Chisnall, D., Watson, R.N.M., Sewell, P.: Into the depths of c: Elaborating the de facto standards. SIGPLAN Not. **51**(6), 1–15 (jun 2016). `https://doi.org/10.1145/2980983.2908081`, `https://doi.org/10.1145/2980983.2908081`

11. Munoz, D.: After all these years, the world is still powered by c programming, `https://www.toptal.com/c/after-all-these-years-the-world-is-still-powered-by-c-programming`

12. Overflow, S.: 2022 stack overflow annual developer survey (2022), `https://survey.stackoverflow.co/2022/`

13. Skorstengaard, L., Devriese, D., Birkedal, L.: StkTokens: Enforcing Well-bracketed Control Flow and Stack Encapsulation using Linear Capabilities. Proceedings of the ACM on Programming Languages **3**(POPL), 1–28 (2019)

14. Wesley Filardo, N., Gutstein, B.F., Woodruff, J., Ainsworth, S., Paul-Trifu, L., Davis, B., Xia, H., Tomasz Napierala, E., Richardson, A., Baldwin, J., Chisnall, D., Clarke, J., Gudka, K., Joannou, A., Theodore Markettos, A., Mazzinghi, A., Norton, R.M., Roe, M., Sewell, P., Son, S., Jones, T.M., Moore, S.W., Neumann, P.G., Watson, R.N.M.: Cornucopia: Temporal safety for cheri heaps. In: 2020 IEEE

A   Syntax

$s ::=$ `Sskip`

$\quad$ |`Sdo` e

$\quad$ |`Sseq` s_1 s_2

$\quad$ |`Sif`(e) `then` s_1 `else` s_2 `join` L

$\quad$ |`Swhile`(e) `do` s `join` L

$\quad$ |`Sdo` s `while` (e) `join` L

$\quad$ |`Sfor`$(s_1; e; s_2)$ `do` s_3 `join`

$\quad$ |`Sbreak`

$\quad$ |`Scontinue`

$\quad$ |`Sreturn`

$\quad$ |`Sswitch` e { $\overline{(L, s)}$ } `join`

$\quad$ |`Slabel` L : s

$\quad$ |`Sgoto` L

$e ::=$		
$Eval\ v@vt$		Value
|$Evar\ x$		Variable
|$Efield\ e\ id$		Field
|$EvalOf\ e$		Load from Object
|$Ederef\ e$		Dereference Pointer
|$EaddrOf\ e$		Address of Object
|$Eunop\ \odot\ e$		Unary Operator
|$Ebinop\ \oplus\ e_1\ e_2$		Binary Operator
|$Ecast\ e\ ty$		Cast
|$Econd\ e_1\ e_2\ e_3$		Conditional
|$Esize\ ty$		Size of Type
|$Ealign\ ty$		Alignment of Type
|$Eassign\ e_1\ e_2$		Assignment
|$EassignOp\ \oplus\ e_1\ e_2$		Operator Assignment
|$EpostInc\ \oplus\ e$		Post-Increment/Decrement
|$Ecomma\ e_1\ e_2$		Expression Sequence
|$Ecall\ e_f(\overline{e}_{args})$		Function Call
|$Eloc\ l@lt$		Memory Location
|$Eparen\ e\ ty\ t$		Parenthetical with Optional Cast

Fig. 7: Tagged C Abstract Syntax

B Continuations

$$
\begin{aligned}
k ::= & Kemp \\
| & Kdo;\ k \\
| & Kseq\ s;\ k \\
| & Kif\ s_1\ s_2\ L;\ k \\
| & KwhileTest\ e\ s\ L;\ k \\
| & KwhileLoop\ e\ s\ L;\ k \\
| & KdoWhileTest\ e\ s\ L;\ k \\
| & KdoWhileLoop\ e\ s\ L;\ k \\
| & Kfor\ (e, s_2)\ s_3\ L;\ k \\
| & KforPost\ (e, s_2)\ s_3\ L;\ k
\end{aligned}
$$

C States

States can be of several kinds, denoted by their script prefix: a *general state* $\mathcal{S}(\dots)$, an *expression state* $\mathcal{E}(\dots)$, a *call state* $\mathcal{C}(\dots)$, or a *return state* $\mathcal{R}(\dots)$. Finally, the special state *failstop* ($\mathcal{F}(\dots)$) represents a tag failure, and carries the state that produced the failure. [Allison: to whatever degree you've figured out what is useful here by publication-time, we can tune this to be more specific.]

$$
\begin{aligned}
S ::= & \mathcal{S}\left(m \mid s \gg k@\mathcal{P}\right) \\
| & \mathcal{E}\left(m \mid e \gg k@\mathcal{P}\right) \\
| & \mathcal{C}\left(\mathcal{P} \mid m(le) \gg f'@f\right) \overline{Eval\ v@vt}k \\
| & \mathcal{R}\left(m \mid ge \gg le@\mathcal{P}\right) Eval\ v@vt k \\
| & \mathcal{F}\left(S\right)
\end{aligned}
$$

D Initial State

Given a list xs of variable identifiers id and types ty, a program's initial memory is defined by iteratively allocating each one in memory and updating the global environment with its base address, bound, type, and a static identity tag. Let $|ty|$ be a function from types to their sizes in bytes. The memory is initialized **undef**$@vt@\overline{lt}$ for some vt and $\overline{lt}$, unless given an initializer. Let m_0 and ge_0 be the initial (empty) memory and environment. The parameter b marks the start of the global region.

$$
globals\ xs\ b = \begin{cases}
(m_0, ge_0) & \text{if } xs = \varepsilon \\
(m[p \dots p + |ty| \mapsto \mathbf{undef}@vt@\overline{lt}]_{|ty|}, & \text{if } xs = (id, ty) :: xs' \\
\quad ge[id \mapsto (p, p + |ty|, ty, pt)]) & \text{and } pt, vt, \overline{lt} \leftarrow \mathbf{GlobalT}(GN(x), TN(ty)) \\
& \text{where } (m, ge) = globals\ xs'\ (b + |ty|)
\end{cases}
$$

E   Step Rules

E.1   Sequencing rules

$$\overline{\mathcal{S}\left(m \mid \texttt{Sdo } e \gg k@\mathcal{P}\right) \longrightarrow \mathcal{E}\left(m \mid e \gg Kdo;\ k@\mathcal{P}\right)}$$

$$\overline{\mathcal{E}\left(m \mid Eval\ v@vt \gg Kdo;\ k@\mathcal{P}\right) \longrightarrow \mathcal{S}\left(m \mid \texttt{Sskip} \gg k@\mathcal{P}\right)}$$

$$\overline{\mathcal{S}\left(m \mid \texttt{Sseq } s_1\ s_2 \gg k@\mathcal{P}\right) \longrightarrow \mathcal{S}\left(m \mid s_1 \gg Kseq\ s_2;\ k@\mathcal{P}\right)}$$

$$\overline{\mathcal{S}\left(m \mid \texttt{Sskip} \gg Kseq\ s;\ k@\mathcal{P}\right) \longrightarrow \mathcal{S}\left(m \mid s \gg k@\mathcal{P}\right)}$$

$$\overline{\mathcal{S}\left(m \mid \texttt{Scontinue} \gg Kseq\ s;\ k@\mathcal{P}\right) \longrightarrow \mathcal{S}\left(m \mid \texttt{Scontinue} \gg k@\mathcal{P}\right)}$$

$$\overline{\mathcal{S}\left(m \mid \texttt{Sbreak} \gg Kseq\ s;\ k@\mathcal{P}\right) \longrightarrow \mathcal{S}\left(m \mid \texttt{Sbreak} \gg k@\mathcal{P}\right)}$$

$$\frac{\mathcal{P}' \leftarrow \mathbf{LabelT}(\mathcal{P}, LN(L))}{\mathcal{S}\left(m \mid \texttt{Slabel } L:\ s \gg k@\mathcal{P}\right) \longrightarrow \mathcal{S}\left(m \mid s \gg k@\mathcal{P}'\right)}$$

E.2   Conditional rules

$$\frac{s = \texttt{Sif}(e) \texttt{ then } s_1 \texttt{ else } s_2 \texttt{ join } L}{\mathcal{S}\left(m \mid s \gg k@\mathcal{P}\right) \longrightarrow \mathcal{E}\left(m \mid e \gg Kif\ s_1\ s_2\ L;\ k@\mathcal{P}\right)}$$

$$\frac{s' = \begin{cases} s_1 & \text{if } boolof(v) = \mathbf{t} \\ s_2 & \text{if } boolof(v) = \mathbf{f} \end{cases} \qquad \mathcal{P}' \leftarrow \mathbf{SplitT}(\mathcal{P}, vt, \boxed{L})}{\mathcal{E}\left(m \mid Eval\ v@vt \gg Kif\ s_1\ s_2\ L;\ k@\mathcal{P}\right) \longrightarrow \mathcal{S}\left(m \mid s' \gg k@\mathcal{P}'\right)}$$

$$\overline{\mathcal{S}\left(m \mid \texttt{Sswitch } e\ \{\ \overline{(v,s)}\ \} \texttt{ join } L \gg k@\mathcal{P}\right) \longrightarrow \mathcal{E}\left(m \mid e \gg Kswitch1\ \overline{(v,s)}\ L;\ k@\mathcal{P}\right)}$$

$$\frac{select\ v\ \overline{(v,s)} = s \qquad\qquad \mathcal{P}' \leftarrow \mathbf{SplitT}(\mathcal{P}, vt, \boxed{L})}{\mathcal{E}\left(m \mid Eval\ v@vt \gg Kswitch1\ \overline{(v,s)}\ L;\ k@\mathcal{P}\right) \longrightarrow \mathcal{S}\left(m \mid s \gg Kswitch2;\ k@\mathcal{P}'\right)}$$

$$\frac{s = \texttt{Sbreak} \lor s = \texttt{Sskip}}{\mathcal{S}\left(m \mid s \gg Kswitch2;\ k@\mathcal{P}\right) \longrightarrow \mathcal{S}\left(m \mid \texttt{Sskip} \gg k@\mathcal{P}\right)}$$

$$\overline{\mathcal{S}\left(m \mid \texttt{Scontinue} \gg Kswitch2;\ k@\mathcal{P}\right) \longrightarrow \mathcal{S}\left(m \mid \texttt{Scontinue} \gg k@\mathcal{P}\right)}$$

E.3 Loop rules

$$\frac{s = \texttt{Swhile}(e) \texttt{ do } s' \texttt{ join } L}{\mathcal{S}\,(m \mid s \gg k@\mathcal{P}) \longrightarrow \mathcal{E}\,(m \mid e \gg KwhileTest\ e\ s'\ L;\ k@\mathcal{P})}$$

$$\frac{boolof(v) = \mathbf{t} \qquad k_1 = KwhileTest\ e\ s\ L;\ k \\ k_2 = KwhileLoop\ e\ s\ L;\ k \quad \mathcal{P}' \leftarrow \mathbf{SplitT}(\mathcal{P}, vt, \boxed{L})}{\mathcal{E}\,(m \mid Eval\ v@vt \gg k_1@\mathcal{P}) \longrightarrow \mathcal{S}\,(m \mid s \gg k_2@\mathcal{P}')}$$

$$\frac{boolof(v) = \mathbf{f}\ k = KwhileTest\ e\ s\ L;\ k'\ \mathcal{P}' \leftarrow \mathbf{SplitT}(\mathcal{P}, vt, \boxed{L})}{\mathcal{E}\,(m \mid Eval\ v@vt \gg k@\mathcal{P}) \longrightarrow \mathcal{S}\,(m \mid \texttt{Sskip} \gg k'@\mathcal{P}')}$$

$$\frac{s = \texttt{Sskip} \vee s = \texttt{Scontinue} \qquad k = KwhileLoop\ e\ s\ L;\ k'}{\mathcal{S}\,(m \mid s \gg k@\mathcal{P}) \longrightarrow \mathcal{S}\,(m \mid \texttt{Swhile}(e) \texttt{ do } s \texttt{ join } L \gg k'@\mathcal{P})}$$

$$\frac{k = KwhileLoop\ e\ s\ L;\ k'}{\mathcal{S}\,(m \mid \texttt{Sbreak} \gg k@\mathcal{P}) \longrightarrow \mathcal{S}\,(m \mid \texttt{Sskip} \gg k'@\mathcal{P})}$$

$$\frac{s = \texttt{Sdo } s' \texttt{ while }(e) \texttt{ join } L\ k' = KdoWhileLoop\ e\ s'\ L;\ k}{\mathcal{S}\,(m \mid s \gg k@\mathcal{P}) \longrightarrow \mathcal{S}\,(m \mid s' \gg k'@\mathcal{P})}$$

$$\frac{k_1 = KdoWhileLoop\ e\ s\ L;\ k' \qquad k_2 = KdoWhileTest\ e\ s\ L;\ k}{\mathcal{S}\,(m \mid s' = \texttt{Sskip} \vee s' = \texttt{Scontinue} \gg k_1@\mathcal{P}) \longrightarrow \mathcal{E}\,(m \mid e \gg k_2@\mathcal{P})}$$

$$\frac{boolof(v) = \mathbf{f}\ k = KdoWhileTest\ e\ s\ L;\ k'\ \mathcal{P}' \leftarrow \mathbf{SplitT}(\mathcal{P}, vt, \boxed{L})}{\mathcal{S}\,(m \mid Eval\ v@vt \gg k@\mathcal{P}) \longrightarrow \mathcal{S}\,(m \mid \texttt{Sskip} \gg k'@\mathcal{P}')}$$

$$\frac{boolof(v) = \mathbf{t}\ k = KdoWhileTest\ e\ s\ L;\ k' \qquad \mathcal{P}' \leftarrow \mathbf{SplitT}(\mathcal{P}, vt, \boxed{L})}{\mathcal{S}\,(m \mid Eval\ v@vt \gg k@\mathcal{P}) \longrightarrow \mathcal{S}\,(m \mid \texttt{Sdo } s \texttt{ while }(e) \texttt{ join } L \gg k'@\mathcal{P}')}$$

$$\frac{k = KdoWhileLoop\ e\ s\ L;\ k'}{\mathcal{S}\,(m \mid \texttt{Sbreak} \gg k@\mathcal{P}) \longrightarrow \mathcal{S}\,(m \mid \texttt{Sskip} \gg k'@\mathcal{P})}$$

$$\frac{s = \texttt{Sfor}(s_1; e; s_2) \texttt{ do } s_3 \texttt{ join } L \qquad\qquad s_1 \neq \texttt{Sskip}}{\mathcal{S}\,(m \mid s \gg k@\mathcal{P}) \longrightarrow \mathcal{S}\,(m \mid s_1 \gg Kseq\ \texttt{Sfor}(\texttt{Sskip}; e; s_2) \texttt{ do } s_3 \texttt{ join } L;\ k@\mathcal{P})}$$

$$\frac{s = \texttt{Sfor}(\texttt{Sskip}; e; s_2) \texttt{ do } s_3 \texttt{ join } L}{\mathcal{S}\,(m \mid s \gg k@\mathcal{P}) \longrightarrow \mathcal{E}\,(m \mid e \gg Kfor\ (e, s_2)\ s_3\ L;\ k@\mathcal{P})}$$

$$\frac{boolof(v) = \mathbf{f} \qquad\qquad \mathcal{P}' \leftarrow \mathbf{SplitT}(\mathcal{P}, vt, \boxed{L})}{\mathcal{E}\,(m \mid Eval\ v@vt \gg Kfor\ (e, s_2)\ s_3\ L;\ k@\mathcal{P}) \longrightarrow \mathcal{S}\,(m \mid \texttt{Sskip} \gg k@\mathcal{P})}$$

$$\frac{k = Kfor\ (e, s_2)\ s_3\ L;\ k'\ boolof(v) = \mathbf{t}\ \mathcal{P}' \leftarrow \mathbf{SplitT}(\mathcal{P}, vt, \boxed{L})}{\mathcal{E}\,(m \mid Eval\ v@vt \gg k@\mathcal{P}) \longrightarrow \mathcal{S}\,(m \mid s_3 \gg k@\mathcal{P})}$$

$$\frac{k = Kfor\ (e, s_2)\ s_3\ L; \qquad\qquad s = \texttt{Sskip} \vee s = \texttt{Scontinue}}{\mathcal{S}\,(m \mid s \gg k@\mathcal{P}) \longrightarrow \mathcal{S}\,(m \mid \texttt{Sfor}(\texttt{Sskip}; e; s_2) \texttt{ do } s_3 \texttt{ join } L \gg KforPost\ (e, s_2)\ s_3\ L;\ k@\mathcal{P})}$$

$$\frac{k = \textit{Kfor } (e, s_1)\ s_2\ L;\ k'}{\mathcal{S}\,(m \mid \mathtt{Sbreak} \gg k@\mathcal{P}) \longrightarrow \mathcal{S}\,(m \mid \mathtt{Sskip} \gg k'@\mathcal{P})}$$

$$\frac{k = \textit{KforPost } (e, s_2)\ s_3\ L;\ k'}{\mathcal{S}\,(m \mid \mathtt{Sskip} \gg k@\mathcal{P}) \longrightarrow \mathcal{S}\,(m \mid \mathtt{Sfor(Sskip}; e; s_2)\ \mathtt{do}\ s_3\ \mathtt{join}\ L \gg k@\mathcal{P})}$$

E.4 Contexts

Our expression semantics are contextual. A context *ctx* is a function from an expression to an expression and a tag. We identify a valid context using the *context* relation over a "kind" (left-hand or right-hand, LH or RH), and an expression.

context $k\ C[e] ::=$

    $\mid context\ k\ \lambda e.e$

    $\mid context$ LH $\lambda e.Ederef\ C[e]$ where *context* RH $C[e]$

    $\mid context$ LH $\lambda e.Efield\ C[e]\ id$ where *context* RH $C[e]$

    $\mid context$ RH $\lambda e.EvalOf\ C[e]$ where *context* LH $C[e]$

    $\mid context$ RH $\lambda e.EaddrOf\ C[e]$ where *context* LH $C[e]$

    $\mid context$ RH $\lambda e.Eunop\ \odot\ C[e]$ where *context* RH $C[e]$

    $\mid context$ RH $\lambda e.Ebinop\ \oplus\ C[e_1]\ e_2$ where *context* RH $C[e_1]$

    $\mid context$ RH $\lambda e.Ebinop\ \oplus\ e_1\ C[e_2]$ where *context* RH $C[e_2]$

    $\mid context$ RH $\lambda e.Ecast\ C[e]\ ty$ where *context* RH $C[e]$

    $\mid context$ RH $\lambda e.EseqAnd\ C[e_1]\ e_2$ where *context* RH $C[e_1]$

    $\mid context$ RH $\lambda e.EseqOr\ C[e_1]\ e_2$ where *context* RH $C[e_1]$

    $\mid context$ RH $\lambda e.Econd\ C[e_1]\ e_2\ e_3$ where *context* RH $C[e_1]$

    $\mid context$ RH $\lambda e.Eassign\ C[e_1]\ e_2$ where *context* LH $C[e_1]$

    $\mid context$ RH $\lambda e.Eassign\ e_1\ C[e_2]$ where *context* RH $C[e_2]$

    $\mid context$ RH $\lambda e.EassignOp\ \oplus\ C[e_1]\ e_2$ where *context* LH $C[e_1]$

    $\mid context$ RH $\lambda e.EassignOp\ \oplus\ e_1\ C[e_2]$ where *context* RH $C[e_2]$

    $\mid context$ RH $\lambda e.EpostInc\ \oplus\ C[e]$ where *context* LH $C[e]$

    $\mid context$ RH $\lambda e.Ecall\ C[e_1]\,(\overline{e_2})$ where *context* RH $C[e_1]$

    $\mid context$ RH $\lambda e.Ecall\ e_1(C[\overline{e_2}])$ where *context* RH $C[e]$ for $e \in \overline{e_2}$

    $\mid context$ RH $\lambda e.Ecomma\ C[e_1]\ e_2$ where *context* RH $C[e_1]$

    $\mid context$ RH $\lambda e.Eparen\ C[e]\ ty$ where *context* RH $C[e]$

    $\mid context$ RH $\lambda e.Eparen\ C[e]\ ty\ t$ where *context* RH $C[e]$

Next, we define a notion of expression reduction. A left-hand reduction relates an expression to an expression. A right-hand reduction relates a triple of PC Tag, memory, and expression to another such triple.

$$\frac{context\ \text{LH}\ C[e] \qquad\qquad e \Rightarrow_{\text{LH}} e'}{\mathcal{E}\,(m \mid C[e] \gg k@\mathcal{P}) \longrightarrow \mathcal{E}\,(m \mid C[e] \gg k@\mathcal{P})}$$

$$\frac{context \text{ RH } C[e] \qquad (\mathcal{P}, m, e) \Rightarrow_{\text{RH}} (\mathcal{P}', m', e')}{\mathcal{E}\,(m \mid C[e] \gg k@\mathcal{P}) \longrightarrow \mathcal{E}\,(m' \mid C[e] \gg k@\mathcal{P}')}$$

E.5 Expression Rules

$$\frac{le[id] = (l, \_, pt, ty)}{Evar\ id \Rightarrow_{\text{LH}} Eloc\ l@pt}$$

$$\frac{le[id] = \bot \ ge[id] = \text{VAR}(l, \_, pt, ty)}{Evar\ id \Rightarrow_{\text{LH}} Eloc\ l@pt}$$

$$\frac{le[id] = \bot \ ge[id] = \text{VAR}(f, pt)}{Evar\ id \Rightarrow_{\text{LH}} Efloc\ l@pt}$$

$$\frac{}{(\mathcal{P}, m, Ederef\ (Eval\ v@vt)) \Rightarrow_{\text{RH}} (\mathcal{P}, m, Eloc\ (to\_ptr\ v)@vt)}$$

$$\frac{ty = TStruct\ id \lor ty = TUnion\ id\ offset\ id\ fld = \delta\ pt' \leftarrow \textbf{FieldT}(pt, TN(ty), GN(x))}{Efield\ (Eval\ p@pt : ty)\ fld) \Rightarrow_{\text{LH}} Eloc\ (p + \delta)@pt'}$$

$$\frac{m[l]_{|ty|} = v@vt@\overline{lt} \qquad vt' \leftarrow \textbf{LoadT}(\mathcal{P}, pt, vt, \overline{lt})}{(\mathcal{P}, m, EvalOf\ (Eloc\ l@pt) : ty) \Rightarrow_{\text{RH}} (\mathcal{P}, m, Eval\ v@vt')}$$

$$\frac{}{(\mathcal{P}, m, EaddrOf\ (Eloc\ p@pt)) \Rightarrow_{\text{RH}} (\mathcal{P}, m, Eval\ p@pt)}$$

$$\frac{\langle\odot\rangle\,v = v' \qquad vt \leftarrow \textbf{UnopT}(\odot, \mathcal{P}, vt)}{(\mathcal{P}, m, Eunop\ \odot\ (Eval\ v@vt)) \Rightarrow_{\text{RH}} (\mathcal{P}, m, Eval\ v'@vt')}$$

$$\frac{v_1\,\langle\oplus\rangle\,v_2 = v'\ \ vt' \leftarrow \textbf{BinopT}(\oplus, \mathcal{P}, vt_1, vt_2)}{e = Ebinop\ \oplus\ (Eval\ v_1@vt_1)\ (Eval\ v_2@vt_2)} \\ \overline{(\mathcal{P}, m, e) \Rightarrow_{\text{RH}} (\mathcal{P}, m, Eval\ v'@vt')}$$

$$\frac{\neg isptr(ty_1)\ \neg isptr(ty_2) \qquad pt \leftarrow \textbf{IICastT}(\mathcal{P}, vt_1)}{(\mathcal{P}, m, Ecast\ (Eval\ v@vt : ty_1)\ ty_2) \Rightarrow_{\text{RH}} (\mathcal{P}, m, Eval\ v@vt' : ty_2)}$$

$$\frac{ty_1 = ptr\ ty_1' \qquad\qquad \neg isptr(ty_2)}{m[v]_{|ty_1'|} = \_@vt@\overline{lt} \qquad vt \leftarrow \textbf{PICastT}(\mathcal{P}, pt, \boxed{vt, \overline{lt}})} \\ \overline{(\mathcal{P}, m, Ecast\ (Eval\ v@pt : ty_1)\ ty_2) \Rightarrow_{\text{RH}} (\mathcal{P}, m, Eval\ v@vt' : ty_2)}$$

$$\frac{\neg isptr(ty_1) \qquad\qquad ty_2 = ptr\ ty_2'}{m[v]_{|ty_2'|} = \_@vt_2@\overline{lt} \qquad pt \leftarrow \textbf{IPCastT}(\mathcal{P}, vt_1, \boxed{vt_2, \overline{lt}})} \\ \overline{(\mathcal{P}, m, Ecast\ (Eval\ v@vt_1 : ty_1)\ ty_2) \Rightarrow_{\text{RH}} (\mathcal{P}, m, Eval\ v@pt : ty_2)}$$

$$\frac{ty_1 = ptr\ ty_1' \qquad\qquad ty_2 = ptr\ ty_2'}{m[v]_{|ty_1'|} = m[v]_{|ty_2'|} = \_@vt@\overline{lt}\ \ pt' \leftarrow \textbf{PPCastT}(\mathcal{P}, pt, \boxed{vt, \overline{lt}})} \\ \overline{(\mathcal{P}, m, Ecast\ (Eval\ v@pt : ty_1)\ ty_2) \Rightarrow_{\text{RH}} (\mathcal{P}, m, Eval\ v@pt' : ty_2)}$$

$$\frac{boolof(v) = \mathbf{t} \qquad \mathcal{P}' \leftarrow \mathbf{ExprSplitT}(\mathcal{P}, vt)}{(\mathcal{P}, m, EseqAnd \ (Eval \ v@vt) \ e) \Rightarrow_{\mathrm{RH}} (\mathcal{P}', m, Eparen \ e \ Tbool \ \mathcal{P})}$$

$$\frac{boolof(v) = \mathbf{f} \qquad \mathcal{P}' \leftarrow \mathbf{ExprSplitT}(\mathcal{P}, vt)}{(\mathcal{P}, m, EseqAnd \ (Eval \ v@vt) \ e) \Rightarrow_{\mathrm{RH}} (\mathcal{P}', m, Eparen \ (Eval \ 0@vt') \ Tbool \ \mathcal{P})}$$

$$\frac{boolof(v) = \mathbf{t} \qquad \mathcal{P}' \leftarrow \mathbf{ExprSplitT}(\mathcal{P}, vt)}{(\mathcal{P}, m, EseqOr \ (Eval \ v@vt) \ e) \Rightarrow_{\mathrm{RH}} (\mathcal{P}', m, Eparen \ (Eval \ 1@vt') \ Tbool \ \mathcal{P})}$$

$$\frac{boolof(v) = \mathbf{f} \qquad \mathcal{P}' \leftarrow \mathbf{ExprSplitT}(\mathcal{P}, vt)}{(\mathcal{P}, m, EseqOr \ (Eval \ v@vt) \ e) \Rightarrow_{\mathrm{RH}} (\mathcal{P}', m, Eparen \ e \ Tbool \ \mathcal{P})}$$

$$\frac{e' = \begin{cases} e_1 & \text{if } boolof(v) = \mathbf{t} \\ e_2 & \text{if } boolof(v) = \mathbf{f} \end{cases} \qquad \mathcal{P}' \leftarrow \mathbf{ExprSplitT}(\mathcal{P}, vt)}{(\mathcal{P}, m, Econd \ (Eval \ v@vt) \ e_1 \ e_2) \Rightarrow_{\mathrm{RH}} (\mathcal{P}', m, Eparen \ e' \ \ \mathcal{P})}$$

$$\frac{m[l]_{|ty|} = v_1@vt_1@\overline{lt} \qquad m' = m[l \mapsto v_2@vt'@\overline{lt}'] }{\begin{array}{c} \mathcal{P}', vt', \overline{lt}' \leftarrow \mathbf{StoreT}(\mathcal{P}, pt, vt_1, vt_2, \overline{lt}) \\ \hline (\mathcal{P}, m, Eassign \ (Eloc \ l@pt) \ (Eval \ v_2@vt_2)) \Rightarrow_{\mathrm{RH}} (\mathcal{P}', m', Eval \ v_2@vt_2) \end{array}}$$

$$\frac{m[l]_{|ty|} = v_1@vt@\overline{lt} \ \oplus \in \{+, -, *, /, \%, <<, >>, \&, {}^\wedge, |\} \ vt' \leftarrow \mathbf{LoadT}(\mathcal{P}, pt, vt, \overline{lt})}{\begin{array}{c} e = Eassign \ (Eloc \ l@pt) \ (Ebinop \ \oplus \ (Eval \ v_1@vt') \ (Eval \ v_2@vt_2)) \\ \hline (\mathcal{P}, m, EassignOp \ \oplus \ (Eloc \ l@pt) \ (Eval \ v_2@vt_2)) \Rightarrow_{\mathrm{RH}} (\mathcal{P}, m, e) \end{array}}$$

$$\frac{\begin{array}{c} m[l] = v@vt@\overline{lt} \ \oplus \in \{+, -\} \qquad vt' \leftarrow \mathbf{LoadT}(\mathcal{P}, pt, vt, \overline{lt}) \\ e = Ecomma \ (Eassign \ (Eloc \ l@pt) \ (Ebinop \ \oplus \ Eval \ v@vt' \ 1@def)) \ (Eval \ v@vt') \end{array}}{(\mathcal{P}, m, EpostInc \ \oplus \ Eloc \ l@pt) \Rightarrow_{\mathrm{RH}} (\mathcal{P}, m, e)}$$

$$\frac{}{(\mathcal{P}, m, Ecomma \ (Eval \ v@vt) \ e) \Rightarrow_{\mathrm{RH}} (\mathcal{P}, m, e)}$$

$$\frac{\mathcal{P}'', vt' \leftarrow \mathbf{ExprJoinT}(\mathcal{P}, \mathcal{P}', vt)}{(\mathcal{P}, m, Eparen \ e \ ty \ \mathcal{P}') \Rightarrow_{\mathrm{RH}} (\mathcal{P}'', m, Eval \ v@vt')}$$

E.6 Call and Return Rules

In order to make a call, we need to reduce the function expression to an *Efloc* _@ value, an abstract location corresponding to a particular function. Then we can make the call.

$$\frac{\mathcal{P}' \leftarrow \mathbf{CallT}(\mathcal{P}, \mathrm{F}_f, \mathrm{F}_{f'})}{\mathcal{E}\left(m \mid C\big[Ecall \ Efloc \ f'@(\overline{v@vt})\big] \ ty \gg k@\mathcal{P}\right) \longrightarrow \mathcal{C}\left(m \mid f'(\overline{v@vt}) \gg Kcall \ f \ C \ \mathcal{P}; \ k@\mathcal{P}'\right)}$$

When we make an internal call, we need to allocated space for locals and arguments using the helper function *frame*.

$$\textit{frame } xs \textit{ as } m = \begin{cases} (m''[p \mapsto \textbf{undef}@vt@\overline{lt}]_{|ty|}, & \text{if } xs = (id, ty) :: xs' \\ \quad le'[id \mapsto (p, p + |ty|, ty, pt)]) & \text{where } (m', p) \leftarrow \textit{stack\_alloc } |ty| \ m, \\ & \quad \mathcal{P}', pt, vt, \overline{lt} \leftarrow \textbf{LocalT}(\mathcal{P}, \textsc{t}_{ty}), \\ & \quad \text{and } (m'', le') = \textit{frame } xs' \textit{ as } m' \\ \\ (m''[p \mapsto v@vt'@\overline{lt}]_{|ty|}, & \text{if } as = (id, ty, v@vt) :: as' \text{ and } xs = \varepsilon \\ \quad le'[id \mapsto (p, p + |ty|, ty, pt)]) & \text{where } (m', p) \leftarrow \textit{stack\_alloc } |ty| \ m, \\ & \quad \mathcal{P}', pt, vt', \overline{lt} \leftarrow \textbf{ArgT}(\mathcal{P}, vt, \textsc{a}_{f,x}, \textsc{t}_{ty}), \\ & \quad \text{and } (m'', le') = \textit{frame } xs' \textit{ as } m' \\ \\ (m, \lambda x.\bot) & \text{if } xs = \varepsilon \text{ and } as = \varepsilon \end{cases}$$

$$\frac{\textit{def}(f) = INT(xs, as, s) \ m', le' = \textit{frame } xs \ (\textit{zip } as \ \textit{args}) \ m \ le}{\mathcal{C}\,(m \mid f(\textit{args}) \gg k@\mathcal{P}) \longrightarrow \mathcal{S}\,(m' \mid s \gg k@\mathcal{P})\,/le'}$$

On the other hand, when we make an external call, we step directly to a return state with some value being returned and an updated memory. [TODO: talk more about how the tag policy applies in external functions, what they can and can't do with tags.]

$$\frac{\textit{def}(f) = EXT(\textit{spec}) \ \mathcal{P}' \leftarrow \textbf{ExtCallT}(\mathcal{P}, \textsc{f}_f, \textsc{f}_{f'}, \overline{vt}) \ \mathcal{P}'', m', (v@vt) = \textit{spec } \mathcal{P}' \textit{ args } m}{\mathcal{C}\,(m \mid f(\textit{args}) \gg k@\mathcal{P}) \longrightarrow \mathcal{R}\,(m' \mid v@vt \gg k@\mathcal{P}'')}$$

Special external functions, such as malloc, just get their own rules.

$$\frac{\mathcal{P}', pt, \boxed{vt, \overline{lt}} \leftarrow \textbf{MallocT}(\mathcal{P}, \textsc{f}_f, \textsc{f}_{f'}, vt) \ m', p \leftarrow \textit{heap\_alloc size } m \quad m'' = m'\,[p + i \mapsto (\textbf{undef}, vt, lt)]_{size}}{\mathcal{C}\,(m \mid \textit{malloc}((\textit{size}@t)) \gg k@\mathcal{P}) \longrightarrow \mathcal{R}\,(m'' \mid \textit{Eval } p@pt \gg k@\mathcal{P}')}$$

And finally, we have the return rules.

$$\frac{k = Kcall \ le' \ ctx \ \mathcal{P}_{CLR} \ k' \qquad \mathcal{P}', vt' \leftarrow \textbf{RetT}(\mathcal{P}_{CLE}, \mathcal{P}_{CLR}, vt, \textsc{f}_f)}{\mathcal{R}\,(m \mid \textit{Eval } v@vt \gg k@\mathcal{P}_{CLE}) \longrightarrow \mathcal{E}\,(m \mid ctx[\textit{Eval } v@vt'] \gg k'@\mathcal{P}')\,/le'}$$

$$\frac{\textit{dealloc } m \ \mathcal{P} = (\mathcal{P}', m')}{\mathcal{E}\,(m \mid \textit{Eval } v@vt \gg Kreturn;\ k@\mathcal{P}) \longrightarrow \mathcal{R}\,(m \mid \textit{Eval } v@vt \gg k@\mathcal{P}')}$$

$$\frac{\textit{dealloc } m \ \mathcal{P} = (\mathcal{P}', m')}{\mathcal{S}\,(m \mid \texttt{Sreturn} \gg k@\mathcal{P}) \longrightarrow \mathcal{R}\,(m' \mid \textit{Eval } \textbf{undef}@\textit{def} \gg k@\mathcal{P}')}$$

F Moved from Intro

[SNA: I'm organizing our diss tracks into paragraphs that we can cut or move as needed]

Why Dynamic? Unfortunately, it is not always possible to fully secure C code before run-time. Ideally, bugs would be quickly identified and then fixed promptly. That is not always possible for a variety of reasons: bugs may escape detection, require significant effort to diagnose, or be impractical to fix. There are many techniques for finding bugs, but there is a shared stumbling block: C is not well defined. We cannot always agree on when something is a bug in C, especially code using Undefined Behaviors (UB) [**?**]. Confusion around expected behavior is no small problem. There are 191 undefined behaviors and 52 unspecified behaviors in the C99 specification [**?**]. Sometimes these behaviors are benign and skillfully used by the developer, other times they are unintended and highly dangerous. Unfortunately the distinction between the two is easily lost. Discerning expert code review is considered best practice, although it is rarely perfectly success-ful [] even if an expert is available at all. Even when there is both consensus and detection of a bug[APT: ??] AN: we can find it at and we can agree its a problem that should be fixed, changing the code may not be possible because it is in proprietary 3rd party libraries and drivers, or because regulations prohibit changes [3].

[APT: last clause is mysterious] AN: for example FDA approval used to forbid patching because you'd have to go through recertification. So healthcare wouldn't patch. SNA pointed out the coverity paper comments on this as a reason for bugs not getting fixed

Why C-Level? Tag-based enforcement in general has a significant body of work at the assembly level, especially PIPE (Programmable Interlocks for Policy Enforce-ment) []. However, even at the assembly-level these systems need the compiler to be in the trusted computing base (TCB), as many policies require knowledge of source-level constructs, even ones that do not depend on detailed knowledge of the program's behavior [cite Nick and Andre; anyone else?]. Moving policy-definition to the source level therefore does not expand the TCB and allows C developers to reason about policies in terms of the language that they program in regularly.

Notations Values are ranged over by v, variable identifiers by x, and function identifiers by f. Tags use a number of metavariables: t ranges over all tags, while we will use vt to refer to the tags associated with values, pt for tags on pointer values and memory-location expressions, lt for tags associated with memory locations themselves, nt for "name tags" automatically derived from identifiers, $\mathcal{P}$ for the global "program counter tag" or PC Tag. An *atom* is a pair of a value and a tag, *Eval* $v@vt$; the @ symbol should be read as a pair in general, and is used when the second object in the pair is a tag. Expressions are ranged over by e, statements by s, and continuations by k. The continuations are defined in appendix B, and step rules in appendix E.

A memory is an array of bytes, where each byte is part of an atom. Each byte is also associated with a "location tag" lt. When a contiguous region of s bytes starting at location l comprise an atom $v@vt$, and their locations tags comprise the list $\overline{lt}$, we write $m[l]_s = v@vt@\overline{lt}$. Likewise, $m[l \ldots l+s \mapsto v@vt@\overline{lt}]_s$ denotes

storing that many bytes. Visually, we will represent whole atoms in memory as condensed boxes, with their location tags separate.