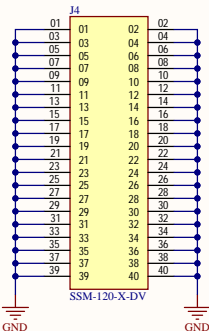
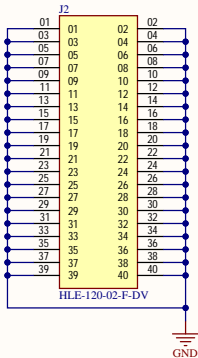



<SCH DWG NO> DWG. NO.		1 REV		2		1	
		REVISION	DESCRIPTION			DATE	APPROVED

Pinout diagram for the HLE-120-02-F DV connector. The diagram shows a 40-pin connector with pins numbered 01 to 40. The pins are organized into two columns: pins 01-20 on the left and pins 02-40 on the right. The signals are as follows:

Pin	Signal
01	VSPICLK
02	VCC
03	VSPDQ
04	VCC
05	VSPDQ
06	VCC
07	VSPIC50
08	VCC
09	SCK
10	VCC
11	SDA
12	VCC
13	GPIO33
14	VCC
15	GPIO34
16	VCC
17	GPIO35
18	VCC
19	GPIO36
20	VCC
21	GPIO39
22	VCC
23	GPIO39
24	VCC
25	GPIO25
26	VCC
27	GPIO26
28	VCC
29	GPIO27
30	VCC
31	VCC
32	VCC
33	VCC
34	VCC
35	VCC
36	VCC
37	VCC
38	VCC
39	VCC
40	VCC

The connector is labeled HLE-120-02-F DV.



APPROVALS		DATE		PROJECT		 2175 SALK AVENUE SUITE 200 CARLSBAD, CA 92008 USA	
ENG:	*	-/-/-		*			
DSN:	*	-/-/-		PROJECT REVISION:		DOCUMENT REVISION:	
CHK:	*	-/-/-		TITLE		005d672f6c8ab99059966a3bc84c25bfbe	
REFERENCE DOCUMENTS				*			
BOM:				<BOM DOC NO>			
ASSY DWG:				SIZE		REV	
<ASSY DWG NO>				B		<SCH DWG NO>	
FAB DWG:				CAGE CODE			
<FAB DWG NO>				?????			
PCB DWG:				FILE NAME		SHEET 1 OF 1	
<PCB DWG NO>				FlashBoard_SchDoc			

Line #	Name	Description	Designator	Quantity	Manufacturer 1	Manufacturer Part Number 1	Manufacturer Lifecycle 1	Supplier 1	Supplier Part Number 1	Supplier Unit Price 1	Supplier Subtotal 1
	Cap Semi	Capacitor (Semiconductor SIM Model)	C1	1							
	W25Q128JV SIM_TR	Integrated Circuit	IC1	1							
	HLE-120-02- F-DV	40 Position Receptacle, Bottom Entry Connector Surface Mount	J1, J2	2							
	SSM-120-X- DV		J3, J4	2							