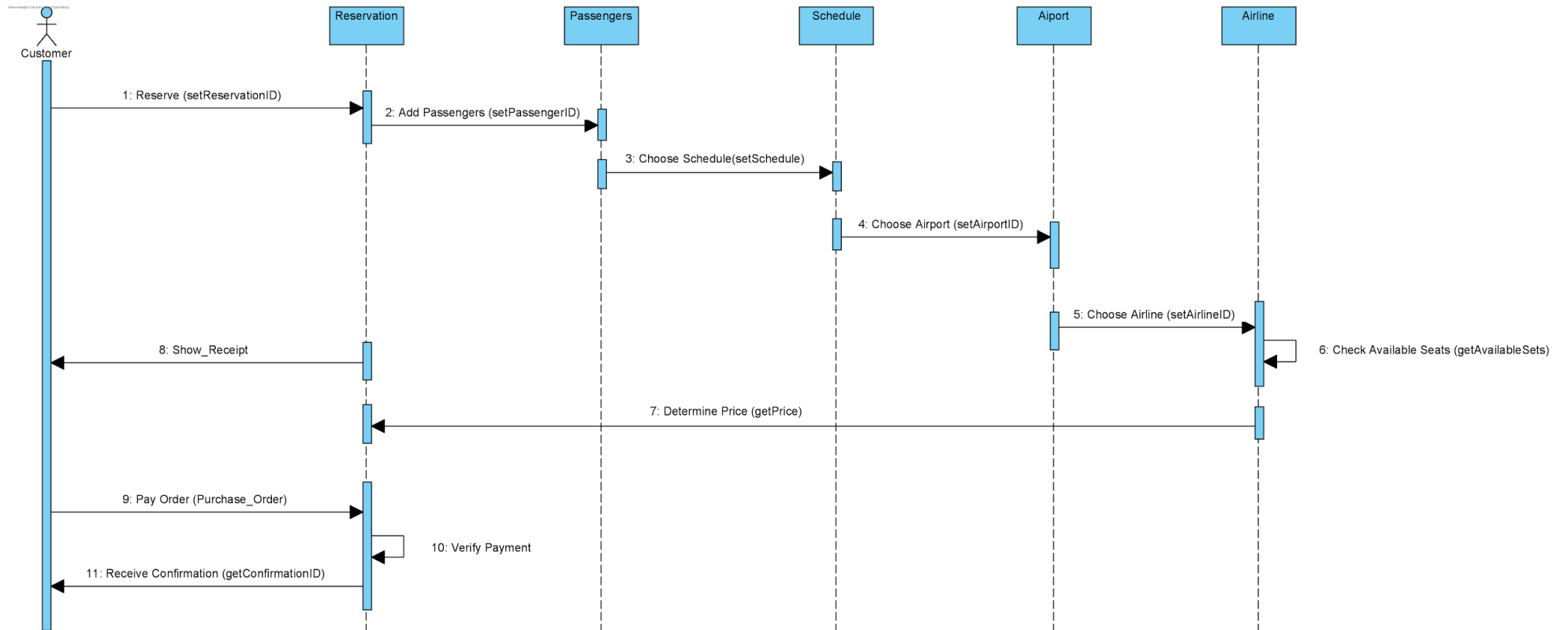


SYSTEM LEVEL sequence diagram



*CLASS LEVEL sequence diagram*