

### PROBLEM STATEMENT

To evaluate the efficacy of CUDA C for accelerating image processing algorithms compared to traditional serial CPU implementations.

## **METHODOLOGY**

#### **ENVIRONMENT SETUP:**

- ☐ Utilized google colab with a google T4 processor for GPU acceleration.
- ☐ Converted serial python code to a c binary using pyinstaller.
- ☐ Developed parallel cuda c code for efficient execution on the t4 processor.

#### **Benchmarking process:**

- ☐ Selected test images of varying resolutions.
- Executed serial and parallel versions of image processing algorithms (gaussian blur, dilation, and mean filtering).
- ☐ Ran each implementation multiple times (10 iterations) for accuracy.

### RUNTIME MEASUREMENT AND CALCULATION:

- ☐ Recorded start and end times using high-resolution timers.
- ☐ Calculated average execution times for each implementation and image combination.
- ☐ Quantified performance improvement using the speedup factor.

#### Data analysis and reporting:

- ☐ Compiled and analyzed data, including average execution times and speedup factors.
- ☐ Presented results visually with charts and graphs.
- ☐ Discussed findings, emphasizing the impact of algorithms, image sizes, and formats on execution times, along with limitations and suggestions for further exploration.

# INFRASTRUCTURE

The serial code:

- ☐ Made in python compiled to C binaries using pyinstaller
- ☐ Run on T4 processor in google colab
- ☐ Implemented in serial fashion

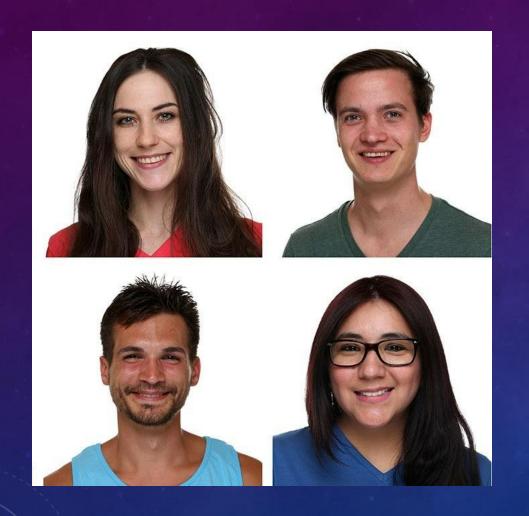
The parallel code:

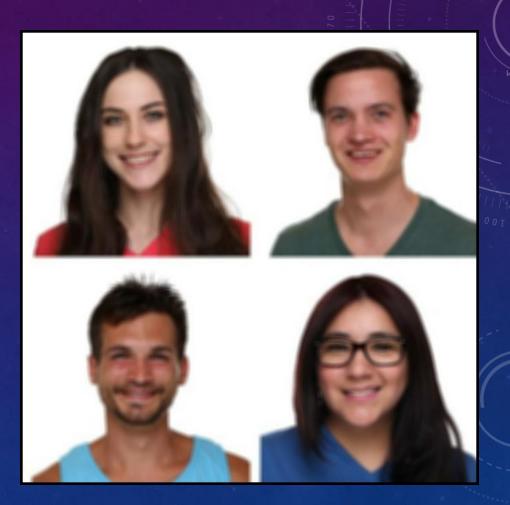
- ☐ Made in CUDA C
- ☐ Run on T4 processor in google colab
- ☐ Implemented using parallel computing techniques

### CUDA IMPLEMENTATION OF MEAN FILTER

```
#include "opencv2/imgproc/imgproc.hpp"
#include <opencv2/highqui.hpp>
#include <iostream>
#include <string>
#include <stdio.h>
#include <cuda.h>
#include "cuda runtime.h"
#define BLOCK SIZE
                        16
#define FILTER WIDTH
#define FILTER_HEIGHT
using namespace std;
__global__ void meanFilter(unsigned char *srcImage, unsigned char *dstImage, unsigned int width,
unsigned int height, int channel)
   int x = blockIdx.x*blockDim.x + threadIdx.x;
   int y = blockIdx.y*blockDim.y + threadIdx.y;
   if((x>=FILTER WIDTH/2) && (x<(width-FILTER WIDTH/2)) && (y>=FILTER HEIGHT/2) && (y<(height-
FILTER_HEIGHT/2)))
      for(int c=0 ; c<channel ; c++)</pre>
         unsigned char filterVector[FILTER_WIDTH*FILTER_HEIGHT];
         int sum = 0;
         for(int ky=0; ky<FILTER_HEIGHT; ky++) {</pre>
            for(int kx=0; kx<FILTER_WIDTH; kx++) {</pre>
                filterVector[ky*FILTER WIDTH+kx] = srcImage[((y+ky-FILTER HEIGHT/2)*width + (x+kx-
FILTER_WIDTH/2))*channel+c];
                sum += filterVector[ky*FILTER_WIDTH+kx];
         dstImage[(v*width+x)*channel+c] = sum / (FILTER WIDTH*FILTER HEIGHT);
```

## ORIGINAL VS RESULT OF MEAN FILTERING:





### CUDA IMPLEMENTATION OF DILATIONFILTER

```
__global__ void dilationFilter(unsigned char *srcImage, unsigned char *dstImage, unsigned int width,
unsigned int height, int channel)
    int x = blockIdx.x * blockDim.x + threadIdx.x;
    int y = blockIdx.v * blockDim.y + threadIdx.v;
    if (x < width && v < height)
        for (int c = 0; c < channel; c++)
            unsigned char maxPixel = 0; // Initialize maxPixel to the minimum possible value
            for (int ky = -FILTER_HEIGHT / 2; ky <= FILTER_HEIGHT / 2; ky++)</pre>
                for (int kx = -FILTER_WIDTH / 2; kx <= FILTER_WIDTH / 2; kx++)</pre>
                    int nx = x + kx;
                    int ny = y + ky;
                    if (nx >= 0 \&\& nx < width \&\& nv >= 0 \&\& nv < height)
                        unsigned char neighborPixel = srcImage[(ny * width + nx) * channel + c];
                        if (neighborPixel > maxPixel)
                            maxPixel = neighborPixel;
            dstImage[(y * width + x) * channel + c] = maxPixel;
```

# **ORIGINAL VS RESULT DILATION:**

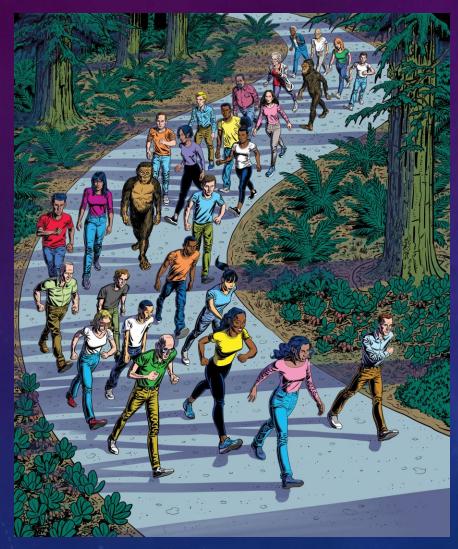


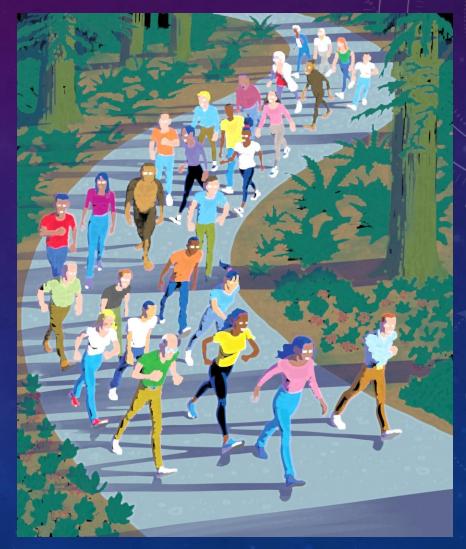


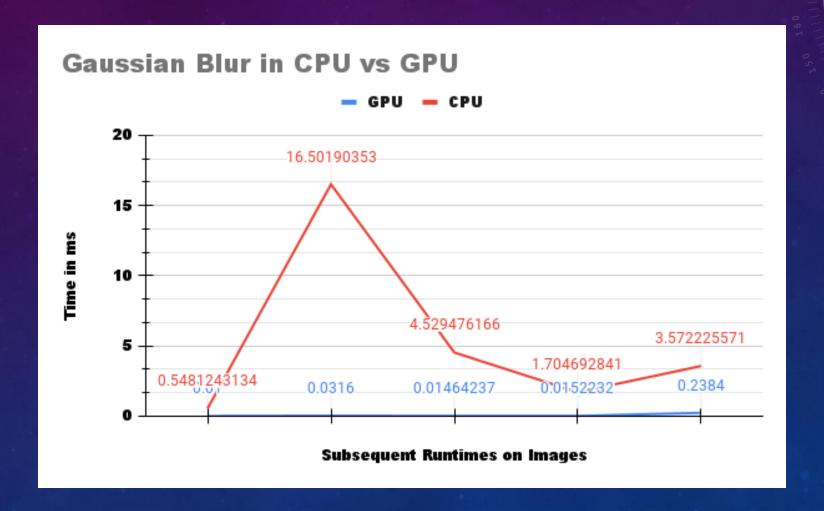
### CUDA IMPLEMENTATION OF GUSSIANFILTER

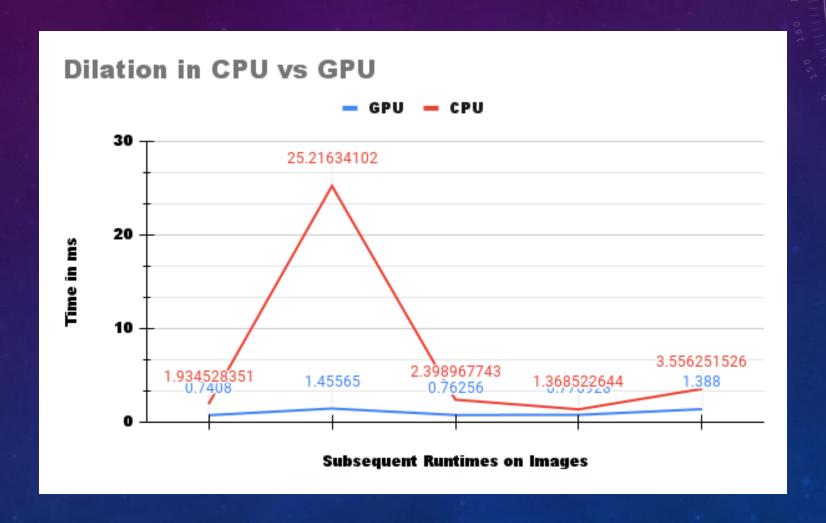
```
__global__ void gaussianFilter(unsigned char *srcImage, unsigned char *dstImage, unsigned int width,
unsigned int height, int channel, float sigma) {
    int x = blockIdx.x * blockDim.x + threadIdx.x;
    int y = blockIdx.y * blockDim.y + threadIdx.y;
    shared float gaussianKernel[FILTER WIDTH * FILTER HEIGHT];
   if (threadIdx.x < FILTER_WIDTH && threadIdx.y < FILTER_HEIGHT) {</pre>
        int index = threadIdx.v * FILTER WIDTH + threadIdx.x;
        gaussianKernel[index] = exp(-(threadIdx.x * threadIdx.x + threadIdx.y * threadIdx.y) / (2 *
sigma * sigma)) / (2 * M_PI * sigma * sigma);
   __syncthreads();
   if (x >= FILTER WIDTH / 2 && x < width - FILTER WIDTH / 2 && y >= FILTER HEIGHT / 2 && y < height -
FILTER_HEIGHT / 2) {
       for (int c = 0; c < channel; c++) {
            float sum = 0.0f;
            int index = 0;
            for (int ky = -FILTER_HEIGHT / 2; ky <= FILTER_HEIGHT / 2; ky++) {
                for (int kx = -FILTER WIDTH / 2; kx <= FILTER WIDTH / 2; kx++) {
                    int srcX = x + kx;
                    int srcY = y + ky;
                    sum += srcImage[(srcY * width + srcX) * channel + c] * gaussianKernel[index];
                    index++;
            dstImage[(y * width + x) * channel + c] = sum;
```

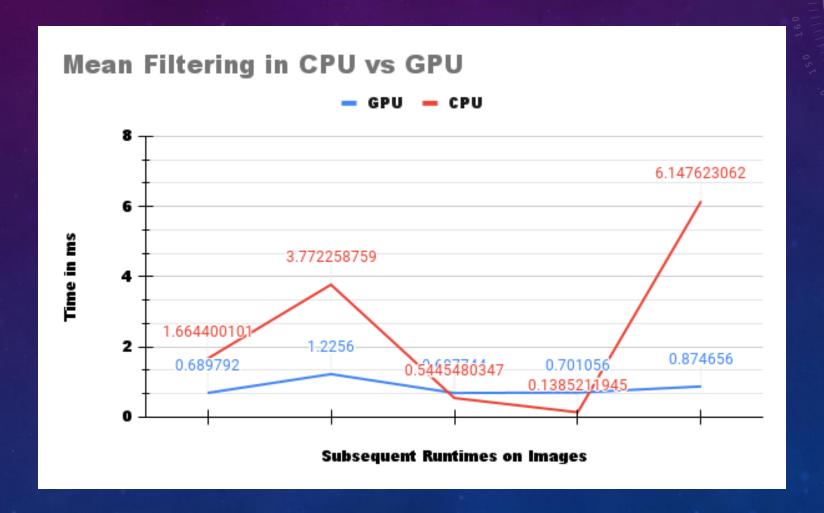
### ORIGINAL VS RESULT GAUSSIAN BLUR:

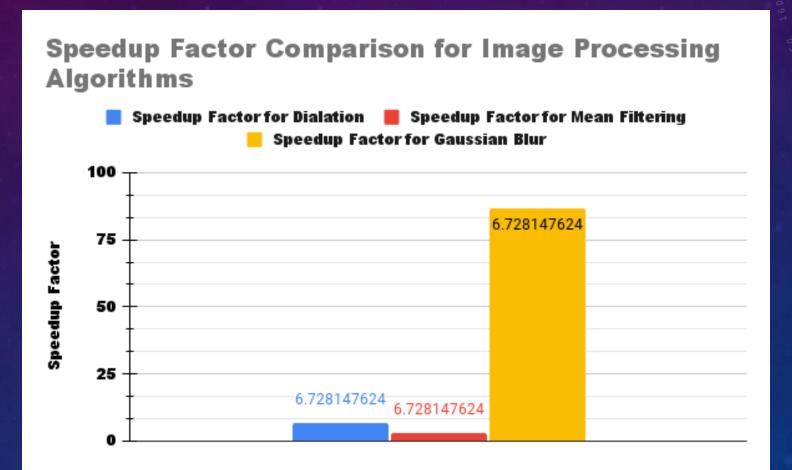












# REFERENCES

Xi Chen, Yuehong Qiu, and Hongwei Yi. Implementation and performance of image filtering on gpu. In 2013 Fourth International Conference on Intelligent Control and Information Processing (ICICIP), pages 514–517, 2013.
Yonny S. Izmantok. Time complexity measurement on cuda-based gpuparallel architecture of morphology operation. Korea Journal of Advanced Science and Technology, page 9, 2013.
Mugdha A. Rane. Fast morphological image processing on gpu using cuda. page 37, 2023.
Ferhat Bozkurt. Effective gaussian blurring process on graphics processing unit with cuda. International Journal of Machine Learning, 5(483):W012, 2023.
Mete Ya gano glu Ferhat Bozkurt and Faruk Baturalp Gunay. Effective gaussian blurring process on graphics processing unit with cuda. International Journal of Machine Learning, (483):1–5, 2024.
Mouna Afif. Efficient 2d convolution filters implementations on graphics processing unit using nvidia cuda. International Journal of Computer Science and Mobile Computing, 6(10):1–5,2020.
Munesh Chauhan. Optimizing gaussian blur filter using cuda parallel framework. International Journal of Computer Science and Mobile Computing, 6(10):1–5, 2018.
Nahla M. Ibrahim. Gaussian blur through parallel computing. page 5.
Department of Computer Science and Applications, Gandhigram Rural Institute –Deemed University Gandhigram, page 13, 2016.