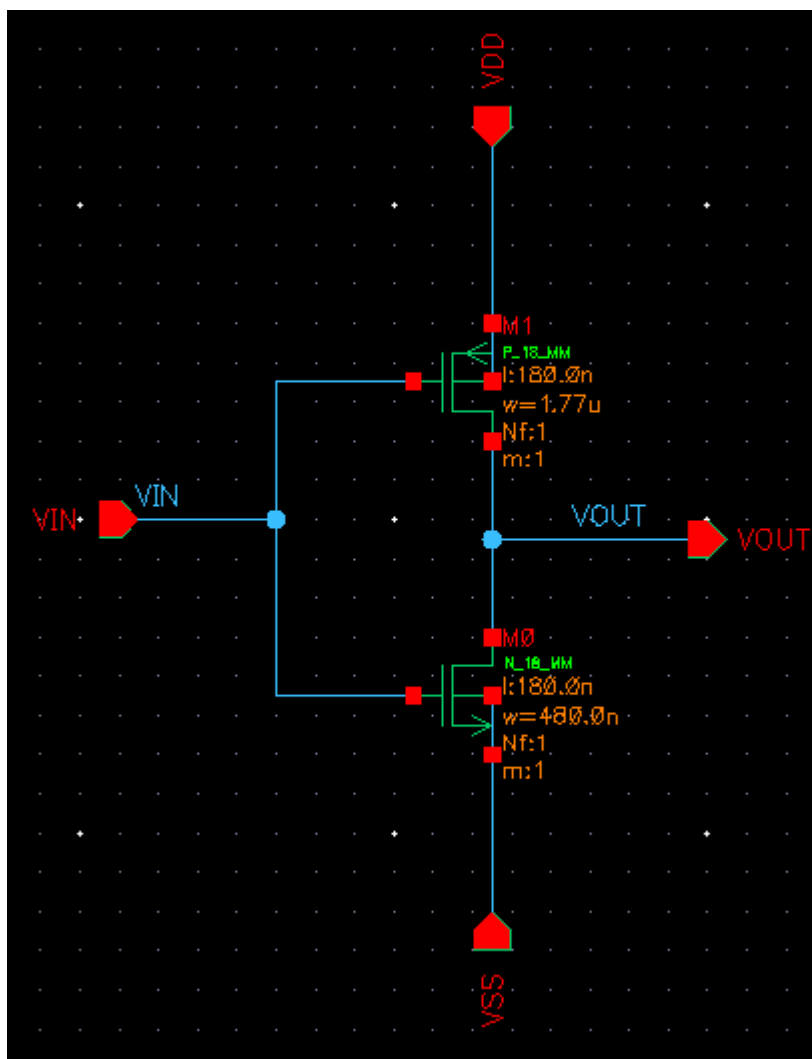
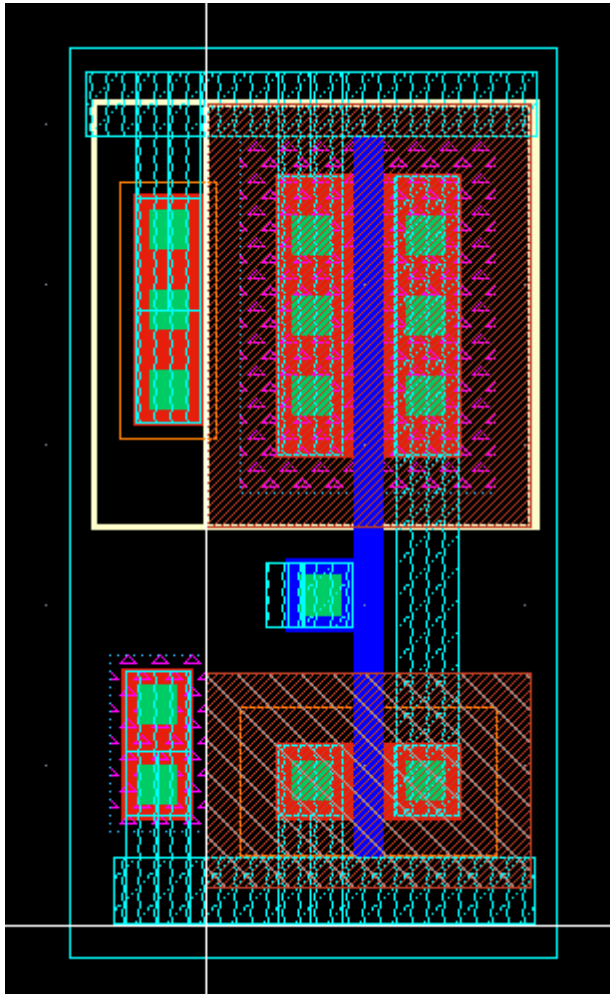


# 1.Inverter

Schematic:

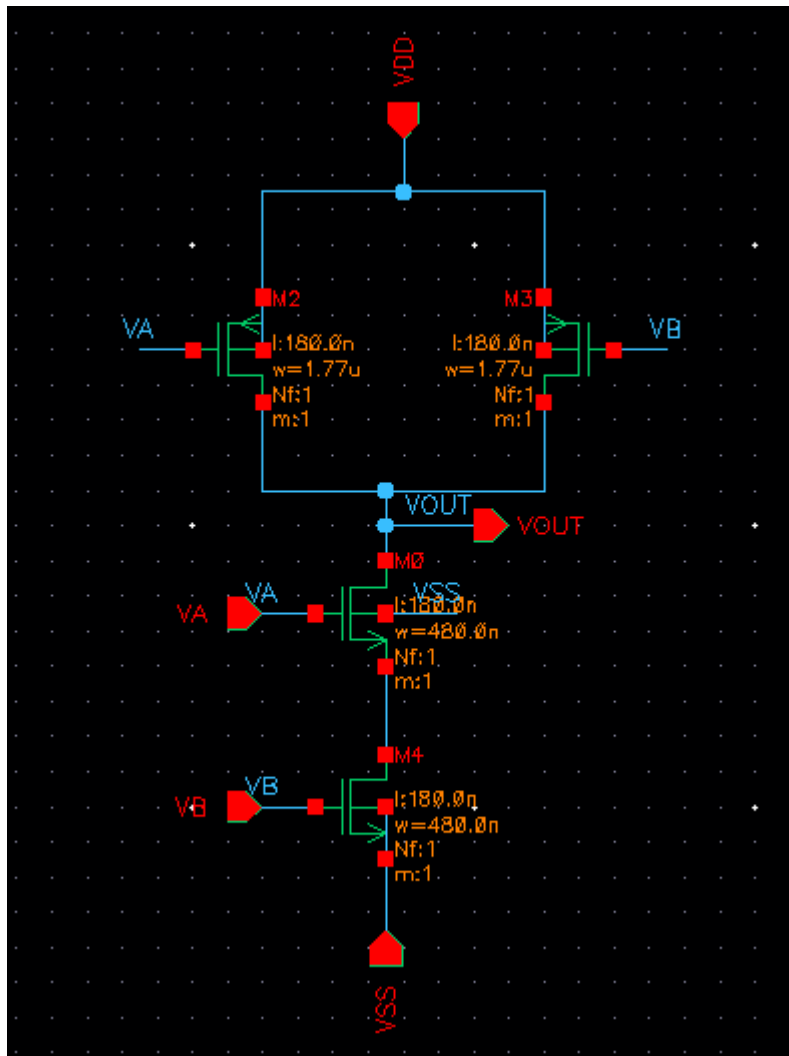


# Inverter Layout:

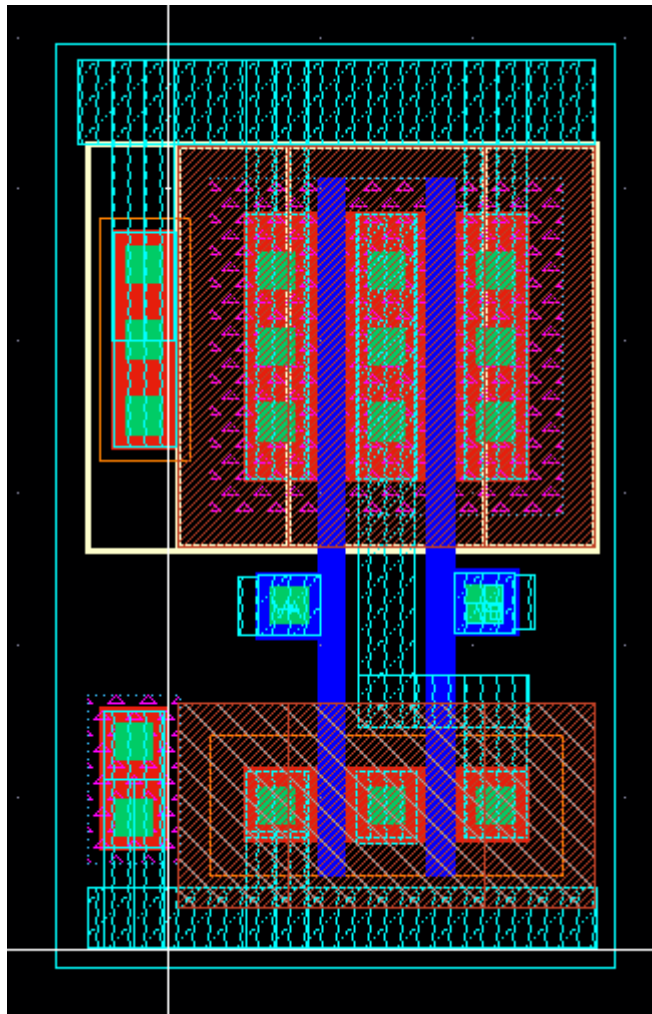


## 2.NAND

Schematic:

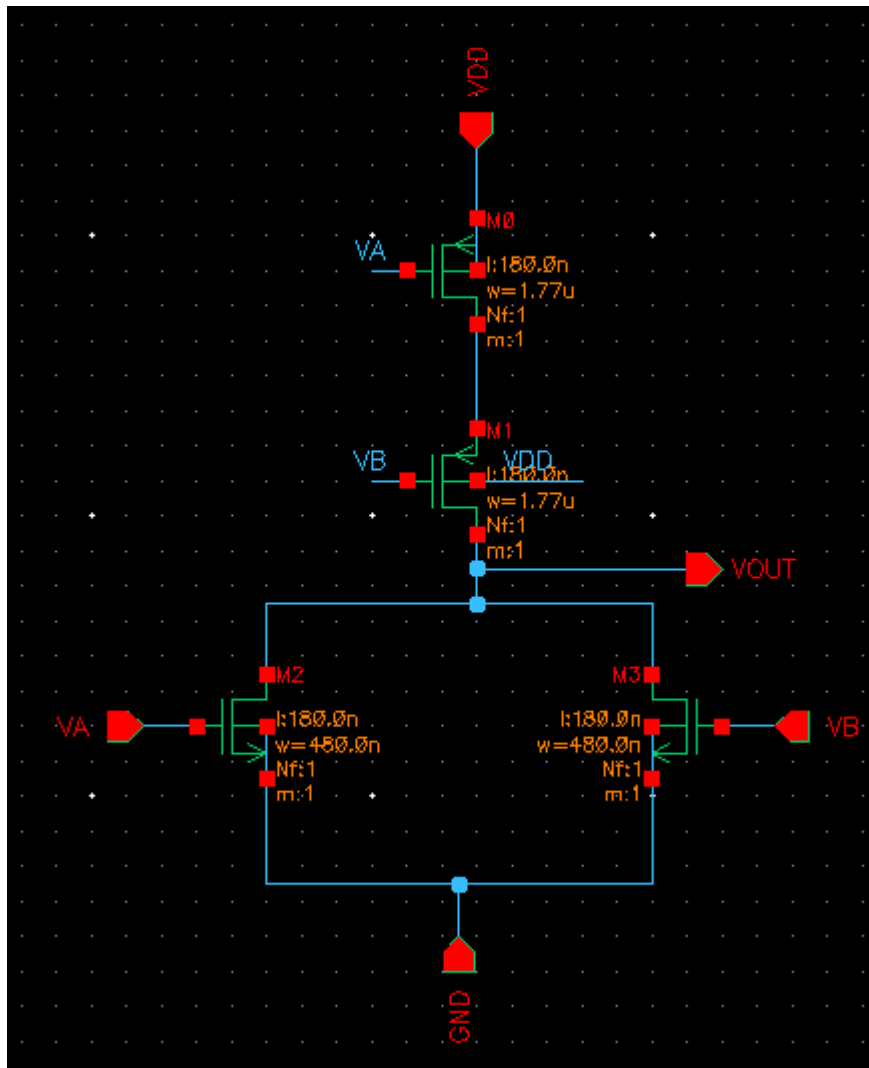


# NAND Layout:

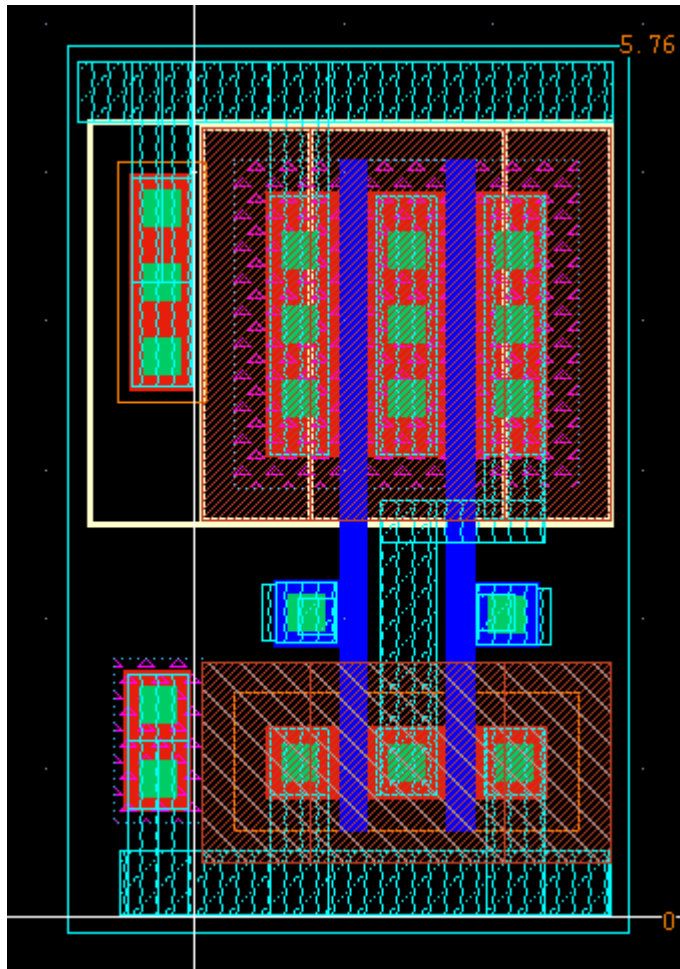


### 3.NOR

Schematic:

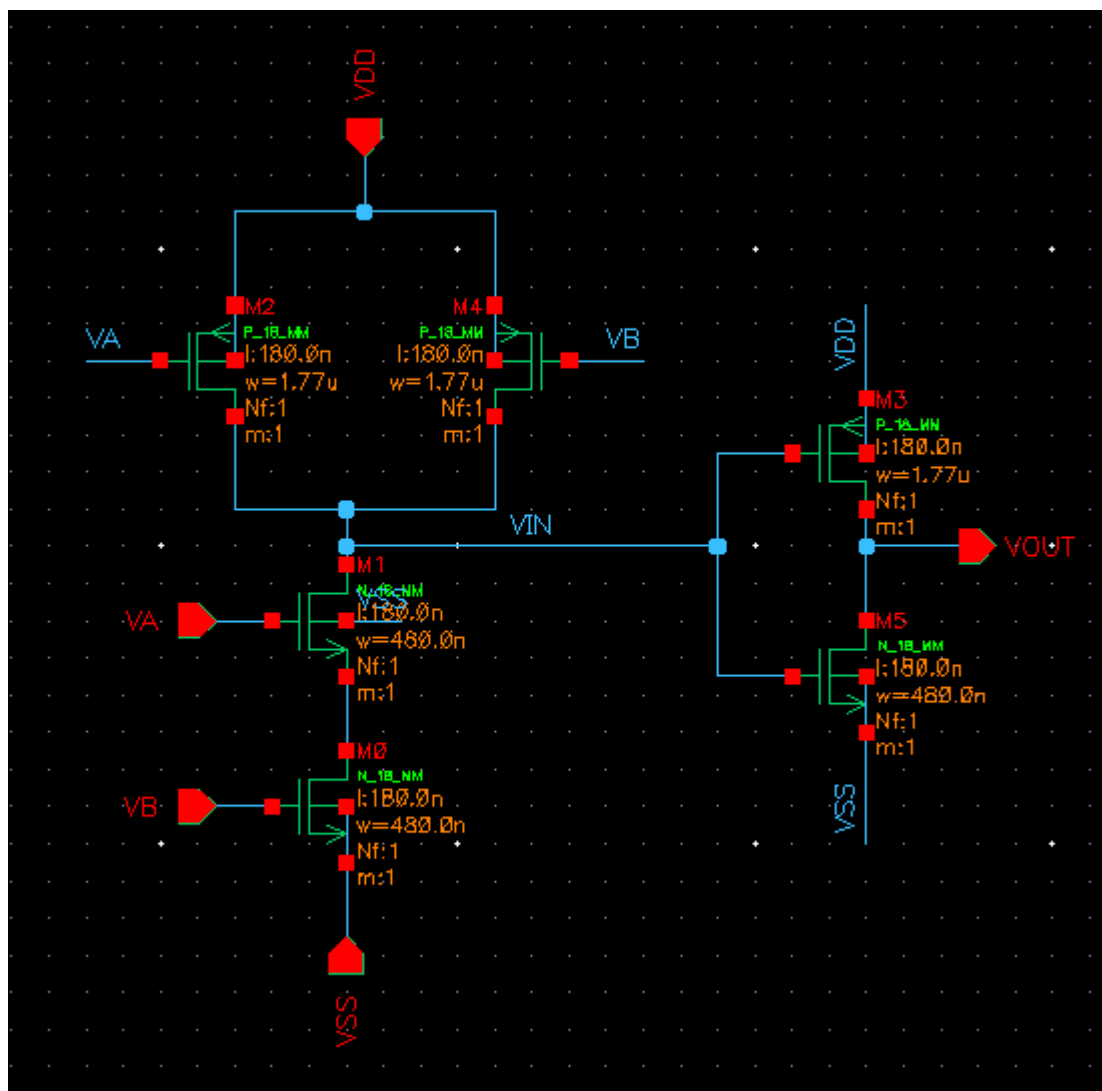


# NOR Layout:

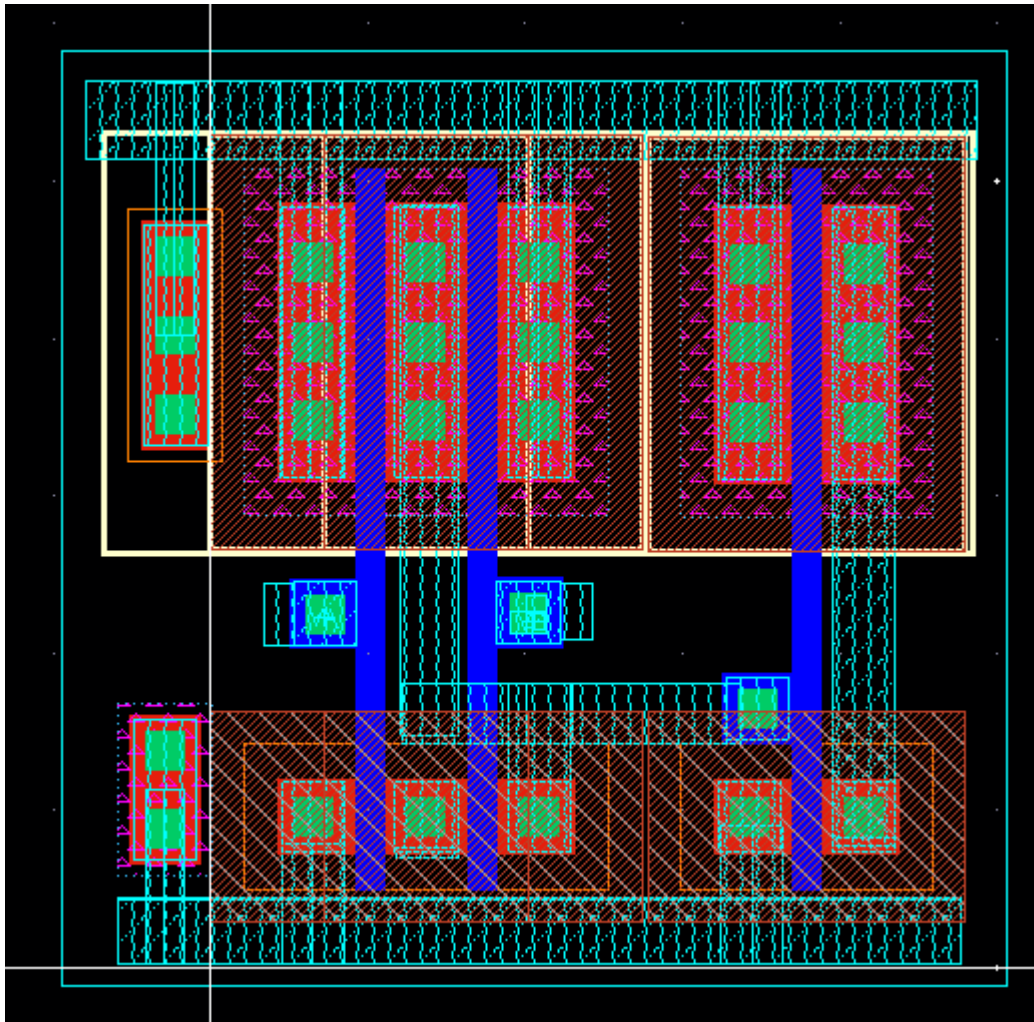


## 4.AND

Schematic:



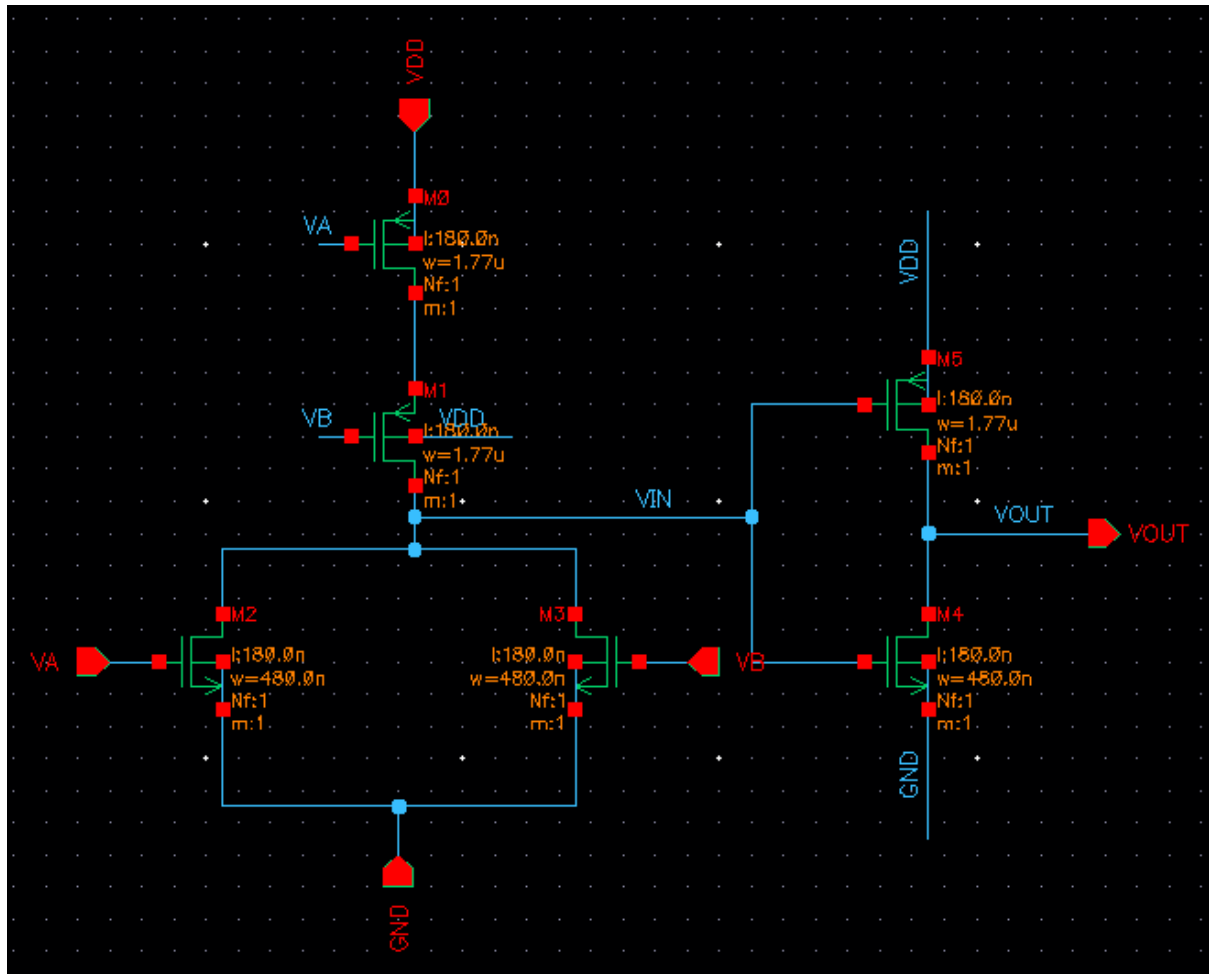
## AND Layout:



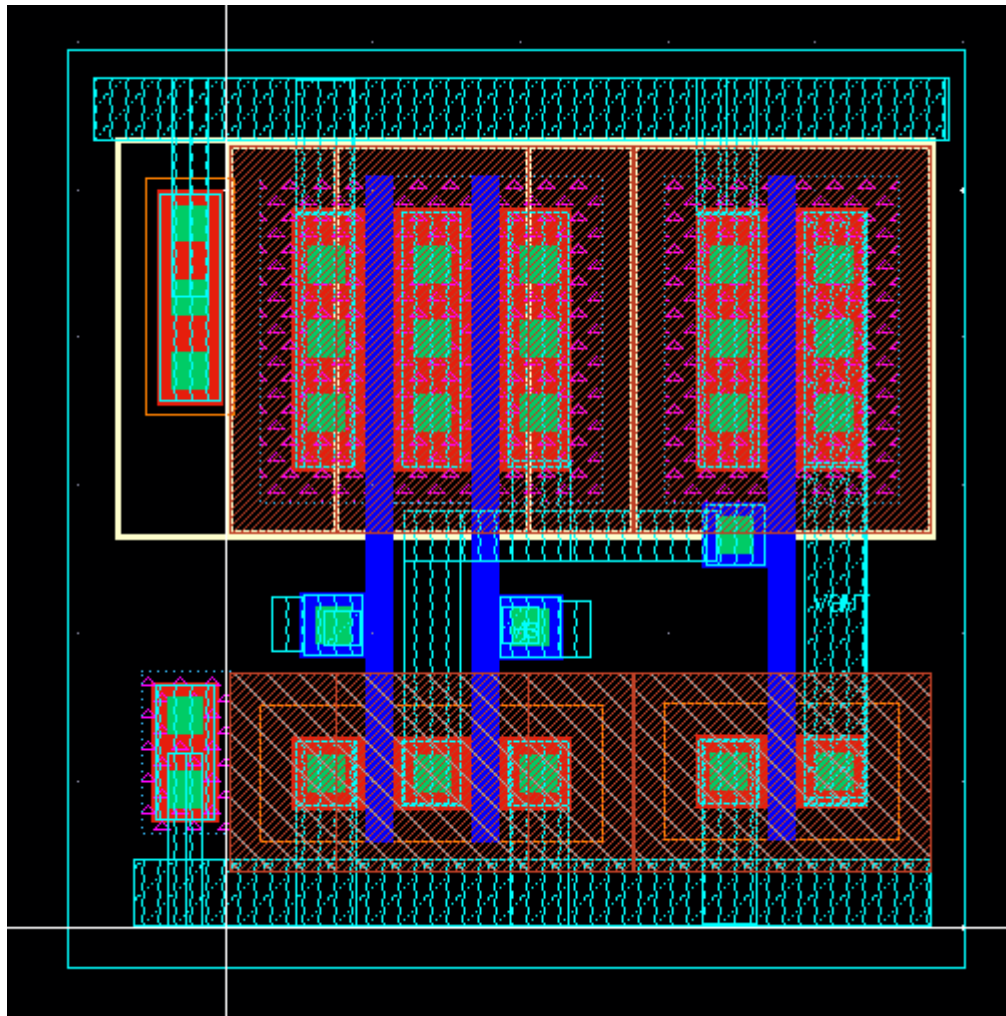


5.OR

Schematic:

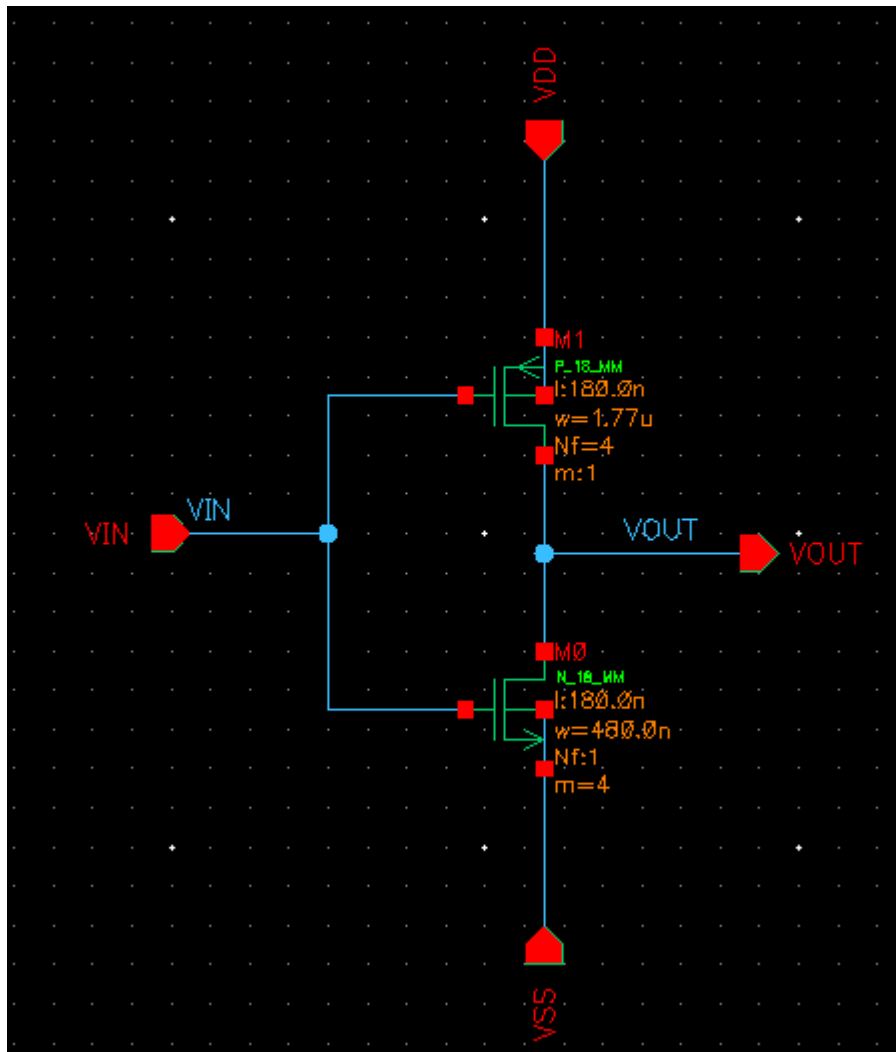


## OR Layout:

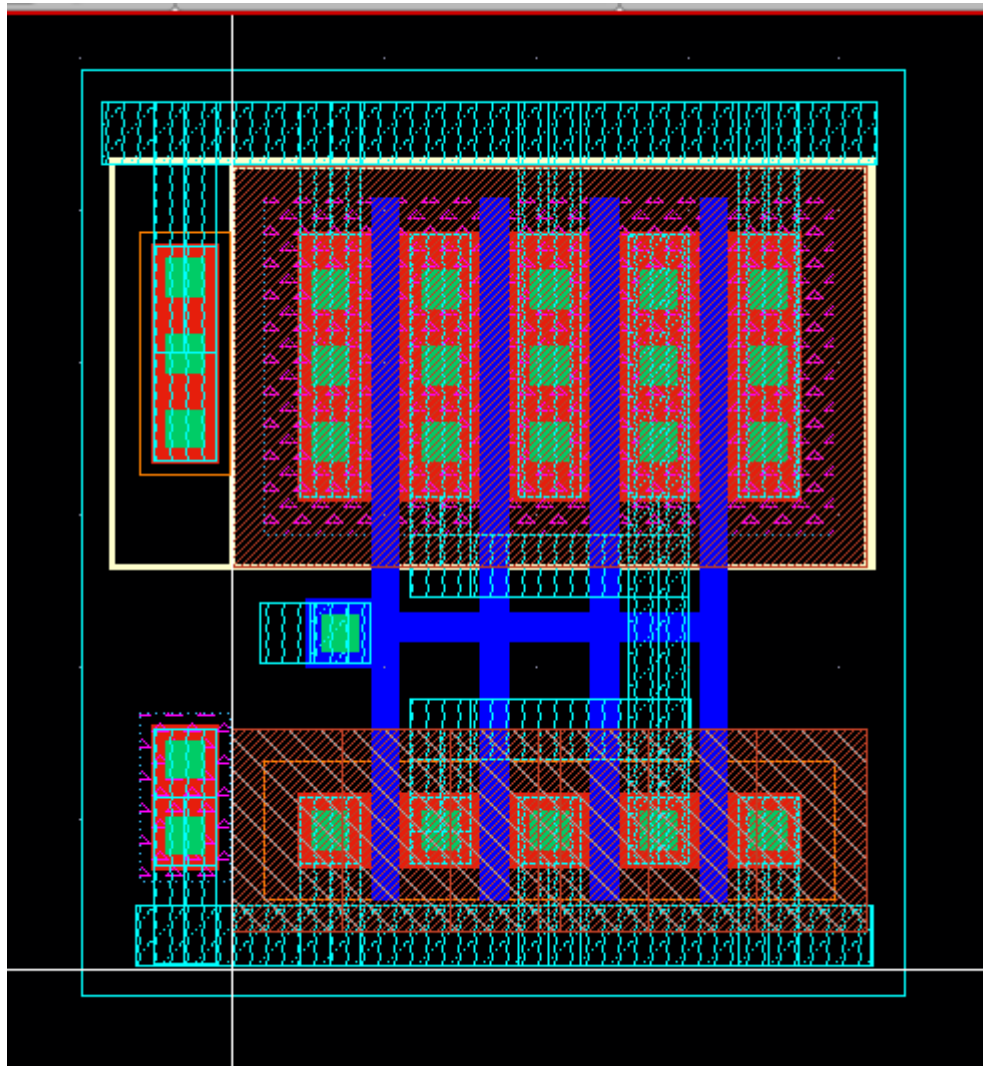


## 6. Inverter 4X

Schematic:

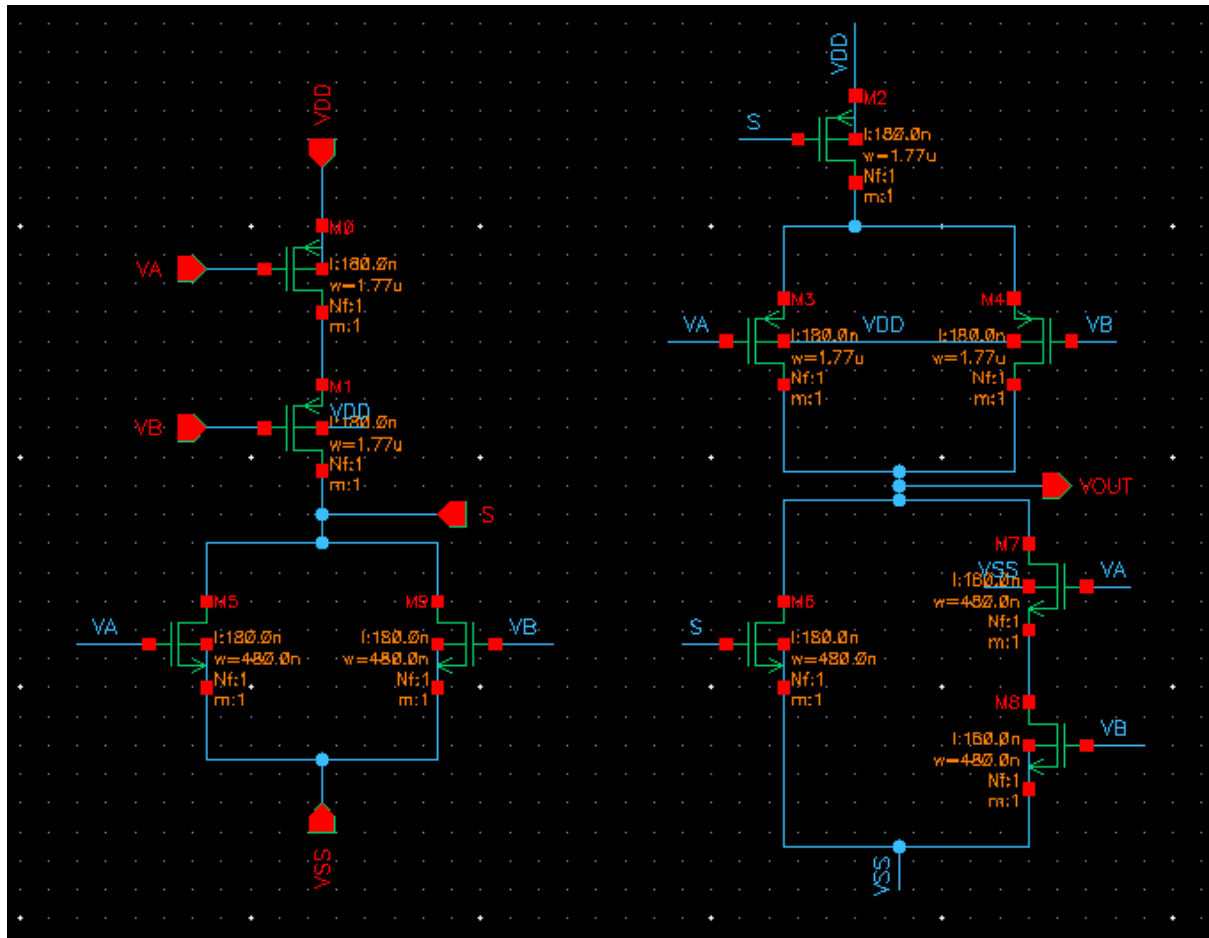


## Inverter-4X Layout:

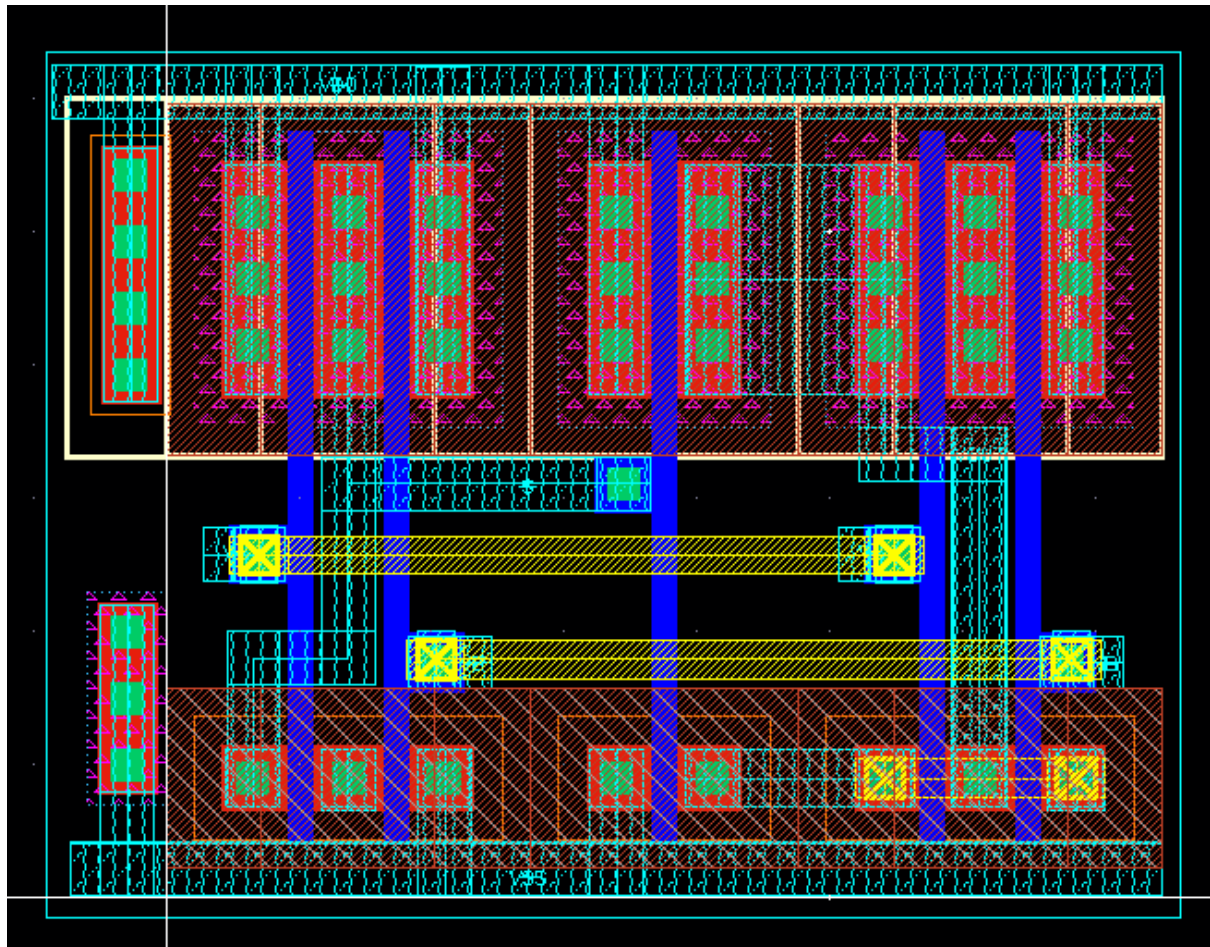


## 7.XOR

Schematic:

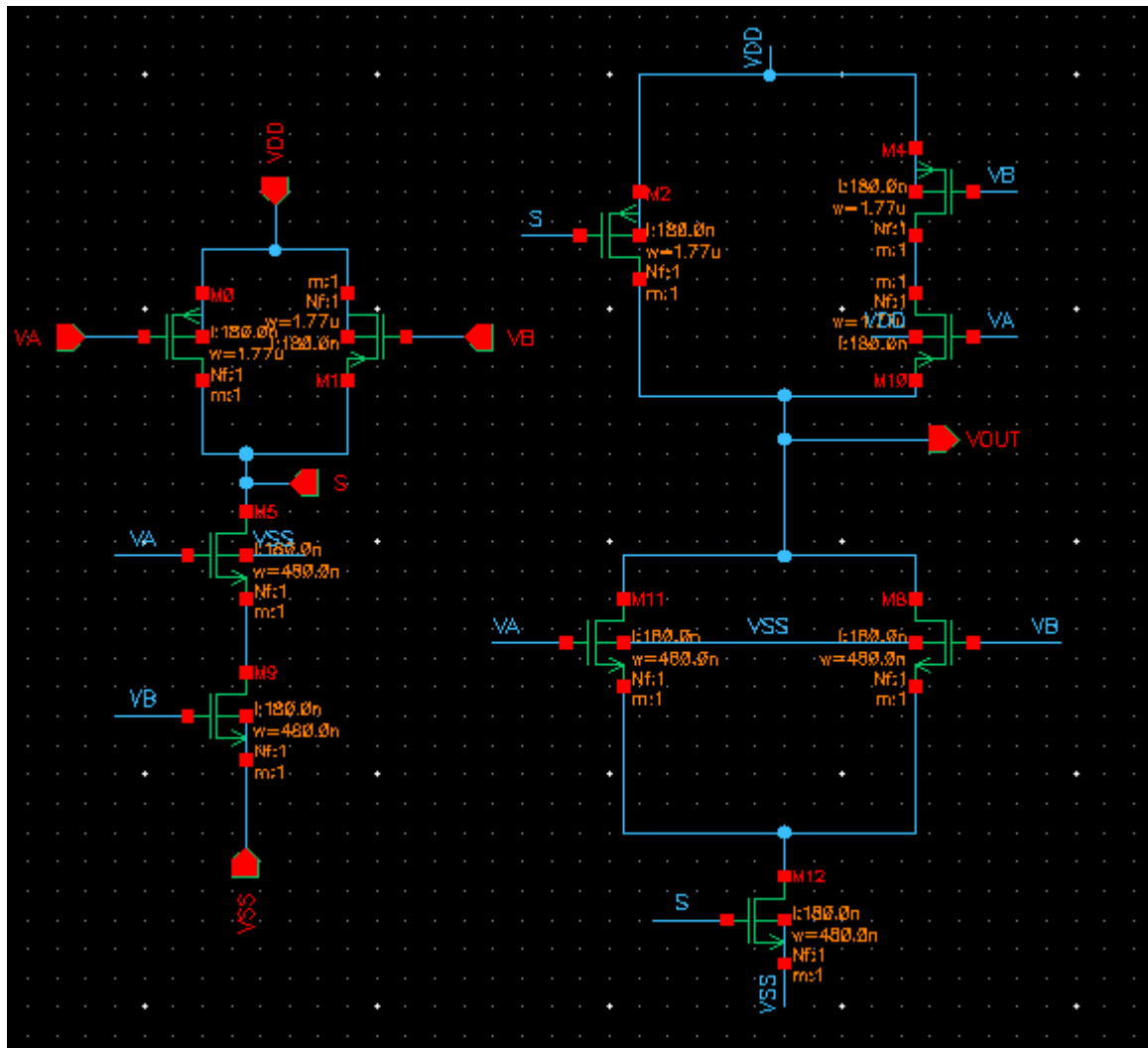


## XOR Layout:



## 8.XNOR

Schematic:





## XNOR Layout:

