

# **Linux SmartFusion**

## **BSP (Board Support Package) Guide for the Microsemi A2F500-DEV-BRD Board**

Release 1.8.0

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## 1. Overview

This document is a Linux SmartFusion BSP (Board Support Package) Guide for the Microsemi A2F500-DEV-BRD board, Release 1.8.0.

The BSP provides a software development environment for evaluation and development of Linux on the Cortex-M3 processor core of the Microsemi SmartFusion microcontroller using the Microsemi A2F500-DEV-BRD board as a hardware platform.

## 2. Product Contents

This product includes the following components.

### 2.1. Shippable Hardware Items

The following hardware items are shipped to customers of this product:

1. None - THIS IS A SOFTWARE-ONLY PACKAGE. Please purchase the A2F500-DEV-BRD board from Microsemi or its distributors.

### 2.2. Downloadable Hardware Materials

The following hardware materials are available for download from Emcraft's web site to customers of this product:

1. None - Please obtain hardware materials pertinent to the A2F500-DEV-BRD board directly from Microsemi.

### 2.3. Downloadable Software Materials

The following software materials are available for download from Emcraft's web site to customers of this product:

1. `a2f-actel-dev-brd-F.pdb` - Libero .pdb file with the U-Boot image embedded, ready for installation onto the A2F500-DEV-BRD board using the Microsemi FlashPro tool;
2. `a2f-actel-dev-brd-F.zip` - Libero project file corresponding to the `pdb` file in the item above;
3. `networking.uImage` - prebuilt kernel image ready to be loaded to the A2F500-DEV-BRD board;
4. `linux-A2F-1.8.0.tar.bz2` - Linux SmartFusion software development environment, including:
  - a) U-Boot firmware;
  - b) Linux kernel;
  - c) `busybox` and other target components;
  - d) Linux-hosted cross-development environment;
  - e) Framework for developing multiple projects (embedded applications) from a single installation, including sample projects allowing to kick-start software development for Linux SmartFusion.

### 2.4. Downloadable Documentation Materials

The following documentation materials are available for download from Emcraft's web site to customers of this product:

1. `linux-cortexm-um-1.8.0.pdf` - Linux Cortex-M User's Manual;
2. `linux-Actel-DEV-BRD-bsp-1.8.0.pdf` - Linux SmartFusion BSP (Board Support Package) Guide for the Microsemi A2F500-DEV-BRD Board (this document).

### 3. Software Functionality

#### 3.1. Supported Features

The following list summarizes the features and capabilities of Linux SmartFusion, Release 1.8.0:

- U-Boot firmware:
  - U-Boot v2010.03;
  - Target initialization from power-on / reset;
  - Runs from the internal eNVM and internal SRAM (no external memory required for standalone operation);
  - Serial console;
  - Ethernet driver for loading images to the target;
  - Serial driver for loading images to the target;
  - Device driver for built-in Flash (eNVM) and self-upgrade capability;
  - Device driver for storing environment and Linux images in external Flash;
  - Autoboot feature, allowing boot of OS images from Flash or other storage with no operator intervention;
  - Persistent environment in Flash for customization of target operation;
  - Sophisticated command interface for maintenance and development of the target.
- Linux:
  - uClinux kernel v2.6.33;
  - Boot from compressed and uncompressed images;
  - Ability to run critical kernel code from integrated Flash of SmartFusion;
  - Serial device driver and Linux console;
  - Ethernet device driver and networking (`ping`, NFS, Telnet, FTP, `ntpd`, etc.);
  - `busybox` v1.17;
  - POSIX pthreads;
  - Process-to-kernel and process-to-process protection using the Memory Protection Unit (MPU) of the SmartFusion core;
  - Hardened exception handling; an exception triggered by a process affects only the offending process;
  - Loadable kernel modules;
  - Secure shell (`ssh`) daemon;
  - Web server;
  - MTD-based Flash partitioning and persistent JFFS2 Flash file system for external Flash;
  - SPI controller master-mode device driver;
  - Device driver for the embedded NVM;
  - Framebuffer device driver for the low-cost SPI-based LCD monitor (Nokia6100 LCD);
  - Serial device driver for CoreUARTapb;
  - Support for RS-485 in the serial device driver;
  - Target tool for self-upgrades of the SmartFusion FPGA fabric over IAP.

- Development tools:
  - ARMv7-optimized GNU toolchain from CodeSourcery (2010q1) is used for development of U-Boot, Linux and user-space applications (toolchain must be downloaded separately from the CodeSourcery web site);
  - Cross GDB for debugging user-space applications;
  - `mkimage` tool used by the Linux kernel build process to create a Linux image bootable by U-Boot.
- Development environment:
  - Linux-hosted cross-development environment;
  - Development of multiple projects (embedded applications) from a single installation;
  - `hello` sample project ("Hello, world!" single-process configuration);
  - `networking` sample project (basic shell, networking and Flash management tools demonstration);
  - `developer` sample project (template project that can be used to jump-start development of custom user-space applications and loadable kernel modules).

### 3.2. New and Changed Features

This section lists new and changed features of this release:

1. Enable tickless kernel (`CONFIG_NO_HZ`).  
*ID: RT 79812.*

### 3.3. Known Problems & Limitations

This section lists known problems and limitations of this release:

1. •NFS-mounts without an `-o rsize=1024` option result in "Frame CRC errors" reported by the Linux Ethernet driver.  
*ID: RT 62655.*  
*Workaround:* Use an `-o rsize=1024` option when NFS-mounting remote directories.  
*Example:*

```
mount -o nolock,rsize=1024 <ip>:<remote_dir> <mount_point>
```

2. SPI driver doesn't work on A2F500.  
*ID: RT 71635.*  
*Workaround:* None.
3. `CONFIG_KERNEL_IN_ENVM` requires disabling `CONFIG_ARM_UNWIND` and `CONFIG_EARLY_PRINTK`.  
*ID: RT 74683.*  
*Workaround:* When enabling `CONFIG_KERNEL_IN_ENVM` in the kernel, disable `CONFIG_ARM_UNWIND` and `CONFIG_EARLY_PRINTK`.

## 4. Hardware Setup

This section explains how to set up the Microsemi A2F500-DEV-BRD board in such a way as to allow running uClinux on this hardware platform.

### 4.1. Hardware Interfaces

For a description of the hardware interfaces provided by the A2F500-DEV-BRD board, refer to detailed technical documentation and other associated materials available from Microsemi.

## 4.2. Jumpers

Linux Cortex-M makes use of the 1.8V 16 MBytes PSRAM memory and the 1.8V 16 MBytes Flash memory for its operation. These memories operate at 1.8V and can not be used simultaneously with the 3.3V memory devices also available on the development board.

To access the 1.8V memory devices, the AGLP125 FPGA device must be programmed with a 3.3V to 1.8V converter design. The development board comes from Microsemi with the AGLP125 pre-programmed with an appropriate design so no action is needed from you on this.

Additionally, the following jumpers must be configured on the board to allow access to the 1.8V memory devices:

Jumpers	Configuration	Notes
JP17	1-2 closed	To keep 3.3V SRAM in tristate (deselect)
JP19	1-2 closed	To keep 3.3V Flash in tristate (deselect)
JP24	2-3 closed	To disable 3.3V Flash
JP16	1-2 closed	To keep AGLP125 in active mode
JP25	1-2 closed	FLASH_WP# to VCC (to disable write protection)

## 4.3. Board Connections

To provide the power to the A2F500-DEV-BRD board, connect the power cable to the power jack of the board.

To provide a serial interface to a PC / notebook plug a mini-USB cable into the USB connector for UART0 on the board. On the PC, the USB link provides a serial console device to the development board. The Linux SmartFusion software is configured for a 115.2Kb terminal. On the Linux host, the serial console is available using a `/dev/ttyUSBn` device.

To provide network connectivity to the board, connect it into your LAN by plugging a standard Ethernet cable into the 10/100 Ethernet connector.

The A2F500-DEV-BRD board comes with the Flash programmer device (LC programmer). Attach the LC programmer to the J15 connector and attach a USB cable to the programmer. As soon as you have installed the Linux A2F500-DEV-BRD Libero project onto the board (refer to Section 5.1), you can remove the connection to the programmer.

Apply the power to the A2F500-DEV-BRD board using the on-board power switch.

## 5. A2F500-DEV-BRD Board Software Set-up

### 5.1. Libero Project

You have to program the Emcraft-provided Linux SmartFusion Libero project onto the Microsemi A2F500-DEV-BRD board, in order to install U-Boot to the non-volatile memory of SmartFusion and provide a SmartFusion configuration suitable for the Linux operation.

Installation of the Libero project is accomplished using the Microsemi FlashPro tool. Here is an example of how this can be done:

1. Start FlashPro on a Windows host;
2. From the FlashPro IDE, create a new project with an arbitrary name;
3. From the main FlashPro window, push `Configure Device`;
4. Push `Browse next` to load existing programming file. Browse to the Linux SmartFusion project file `a2f-actel-dev-brd-F.pdb` and chose it;

5. Push `Program` at the top of the main window to program the project onto the SmartFusion device and wait for the programming procedure to complete. If the programming completes successfully, a next reset should bring the U-Boot start-up messages and the command line interface onto the serial console interface.

## 5.2. U-Boot Environment

When the A2F500-DEV-BRD board is reset, U-Boot comes up from the built-in Flash printing the following output to the serial console:

```
U-Boot 2010.03-linux-cortexm-1.8.0 (Sep 07 2012 - 19:17:21)

CPU : SmartFusion FPGA (Cortex-M3 Hard IP)
Freqs: FCLK=80MHz, PCLK0=20MHz, PCLK1=20MHz, ACE=40MHz, FPGA=40MHz
Board: Actel SmartFusion board 1.8V Memory
DRAM: 16 MB
Flash: 16 MB
*** Warning - bad CRC, using default environment

In:      serial
Out:     serial
Err:     serial
Net:     Core10/100
Hit any key to stop autoboot:  0
Actel-DEV-BRD>
```

The reason for the warning about the "bad CRC" is that U-Boot is configured to store its environment variables in the external Flash. However, this being the first time when you boot Linux SmartFusion on the development board, obviously there is no U-Boot environment programmed to the external Flash. U-Boot goes to the external Flash, fails to find its environment there, prints the warning message and resorts to using the default environment integrated into the U-Boot image at build time.

U-Boot provides a command called `saveenv` that stores the up-to-date run-time environment to the persistent storage, which will be the external Flash for the U-Boot configuration used on the A2F500-DEV-BRD board.

This is how you can write the current U-Boot environment to the external Flash:

```
Actel-DEV-BRD> saveenv
Saving Environment to Flash...
...
Actel-DEV-BRD>
```

Reset the A2F500-DEV-BRD board and check that there is no warning about the bad CRC in the boot-up messages. This is expected since now U-Boot successfully finds its environment in the external Flash:

```
Actel-DEV-BRD> reset
resetting ...

U-Boot 2010.03-linux-cortexm-1.8.0 (Sep 07 2012 - 19:43:45)
...
Hit any key to stop autoboot:  0
Actel-DEV-BRD>
```

## 5.3. Ethernet MAC Address

In Linux SmartFusion, the MAC address of the Ethernet interface is defined by the `ethaddr` U-Boot environment variable. The value of the MAC address can be examined from the U-Boot command line monitor as follows:

```
Actel-DEV-BRD> printenv ethaddr
ethaddr=C0:B1:3C:88:88:88
Actel-DEV-BRD>
```

The default U-Boot environment for the A2F500-DEV-BRD board sets `ethaddr` to a fixed MAC address. This address should work for you in a general case, however if you have more than two A2F500-DEV-BRD boards in your LAN, use of the same address on multiple boards may result in packet collisions in your LAN and overall may render your LAN mal-functioning.

If you have more than one A2F500-DEV-BRD boards in your LAN, you have to assign a unique MAC address to each board.

The MAC address can be changed by modifying the `ethaddr` variable as follows:

```
Actel-DEV-BRD> setenv ethaddr C0:B1:3C:88:88:89
```

Don't forget to store your update in the persistent storage so it is remembered across resets and power cycles:

```
Actel-DEV-BRD> saveenv
Saving Environment to Flash...
...
```

## 5.4. Network Configuration

You will have to update the network configuration of your board to match settings of your local environment.

Typically, all you have to allow loading images over network from a TFTP server is update the U-Boot environment variables `ipaddr` (the board IP address) and `serverip` (the IP address of the TFTP server). Here is how it is done.

Update `ipaddr` and `serverip`:

```
Actel-DEV-BRD> setenv ipaddr 192.168.0.2
Actel-DEV-BRD> setenv serverip 192.168.0.1
```

and then save the updated environment to the external Flash so that your changes are persistent across resets/power cycles:

```
Actel-DEV-BRD> saveenv
Saving Environment to Flash...
...
Actel-DEV-BRD>
```

## 5.5. Installation of Linux Images to Flash

At this point, you are able to load Linux bootable images to the board over TFTP and either boot them directly or install them to the external Flash to allow booting Linux from Flash in the auto-boot mode.

On the host, activate the Linux SmartFusion development environment and build the networking project:

```
-bash-3.2$ . ACTIVATE.sh
-bash-3.2$ cd projects/networking/
-bash-3.2$ make
...
-bash-3.2$
```

Copy the Linux bootable image to the TFTP download directory:

```
-bash-3.2$ cp networking.uImage /tftpboot/vlad/
-bash-3.2$
```

To load the image directly, use the `netboot` U-Boot macro:

```
Actel-DEV-BRD> setenv image vlad/networking.uImage
Actel-DEV-BRD> run netboot
Auto-negotiation...completed.
```



```

Corel0/100: link UP (100/Full)
Using Corel0/100 device
TFTP from server 172.17.0.1; our IP address is 172.17.5.100
Filename 'vlad/networking.uImage'.
...
Loading: #####
          #####
          #####
done
Bytes transferred = 2084704 (1fcf60 hex)
...
Image Name:      Linux-2.6.33-arm1
Image Type:      ARM Linux Kernel Image (uncompressed)
...
Verifying Checksum ... OK
Loading Kernel Image ... OK
OK

Starting kernel ...

Linux version 2.6.33-arm1 (vlad@ocean.emcraft.com) (gcc version 4.4.1 (Sourcery G++ Lite
2010q1-189) ) #1 Mon Mar 12 15:43:44 MSK 2012
...

```

To load the image into the Flash, use the U-Boot update macro:

```

Actel-DEV-BRD> setenv image vlad/networking.uImage
Actel-DEV-BRD> run update
Auto-negotiation...completed.
Corel0/100: link UP (100/Full)
Using Corel0/100 device
TFTP from server 172.17.0.1; our IP address is 172.17.5.100
Filename 'vlad/networking.uImage'.
...
Loading: #####
          #####
          #####
done
Bytes transferred = 2084704 (1fcf60 hex)
..... done
Un-Protected 32 sectors

..... done
Erased 32 sectors
Copy to Flash... done
Actel-DEV-BRD>

```

Reset the board and verify that the newly programmed image boots on the target in the autoboot mode:

```

Actel-DEV-BRD> reset
resetting ...

U-Boot 2010.03-linux-cortexm-1.8.0 (Sep 07 2012 - 17:19:37)
...
Starting kernel ...
...
init started: BusyBox v1.17.0 (Sep 07 2012 - 17:19:37)
~ #

```

## 5.6. U-Boot Build

The BSP distribution comes with U-Boot pre-built for the A2F500-DEV-BRD board. If however you need to re-build U-Boot for your board, please follow the instructions below:

1. Install the Linux SmartFusion distribution to the development host, as described in the Linux Cortex-M User's Manual.
2. From the top of the Linux SmartFusion installation, activate the Linux SmartFusion cross-compile environment by running `. ACTIVATE.sh`.
3. Go to the U-Boot source directory (`cd u-boot/`).

#### 4. Run the following commands:

```
[psl@pvr u-boot]$ make a2f-actel-dev-brd_config
Configuring for a2f-actel-dev-brd board...
[psl@pvr u-boot]$ make -s
[psl@pvr u-boot]$ make -s u-boot.hex
```

## 6. FPGA Interfaces

### 6.1. FPGA IP Programming Interfaces

The Libero project included with the Linux SmartFusion distribution installs the following IP blocks to the FPGA fabric of the SmartFusion:

IP	Address Range	Tiles	Description
CoreInterrupt	0x40050000-0x4005002F	68*	Flexible Interrupt Controller for AMBA-Based Systems
VersionROM	0x40050100-0x4005010F	20	Provides an APB register-based interface ROM used for storing FPGA design type and version information
CoreGPIO	0x40050200-0x400502A3	460**	Provides an APB register-based interface to up to 32 GPIO signals
PSRAM_CR	0x40050300-0x40050313	470	Emcraft's custom IP allowing to configure external RAM (Micron's PSRAM) and put it to the faster Page Mode

\* - This is an approximate value for the Number of IRQ Sources = 4, taken from the CoreInterrupt datasheet. The exact number of used tiles may vary in different projects.

\*\* - This is an approximate value for the "minimum" configuration of the CoreGPIO (number of used GPIOs = 8), taken from the CoreGPIO datasheet. The exact number of used tiles may vary in different projects.

### 6.2. FPGA Development in Linux SmartFusion

To facilitate development involving FPGA modifications, the `developer` project includes the `iap_tool` utility designed for run-time FPGA fabric upgrades of the SmartFusion. Such FPGA self-upgrades are performed on a running system and do not require additional hardware, such as a FlashPro3/4 programmer device.

The `iap_tool` utility resides in the `/bin` directory of the `developer` project's root filesystem. It can be invoked from the command line as follows:

```
~ # /bin/iap_tool
Program FPGA Array of the SmartFusion
Usage: /mnt/iap_tool [options] <programming_data_file>
Options:
  -h, --help
        display this help and exit
  -l, --lock
        specify the lock file to use (default is /var/run/iap_tool)
  -a, --action
        specify the IAP action to run (default is PROGRAM_ARRAY)
        available actions (case insensitive):
            1 DEVICE_INFO
            2 READ_IDCODE
            3 ERASE
            4 ERASE_ALL
            5 PROGRAM
            6 VERIFY
            7 ENC_DATA_AUTHENTICATION
```

```

8 ERASE_ARRAY
9 PROGRAM_ARRAY
10 VERIFY_ARRAY
11 ERASE_FROM
12 PROGRAM_FROM
13 VERIFY_FROM
14 ERASE_SECURITY
15 PROGRAM_SECURITY
16 PROGRAM_NVM
17 VERIFY_NVM
18 VERIFY_DEVICE_INFO
19 READ_USERCODE
20 PROGRAM_NVM_ACTIVE
21 VERIFY_NVM_ACTIVE
22 IS_CORE_CONFIGURED

```

Behavior of the commands listed above is the same as implemented by the corresponding commands supported by the Actel FlashPro programming tool. Refer to:

[http://www.actel.com/documents/flashpro\\_ug.pdf](http://www.actel.com/documents/flashpro_ug.pdf)

for further details.

For example, to upgrade the FPGA array from a `file.dat` file the utility should be invoked as follows:

```
~ # /bin/iap_tool --action PROGRAM_ARRAY file.dat
```

As soon as the above command completes, the new image has been installed into the FPGA fabric and is running. No reset or Linux reboot is required.

To create a `.dat` file for FPGA upgrades using the `iap_tool` utility, load corresponding `.pdb` file into the Microsemi FlashPro application, select the `File -> Export -> Export Single Programming File` item of the main menu, choose the "DirectC File (\*.dat)" in the "Output formats" list, type in the resulting file name and location, and press button "Export".

Note that the FPGA fabric is hold in reset during execution of IAP operations and cannot be accessed by software drivers. Due to this, it is strongly recommended to configure kernel drivers for FPGA-based controllers as modules and unload all such modules before running the `iap_tool` utility.

The `iap_tool` utility must be used with caution since some actions (e.g. `ERASE_ARRAY`) can render the A2F non-functional (in such cases, a FlashPro3/4 device will be required to restore the SmartFusion device to a functional state).

## 7. Further Materials

Refer to *Linux Cortex-M User's Manual* for detailed information on the software architecture of the Linux SmartFusion distribution.

Visit Emcraft Systems' web site at [www.emcraft.com](http://www.emcraft.com) to obtain additional materials related to Linux SmartFusion.

## 8. Support

We appreciate your review of our product and welcome any and all feedback. Comments can be sent directly by email to:

[a2f-linux-support@emcraft.com](mailto:a2f-linux-support@emcraft.com)

The following level of support is included with your purchase of this product:

- Email support for installation, configuration and basic use scenarios of the product during 6 months since the product purchase;
- Free upgrade to new releases of the downloadable materials included in the product during 6 months since the product purchase.

If you require support beyond of what is described above, we will be happy to provide it using resources of our contract development team. Please contact us for details.