

Università degli Studi di Udine

Master degree in Artificial Intelligence and Cybersecurity

# FAIR TRANSITION SYSTEMS - PART 3

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## AN EXAMPLE OF REACTIVE PROGRAM

local x: integer where x=1

$$P1:: \left[ \begin{array}{l} \ell_0: \left[ \begin{array}{l} \ell_{0a}: \text{await } x=1 \\ \text{OR} \\ \ell_{0b}: \text{skip} \end{array} \right] \\ \ell_1: \end{array} \right] \parallel P2:: \left[ \begin{array}{l} m_0: \text{loop forever do} \\ m_1: X := -X \end{array} \right]$$

# PROPERTIES OF THE PROGRAM

- $P2$  never ends.
- Are there computations where  $P1$  does not terminate its execution?
  - The **only candidate computation** is:  
 $\sigma = \langle \pi = \{l_o, m_0\}, x = 1 \rangle \rightarrow^{m_0}$   
 $\langle \pi = \{l_o, m_1\}, x = 1 \rangle \rightarrow^{m_1}$   
 $\langle \pi = \{l_o, m_0\}, x = -1 \rangle \rightarrow^{m_0}$   
 $\langle \pi = \{l_o, m_1\}, x = -1 \rangle \rightarrow^{m_1}$   
 $\langle \pi = \{l_o, m_0\}, x = 1 \rangle \rightarrow^{m_0} \dots$
  - **It is not an admissible computation**: the requirement of justice is satisfied by  $l_{0a}$ , since it is not constantly enabled, but not by  $l_{0b}$ , which is constantly enabled and never executed.

## AN EXAMPLE OF REACTIVE PROGRAM - VARIANT 1

local x: integer where x=1

$$P1:: \left[ \begin{array}{l} \ell_0: \left[ \begin{array}{l} \ell_{0a}: \text{await } x=1 \\ \text{OR} \\ \ell_{0b}: \text{await } x \neq 1 \end{array} \right] \\ \ell_1: \end{array} \right] \parallel P2:: \left[ \begin{array}{l} m_0: \text{loop forever do} \\ m_1: x := -x \end{array} \right]$$

# PROPERTIES OF THE PROGRAM - VARIANT 1

- $P2$  never ends.
- Are there computations where  $P1$  does not terminate its execution?
  - The **only candidate computation** is the previously identified one.
  - In such a case, it turns out to be an **admissible computation**: the requirement of justice is satisfied both by  $l_{0a}$  and by  $l_{0b}$ , as no one of them is constantly enabled.

## AN EXAMPLE OF REACTIVE PROGRAM - VARIANT 2

local x: integer where x=1

$$\begin{array}{l} \text{P1::} \left[ \begin{array}{l} \ell_0: \text{if } x=1 \text{ then} \\ \quad \ell_1: \text{skip} \\ \text{else} \\ \quad \ell_2: \text{skip} \\ \ell_3: \end{array} \right] \parallel \text{P2::} \left[ \begin{array}{l} m_0: \text{loop forever do} \\ \quad m_1: x := -x \end{array} \right] \end{array}$$

# PROPERTIES OF THE PROGRAM - VARIANT 2

- $P2$  never ends.
- Are there computations where  $P1$  does not terminate its execution?
  - The **candidate computations** are those where, from a certain point onwards,  $l_0$  (respectively,  $l_1, l_2$ ) constantly belongs to the value of  $\pi$ .
  - **They are not admissible computations:**  
(the transition corresponding to) the instruction `if` is always enabled and thus sooner or later it must be executed;  
if it is never executed, then the condition of justice is violated,  
if it is executed, then (the transition corresponding to) one of the two instructions `skip` is reached, and, since it is constantly enabled, sooner or later it must be executed;  
if it is never executed, then the condition of justice is violated.

# THE MUTUAL EXCLUSION PROBLEM: THE SPL PROGRAM MUX\_SEM

local y: integer where y=1

$$\begin{array}{l}
 \text{P1::} \left[ \begin{array}{l} \ell_0: \text{ loop forever do} \\ \quad \left[ \begin{array}{l} \ell_1: \text{ noncritical} \\ \ell_2: \text{ request y} \\ \ell_3: \text{ critical} \\ \ell_4: \text{ release y} \end{array} \right] \\ \ell_5: \end{array} \right] \parallel \parallel \text{P2::} \left[ \begin{array}{l} m_0: \text{ loop forever do} \\ \quad \left[ \begin{array}{l} m_1: \text{ noncritical} \\ m_2: \text{ request y} \\ m_3: \text{ critical} \\ m_4: \text{ release y} \end{array} \right] \\ m_5: \end{array} \right]
 \end{array}$$



# PROPERTIES: MUTUAL EXCLUSION

- **Mutual exclusion:** no computation of the program may include a state where both processes are within their critical region ( $P1$  at  $l3$  and  $P2$  at  $m3$ ).
- If  $P1$  is at  $l3$ , the instruction  $l2$  has assigned the value 0 to  $y$  (by decrementing it by 1) and, until instruction  $l4$  is executed, any attempt of  $P2$  to execute  $m2$  is bound to fail (as the enabling condition  $y > 0$  is not satisfied). A similar argument holds in case  $P2$  is at  $m3$ .

It is worth pointing out that process  $P1$  cannot stay forever in the critical region, that is, it cannot remain indefinitely at position  $l3$ , thanks to the justice requirement for instruction `critical` (the same remark applies to  $P2$ ).

# PROPERTIES: ACCESSIBILITY - 1

- **Accessibility:** every state of a computation where a process is at the end of its non-critical region, that is,  $P1$  is at  $l2$  (resp.,  $P2$  is at  $m2$ ), must be followed by a state where it is within its critical region, that is, it must be followed by a state where  $P1$  is at  $l3$  (resp.,  $P2$  is at  $m3$ ).
- Let us show now that every computation where this is not the case is not admissible.

Let us assume that  $P1$  is blocked at  $l2$  (the case in which  $P2$  is blocked at  $m2$  is analogous). If, from a certain point onwards,  $y$  remains constantly equal to 1, that is,  $P2$  remains indefinitely within its non-critical region,  $l2$  is constantly enabled and never executed, and thus the requirement of justice, which is implied by the requirement of compassion, is violated.

# PROPERTIES: ACCESSIBILITY - 2

- If  $y$  does not remain constantly equal to 1, the computation necessarily has the following form:

$$\begin{aligned} \sigma: & \langle \pi = \{l_0, m_0\}, y = 1 \rangle \rightarrow \dots \langle \pi = \{l_2, m_2\}, y = 1 \rangle \rightarrow^{m_2} \\ & \langle \pi = \{l_2, m_3\}, y = 0 \rangle \rightarrow^{m_3} \langle \pi = \{l_2, m_4\}, y = 0 \rangle \rightarrow^{m_4} \\ & \langle \pi = \{l_2, m_0\}, y = 1 \rangle \rightarrow^{m_0} \langle \pi = \{l_2, m_1\}, y = 1 \rangle \rightarrow^{m_1} \\ & \langle \pi = \{l_2, m_2\}, y = 1 \rangle \rightarrow \dots \end{aligned}$$

Such a computation visits infinitely many times the state  $\langle \pi = \{l_2, m_2\}, y = 1 \rangle$  where the transition  $l_2$  is enabled, which implies that the requirement of compassion is violated.

This allows us to conclude that the program satisfies the property of accessibility,

# THE MUTUAL EXCLUSION PROBLEM: THE SPL PROGRAM MUX\_WHEN

local y: integer where y=1

$P1:: \left[ \begin{array}{l} l0: \text{ loop forever do} \\ \quad \left[ \begin{array}{l} l1: \text{ noncritical} \\ l2: \text{ <when y>0 do y:=y-1>} \\ l3: \text{ critical} \\ l4: \text{ y:=y+1} \end{array} \right] \\ l5: \end{array} \right]$	$\parallel$	$P2:: \left[ \begin{array}{l} m0: \text{ loop forever do} \\ \quad \left[ \begin{array}{l} m1: \text{ noncritical} \\ m2: \text{ <when y>0 do y:=y-1>} \\ m3: \text{ critical} \\ m4: \text{ y:=y+1} \end{array} \right] \\ m5: \end{array} \right]$
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# THE PROPERTIES OF EXCLUSIVITY AND ACCESSIBILITY

- It **satisfies** the property of **mutual exclusion**: it can be shown by means of an argument analogous to the one used for the previous program (the use of a grouped instruction in  $I2$  and  $m2$  is fundamental).
- It **does not satisfy** the property of **accessibility**: the previously described computation where  $I2$  is enabled infinitely many times (but not constantly enabled) and never taken from a given point onwards is admissible (for  $I2$  and  $m2$  justice, and not compassion, is required).

# THE PROPERTY OF “COMMUNAL” ACCESSIBILITY

- The proposed variant **satisfies** a weaker form of accessibility, called **communal accessibility**.

Communal accessibility states that each state of a computation where a process reaches the end of its non-critical region, e.g., location  $l_2$  for  $P_1$ , is followed by a state where some process, not necessarily the same, is within its critical region, that is, location  $m_3$  for  $P_2$  or location  $l_3$  for  $P_1$ .

## PRODUCER-CONSUMER WITH A BOUNDED BUFFER

local send, ack: channel [1..] of integer  
 where send =  $\Lambda$  , ack = [1, ..., 1]

Prod::  $\left[ \begin{array}{l} \text{local } x, t: \text{integer} \\ \ell_0: \text{loop forever do} \\ \quad \left[ \begin{array}{l} \ell_1: \text{produce } x \\ \ell_2: \text{ack} \Rightarrow t \\ \ell_3: \text{send} \Leftarrow x \end{array} \right] \end{array} \right]$  || Cons::  $\left[ \begin{array}{l} \text{local } y: \text{integer} \\ m_0: \text{loop forever do} \\ \quad \left[ \begin{array}{l} m_1: \text{send} \Rightarrow y \\ m_2: \text{ack} \Leftarrow 1 \\ m_3: \text{consume } y \end{array} \right] \end{array} \right]$

where ack initially contains  $n$  occurrences of 1;  $\ell_2$  can be executed only if ack is not empty,  $m_1$  only if send is not empty.

## THE SPL PROGRAM FAIR-MERGE

out b : channel of integer  
 local a1, a2 : channel of integer

P1::  $\left[ \begin{array}{l} \text{local } x1: \text{integer} \\ \ell_0: \text{loop forever do} \\ \quad \left[ \begin{array}{l} \ell_1: \text{produce } x1 \\ \ell_2: a1 \Leftarrow 2 \cdot x1 + 1 \end{array} \right] \end{array} \right] \parallel$

P2::  $\left[ \begin{array}{l} \text{local } x2: \text{integer} \\ \ell_0: \text{loop forever do} \\ \quad \left[ \begin{array}{l} \ell_1: \text{produce } x2 \\ \ell_2: a2 \Leftarrow 2 \cdot x2 \end{array} \right] \end{array} \right] \parallel$

Merger::  $\left[ \begin{array}{l} \text{local } y: \text{integer} \\ m_0: \text{loop forever do} \\ \quad \left[ \begin{array}{l} m1a: a1 \Rightarrow y \\ \text{or} \\ m1b: a2 \Rightarrow y \end{array} \right] \\ m2: b \Leftarrow y \end{array} \right] \parallel$

Cons::  $\left[ \begin{array}{l} \text{local } z: \text{integer} \\ n_0: \text{loop forever do} \\ \quad \left[ \begin{array}{l} n1: b \Rightarrow z \\ n2: \text{consume } z \end{array} \right] \end{array} \right]$