Analog Lab Midsem solutions & Marking Scheme Total 25 marks

1. Testing of the IC TLO71 (2 marks)

give +1 mork each of both of the IC's successfully tested.

Vout = Vin

- 2 Trianguler Wave generator.
 - 1. First Part: Analysis & Circuit
 - (a) Analysis J Sub circuit 1 (6 Monks)
 - 1) Positive Feedback (0.5 mark)
- 2) if Schmitt trigger -> 0.5 Marks

 if Non-inverting Schmitt trigger -> 1 mark)
 - 3) Virtual ground -> Not applicable (0.5 Marke)

4) Derivation sin

$$\frac{\sqrt{x-Vini} + \sqrt{x-Vout} = 0}{RI}$$

When VX = 0 the Yout would change.

$$Vint = -\frac{R1}{R2} V_{SAT} \frac{2}{3} \frac{0.5 M}{0.5 M} \quad Vin = \frac{R1}{R^2} V_{SAT}, \quad 1.5 M$$

$$VUTP = \frac{R_1}{R_L} \times 15 = \frac{15}{2.2} = 6.818$$

$$V_{CTP} = \frac{-15}{2.2} = -6.818$$

6) Analysis 1 Subcircut 2 (5 Marks)

2. Negetive Feedbock (0.5 Marks)

is Integralor circuit (1 Mark)

in yes (D.5 Magks)

Vinp R3

Vinp Vout.

IR - IC = 0 (K(L)

 $\frac{1}{1} = \frac{1}{1} = \frac{1}$

Ic = - C dVout

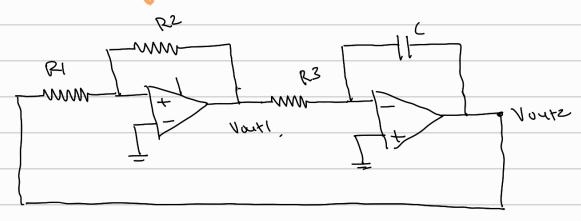
IR = Vinp-Vx = Vinp

Rz R3

IR - Ic = 0

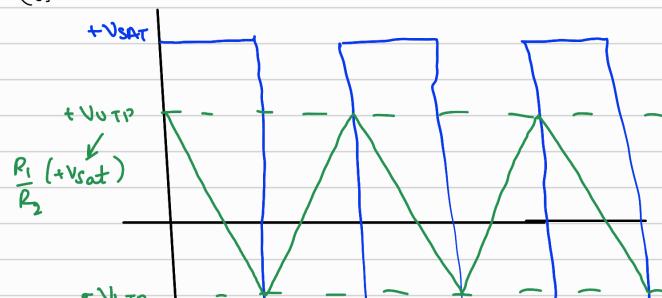
Vinp + c about = 0

c) Analysis 1 Integreted Circuit (6 marks)



inul condition

 $(\dot{\mathcal{L}})$



P1 (-Vsat)

-Vsat.

+1 for drawing t-rangular vorce

+1 for drawing Square vove

(22) Assuming inchial condition, & also the.

Joct that when non-inversity terminal

VOLY: & First Op-Amp Yeaches Zero

we switch from VSAT -- VSAT we get.

VUTP = RI VSAT

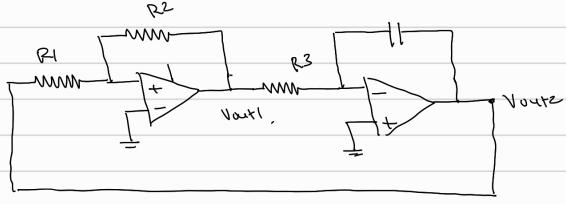
VUTP: - RI VSAT

Vpp= 2R1 VSAT

R2

(+1 for getting peak to peak)

Also assuming initial conditions



Voutl = + VSAT.

from 0 to Ty the output swings from.

$$Vout2 = - \frac{1}{R_3C} \int_{0}^{T/2} V_{SAT} \cdot dt$$

2. Determining the Component Values (2 Marks)

STUDENTS MAY USE DIFFERENT COMBINATIONS *

3. Hordwore Implementation (4 marks)

+1 for correct peak to peak values of square work

+1 for correct frequency of square wave

+1 for correct peak to peak value of triangular wave

+1 for correct frequency of trangular wave

freq TO1 = ± 100Hz