

XEN10

device> in family write max10 and in name write name
assignments>pinplanner
assign pins to leds/switches
SW1—8 are 38 to 47 (-42)
LED1—8 are 50 to 60 (-51 53 55)

prpcessing>start io analsis'
then compile full

tools>assignments>programmer>file>

add files>. add the .sof file

file> create svf file

click on jtag.exe
cable ft2232
detect
svf <filename>.svf progress

close the terminal

now u can check w xen10 boards:)

SCANCHAIN

scanchainfiles: synthesis ke 2 + toplevel
change toplevel and set as toplevelentity
compile
tools>assignments>programmer>file>create svf file
copy this svf file into URvjtag
click on jtag.exe
cable ft2232
detect
svf <filename>.svf progress

close the terminal

copypaste the tracefile.txt in scanchainfiles
write cmd in address bar
scan_vjtag.exe TRACEFILE.txt output123.txt

name and create output123.