

Analog Circuits(EE3300)

Simulation Exercise 1

EE19BTECH11041,
Srijith Reddy Pakala
 Department of Electrical Engineering
 IIT Hyderabad

September 26, 2021

1 Problem statement

1. MOS transistors in nano-scale CMOS processes don't quite follow quadratic I-V characteristics we studied in the class. Their exact models (BSIM, PSP) contain hundreds of parameters, and it is almost impossible to use these models for hand calculations. In this exercise, we will try to extract a simple quadratic model for MOS transistors from the available BSIM4 model.

Download model files and corresponding LTspice files from the class website.

Determine the following for NMOS (L=65nm, W=1um) and PMOS (L=65nm, W=1um) transistors from the given model files:

(Given supply voltage, $V_{dd}=1.1$ V. Use $V_{ds}=V_{ds0}=V_{dd}/2$, when not specified.)

- . • V_{gs0} : V_{gs} value in the middle of the quadratic region of I_{ds} vs V_{gs} curve. Use derivative function available in waveform viewer.
- . • g_{m0} : g_m at V_{gs0} .
- . • V_T : x-axis intercept of tangent at V_{gs0} in g_m vs V_{gs} curve.
- . • $\beta = \mu C_{ox}$: Using above extracted values and assuming quadratic behaviour.
- . • g_{ds0} : g_{ds} at V_{gs0} .
- . • λ : Channel-length modulation parameter. ($V_{gs}=V_{gs0}$).

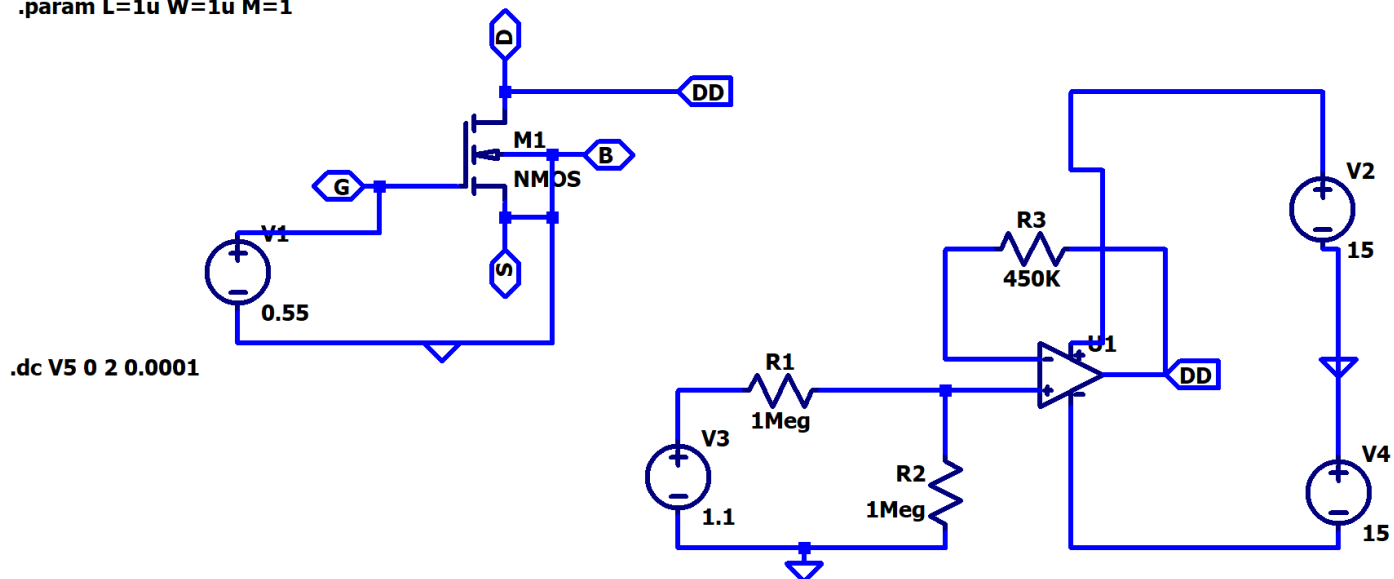
What is the range of V_{gs} for which error in estimated g_m is less than 50 %?

2. Repeat the above exercise for longer channel length devices. NMOS ($L=1\mu\text{m}$, $W=1\mu\text{m}$) and PMOS ($L=1\mu\text{m}$, $W=1\mu\text{m}$).

Question 1

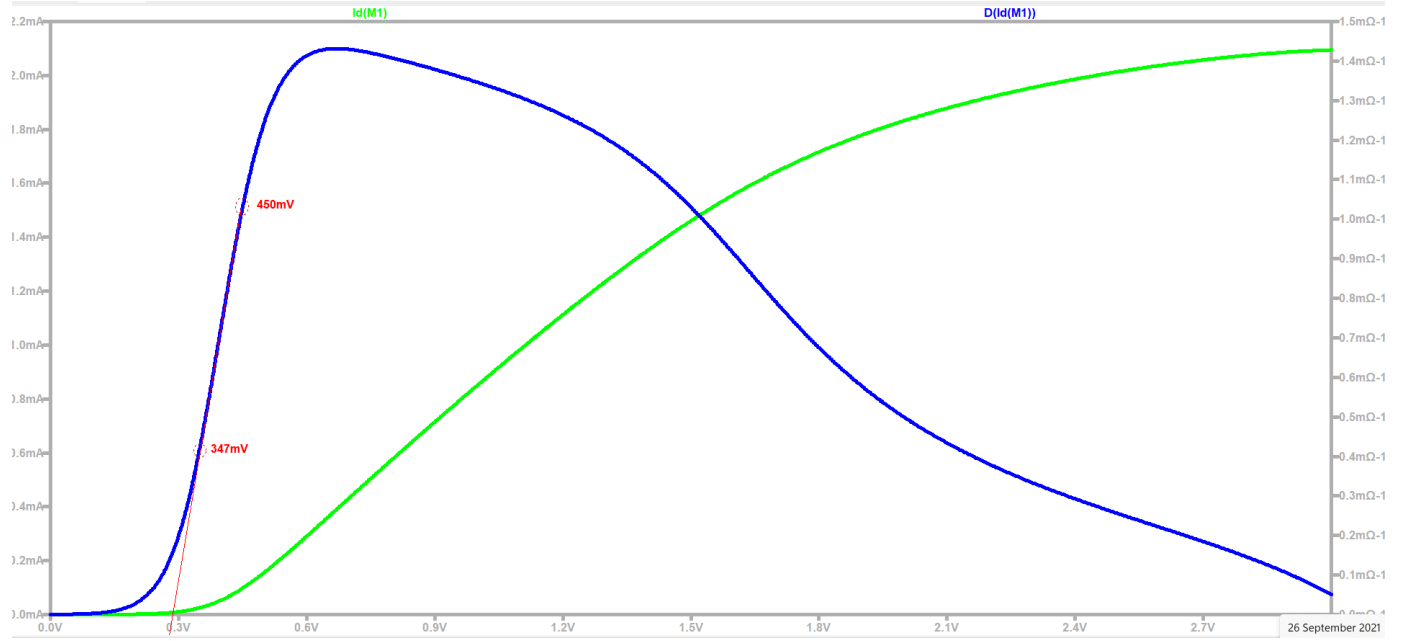
Schematic of NMOS

```
.include 65nm_NMOS_bulk.pm
.param L=1u W=1u M=1
```



Circuit schematic

I_{ds} Vs V_{gs} and g_m Vs V_{gs} Curves



From the graph we can see that the derivative is linear for V_{gs} in range of 347mV and 450mV.

$$V_{gs0} = \frac{347mV + 450mV}{2} = 388mV \quad (1)$$

$$g_{m0} = g_m|_{V_{gs0}} = 714\mu\Omega^{-1} \quad (2)$$

From the extension of tangent to x-axis we get

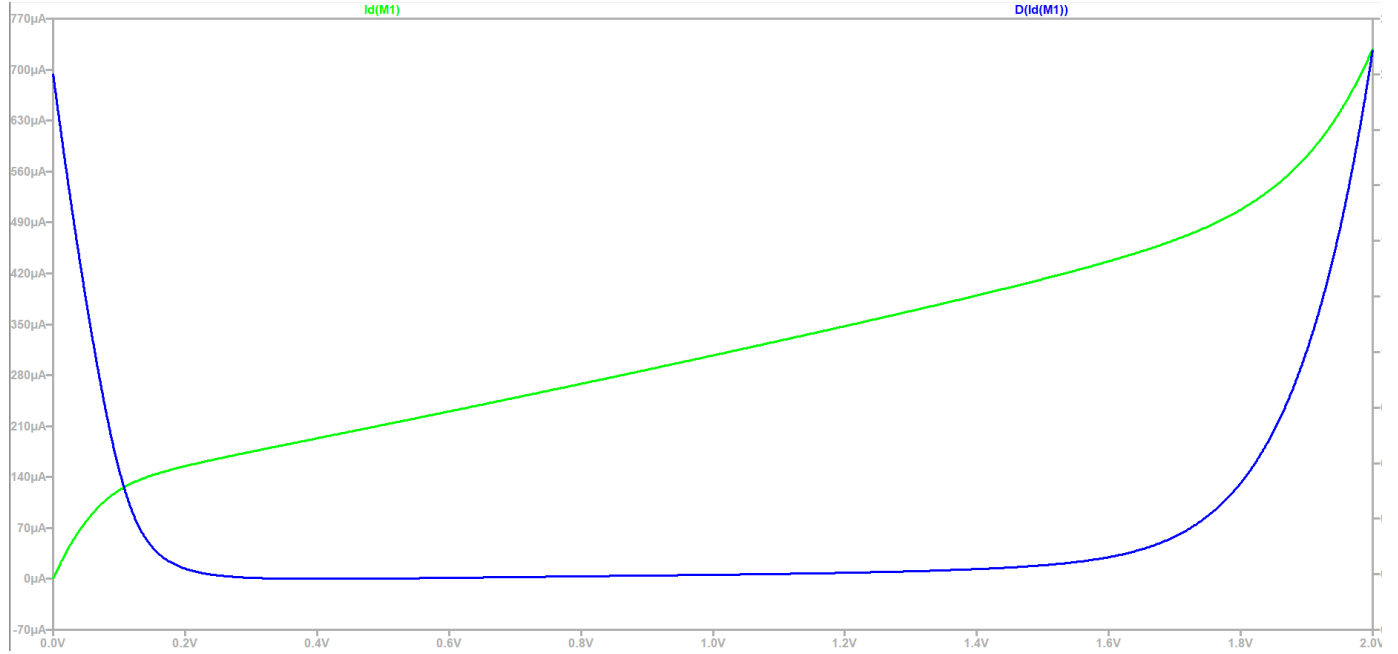
$$V_T = 288mV \quad (3)$$

Now using the below equation

$$\frac{\beta W(V_{gs0} - V_T)}{L} = g_{m0} \quad (4)$$

$$\beta = 569 \mu$$

I_{ds} Vs V_{ds} and g_{ds} Vs V_{ds} Curves



from the graph g_{ds0} : g_{ds} at V_{gs0} and $V_{ds} = 0.55V$.

$$g_{ds0} = 185\mu\Omega^{-1} \quad (5)$$

From the below equation we can calculate λ .

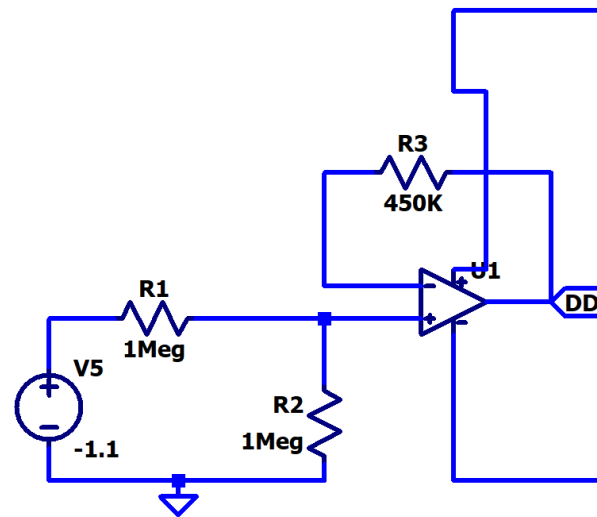
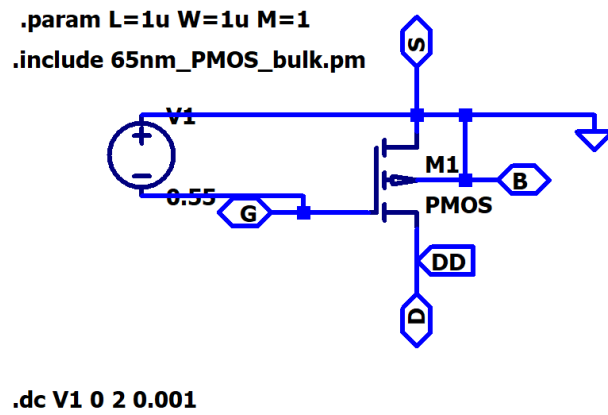
$$I_{ds}\lambda = g_{ds} \quad (6)$$

$$\lambda = 0.87.$$

Now to find range of V_{gs} with less than 50 % Error we need to find values in range of $(g_m/2, 3g_m/2)$.

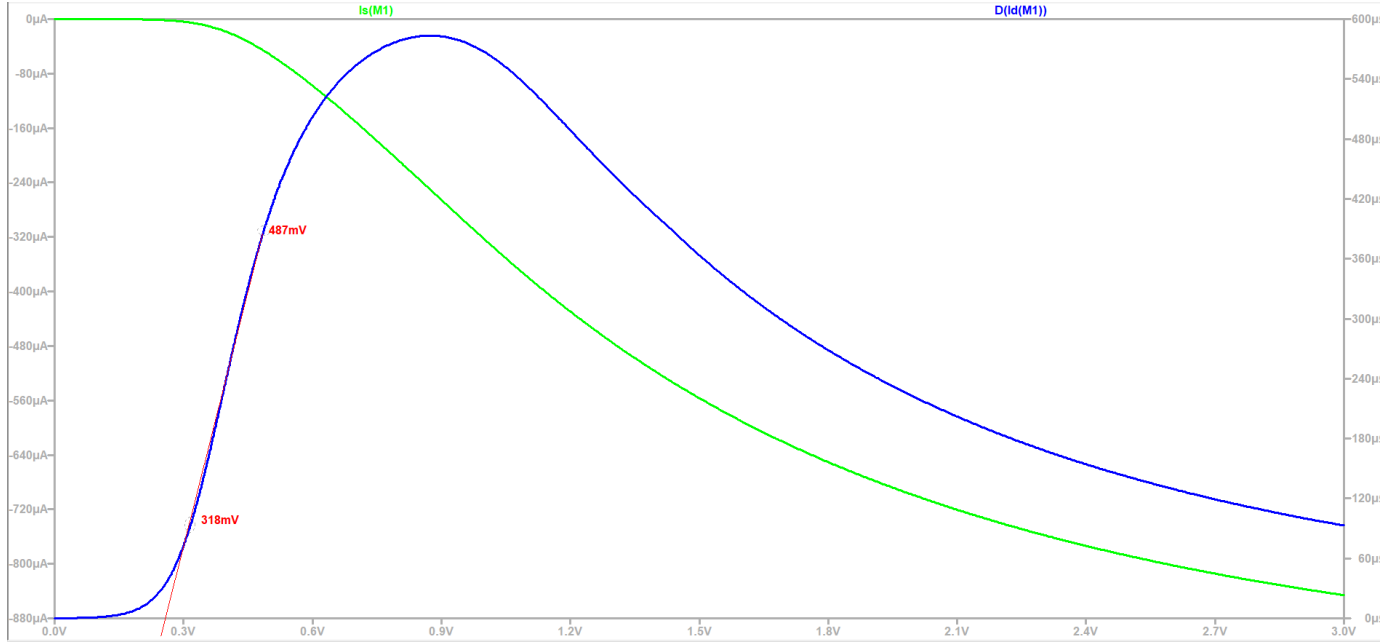
$$V_{gs} \text{ range} = (337mV, 458mV)$$

Schematic of PMOS



Circuit schematic

I_{ds} Vs V_{gs} and g_m Vs V_{gs} Curves



From the graph we can see that the derivative is linear for V_{gs} in range of -318mV and -487mV.

$$V_{gs0} = \frac{-318mV - 487mV}{2} = -395mV \quad (7)$$

$$g_{m0} = g_m|_{V_{gs0}} = -232\mu\Omega^{-1} \quad (8)$$

From the extension of tangent to x-axis we get

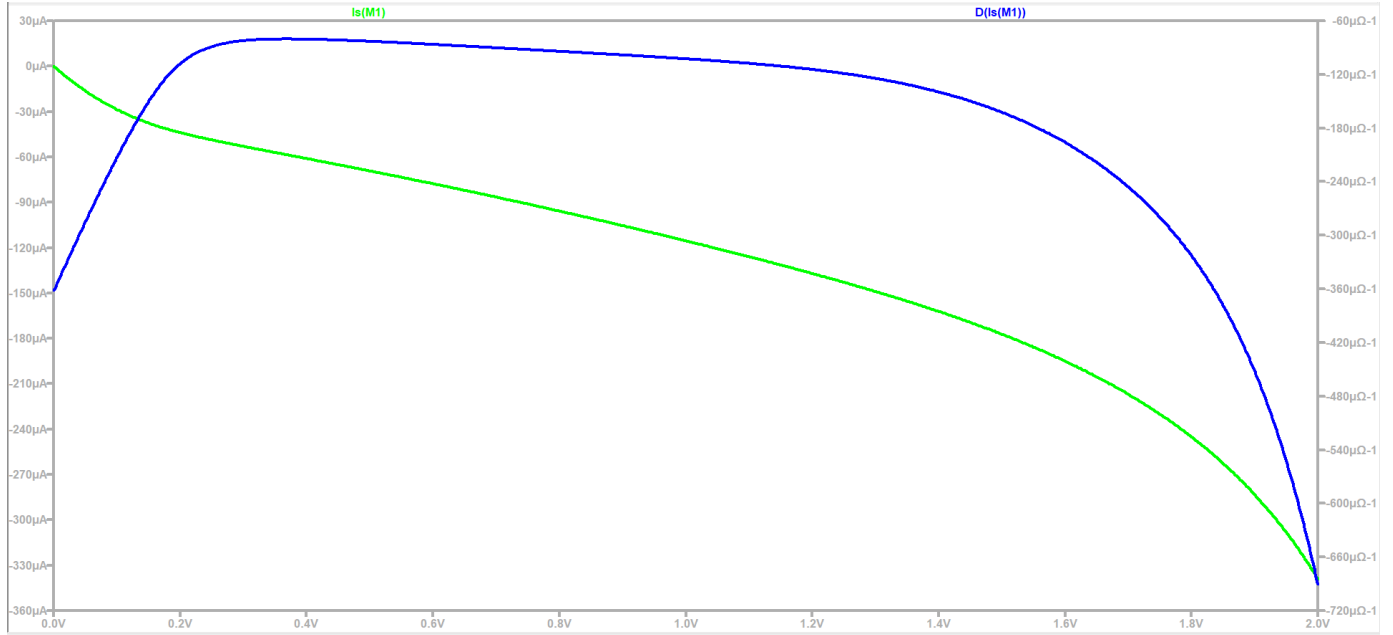
$$V_T = -280mV \quad (9)$$

Now using the below equation

$$\frac{\beta W(V_{gs0} - V_T)}{L} = g_{m0} \quad (10)$$

$$\beta = 184.8\mu$$

I_{ds} Vs V_{ds} and g_{ds} Vs V_{ds} Curves



from the graph g_{ds0} : g_{ds} at V_{gs0} and $V_{ds} = 0.55V$.

$$g_{ds0} = -84.99\mu\Omega^{-1} \quad (11)$$

From the below equation we can calculate λ .

$$I_{ds}\lambda = g_{ds} \quad (12)$$

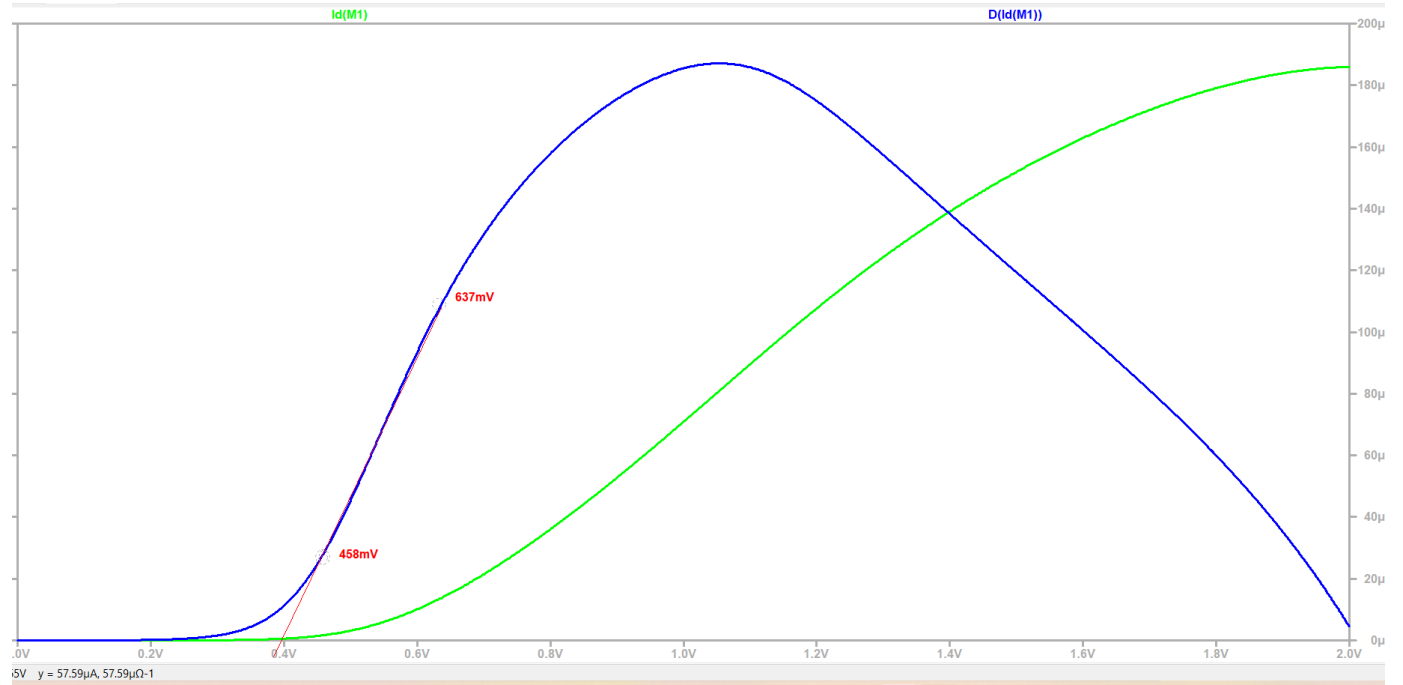
$$\lambda = 1.14.$$

Now to find range of V_{gs} with less than 50 % Error we need to find values in range of $(g_m/2, 3g_m/2)$.

$$V_{gs} \text{ range} = (-332mV, -460mV)$$

Long channeled NMOS

I_{ds} Vs V_{gs} and g_m Vs V_{gs} Curves



From the graph we can see that the derivative is linear for V_{gs} in range of 458mV and 637mV.

$$V_{gs0} = \frac{458mV + 637mV}{2} = 550mV \quad (13)$$

$$g_{m0} = g_m|_{V_{gs0}} = 69.35\mu\Omega^{-1} \quad (14)$$

From the extension of tangent to x-axis we get

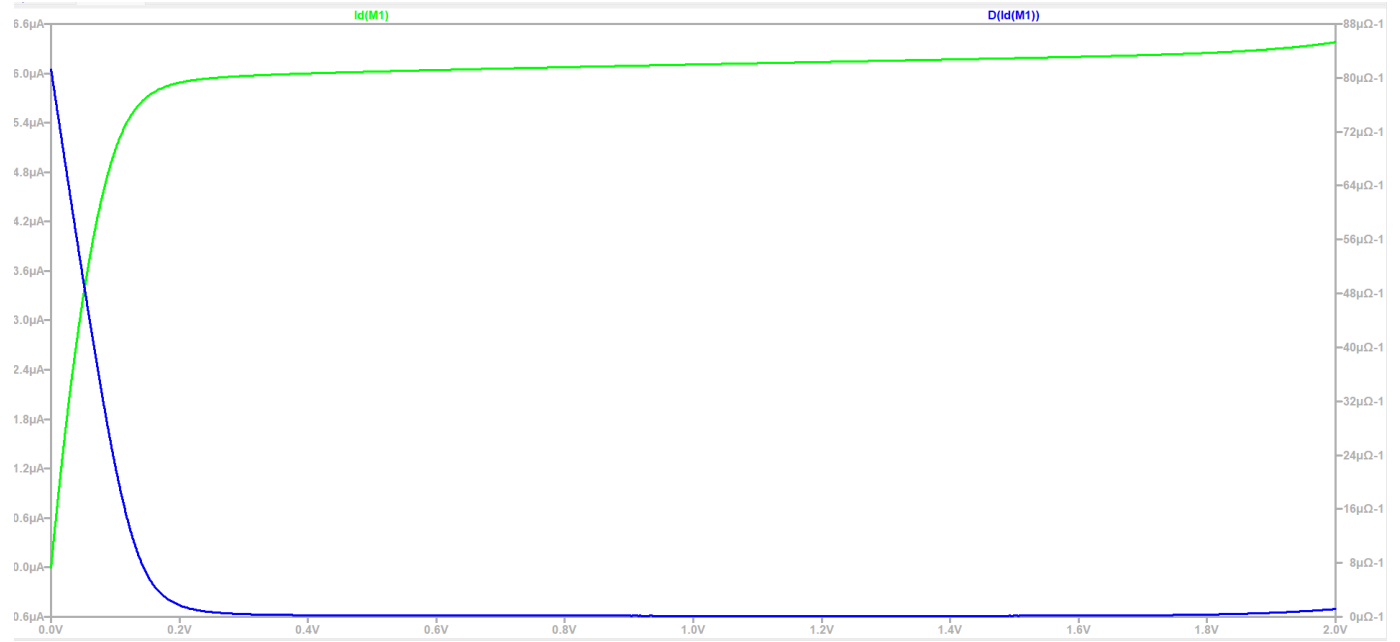
$$V_T = 400mV \quad (15)$$

Now using the below equation

$$\frac{\beta W(V_{gs0} - V_T)}{L} = g_{m0} \quad (16)$$

$$\beta = 541 \mu$$

I_{ds} Vs V_{ds} and g_{ds} Vs V_{ds} Curves



from the graph g_{ds0} : g_{ds} at V_{gs0} and $V_{ds} = 0.55V$.

$$g_{ds0} = 191 n\Omega^{-1} \quad (17)$$

From the below equation we can calculate λ .

$$I_{ds}\lambda = g_{ds} \quad (18)$$

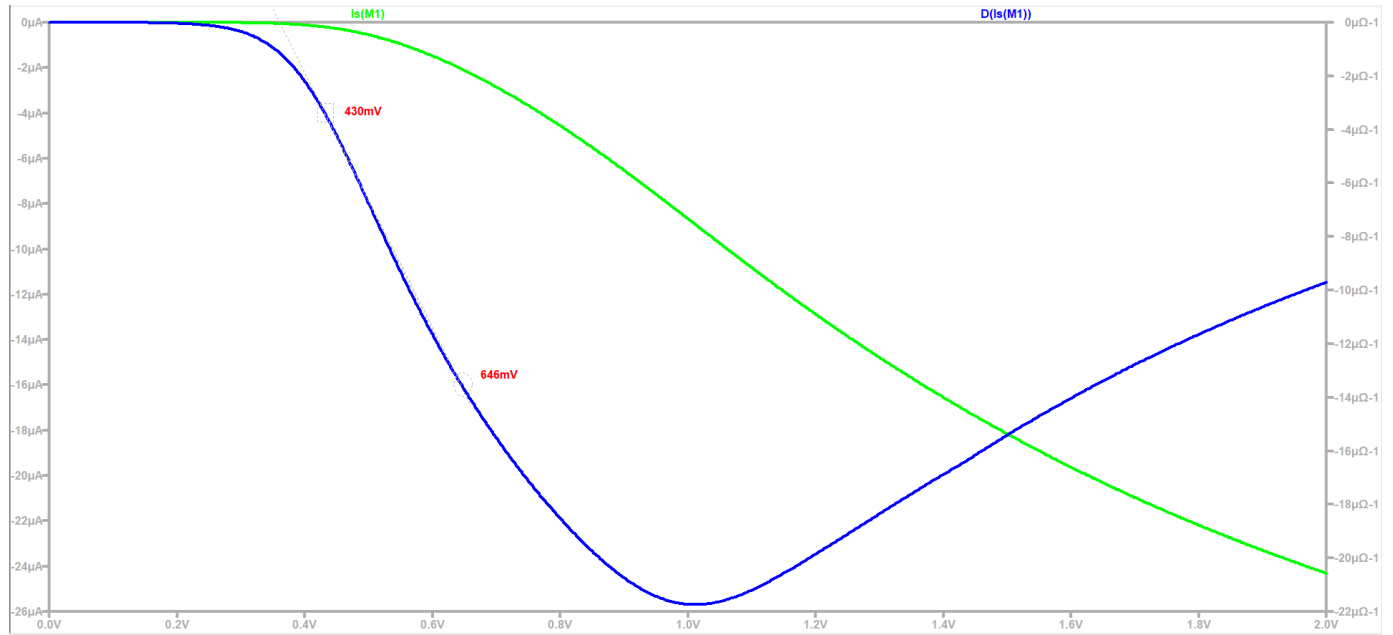
$$\lambda = 0.0316.$$

Now to find range of V_{gs} with less than 50 % Error we need to find values in range of $(g_m/2, 3g_m/2)$.

$$V_{gs} \text{ range} = (475mV, 626mV)$$

Long channeled PMOS

I_{ds} Vs V_{gs} and g_m Vs V_{gs} Curves



From the graph we can see that the derivative is linear for V_{gs} in range of -430mV and -646mV.

$$V_{gs0} = \frac{-430mV - 646mV}{2} = -538mV \quad (19)$$

$$g_{m0} = g_m|_{V_{gs0}} = -8.68\mu\Omega^{-1} \quad (20)$$

From the extension of tangent to x-axis we get

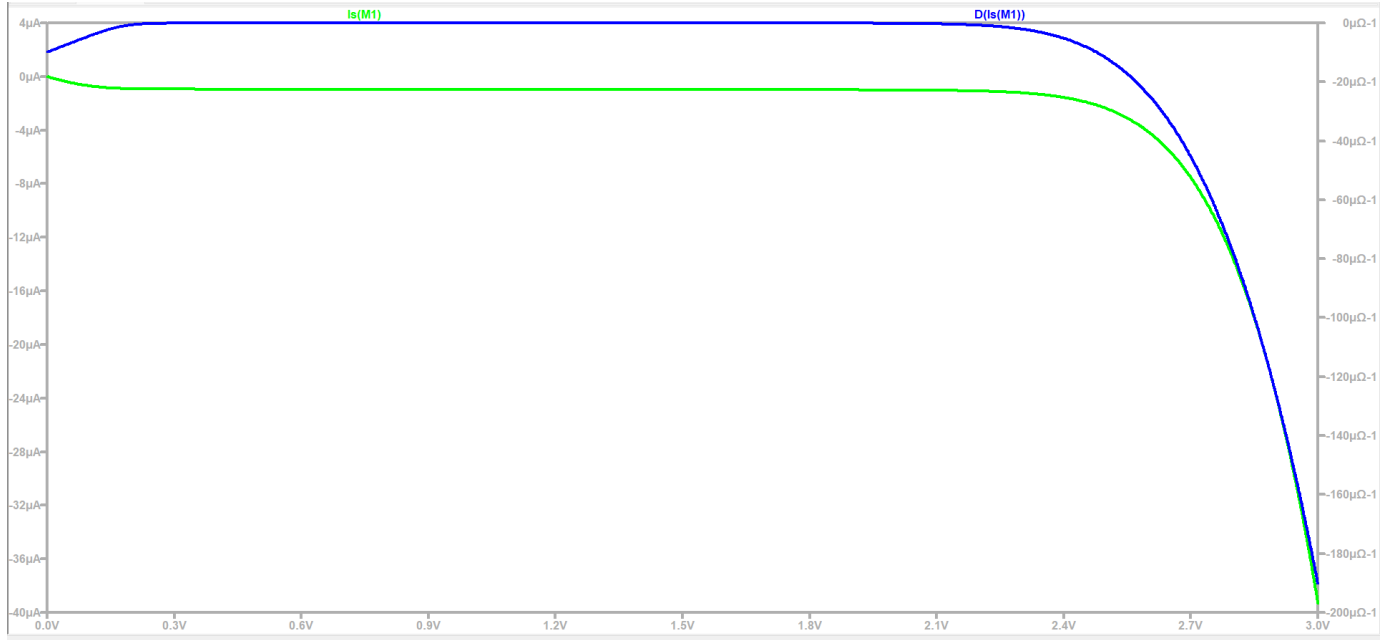
$$V_T = -350mV \quad (21)$$

Now using the below equation

$$\frac{\beta W(V_{gs0} - V_T)}{L} = g_{m0} \quad (22)$$

$$\beta = 491\mu$$

I_{ds} Vs V_{ds} and g_{ds} Vs V_{ds} Curves



from the graph g_{ds0} : g_{ds} at V_{gs0} and $V_{ds} = 0.55V$.

$$g_{ds0} = -30n\Omega^{-1} \quad (23)$$

From the below equation we can calculate λ .

$$I_{ds}\lambda = g_{ds} \quad (24)$$

$$\lambda = 0.0315.$$

Now to find range of V_{gs} with less than 50 % Error we need to find values in range of $(g_m/2, 3g_m/2)$.

$$V_{gs} \text{ range} = (-452mV, -633mV)$$

.

Thank
you