## ASSIGNMENT 9

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Question:;

 $The logic gates shown in the digital circuit below uses trong pull-down nMOS transistors for LOW logic downs are of f, high-value resistors set the output logic levels to HIGH (i.e. the pull-upsare weak). Note that some nodes are intentionally shorted to implement \wired logic". Such shorted nodes will be HIGH only if the outputs of all the gates whose outputs are shorted are HIGH.$ 

The number of distinct values of X3X2X1X0 (out of the 16 possible values) that give Y=1 is

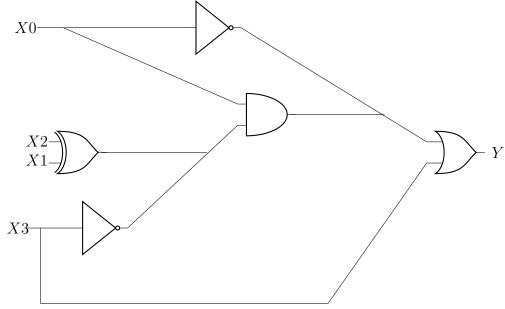
ANSWERIS: 8 X0X1X2X3

IfX3 = 0, thenbandonX1

ifX1 = 0 output is 0

X1 = 1 output is 0

Soonly8inputs



 $\overline{BOOLEANEXPRESSION}: Y = (X0)(X3')(X1(X2') + X2(X1')) + (X0') + X3$