

ASSIGNMENT 9

Beerelly srinitha

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Question ::

The logic gates shown in the digital circuit below use strong pull-down nMOS transistors for LOW logic levels. High-value resistors set the output logic level to HIGH (i.e. the pull-ups are weak). Note that some nodes are intentionally shorted to implement "wired logic". Such shorted nodes will be HIGH only if the output of all the gates whose outputs are shorted are HIGH.

The number of distinct values of $X_3X_2X_1X_0$ (out of the 16 possible values) that give $Y = 1$ is

ANSWER IS : 8 $X_0X_1X_2X_3$

If $X_3 = 0$, then bandon X_1

if $X_1 = 0$ output is 0

$X_1 = 1$ output is 0

So only 8 inputs

