Cour		USA20302J	300000	urse ame		(PERATING SYSTEMS			Categ			С			Pr	ofes	sion	al Co	ore	Cou	ırse				L 4	T 0	P 4	C 6
	irses	8		01	- 4	Cours	7.70	10 1	-101 -1			ress	sive es							"	Nil	İ							- 1
Course	Offerir	ng Departmer	nt	Computer App	olicati	ons	Data Bool	k / Code	es/Standar	rds	-	-							N	II									
Course (CLR):		ing Rationale		The purpose o	f lear	ning this	course is to:				Le	arnii	ng					Prog	ram	Lea	rnin	ng O	utco	mes	(PL	0)	200	20	
				s based on its							1	2	3		2	2 3	-	5	i (6	7	8	9	10	11	12	13	14	15
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							s of an Operating system			177	E	%)	(%)	-	afinamenta de la constanta de	Eld'	5	ם מ	= -	knowledge		ta		S	S			_	
							ng system functions		-		ĕ	ncy	ent			Collegate		di di	allo		.00,000	Data	S	Skills	Skills			avio	D
CLR-5 : Utilize the File Management functions of an Operating system CLR-6 : Analyze the practical operating systems and evaluate their utility							ing	icie	inm	3		3	nea	1 5		975	g	pre	Skills		150.000	S		Behavior	i.				
OLIVO	. ¡Aliqi	yze the practi	oai op	orating system	iio uii	a cvaldat	c their titlinty			10-1	of Thinking (Bloom)	pected Proficiency (%)	pected Attainment	1	nualization of	o Homonida	ik with Related Disciplines	Oceanial Milowiedge	III opecialization	IIIITY TO UNIZE	in Modeling	ze, Interpret	Investigative S	Problem Solving	Communication	ical Skills	Skills	ofessional E	e Long Learning
Course (CLO):		ing Outcomes	5	At the end of t	his c	ourse, lea	rners will be able to:		N. Va		<u>e</u>		Expec		Applied A		LINK	Chille in	SILINO	Apillity	S	Analyze,	nvest	Proble	Comr	Analytical	ICT SI	Profes	Life Lo
		ify functions of	of an o	perating syste	em, a	nalyze the	e process management fur	nctions		417		80	70	i i	1 1	-	1 1	1 1	1 1	1	L	M	H	M	-	Н	H	Н	M
	CLO-2 : Analyze CPU scheduling and synchronization process of an operating system						71.	3	85	75	F	1 1	1 1	1 1	1 1	1	Λ	L	M	Н	М	-	Н	Н	Н	M			
CLO-3	: Analy	yze the need	of Me	mory manage	ment	functions	of an operating system	11	0.1		3	75	70	H	1 1	1 1	1 1	1 1	1	Λ	L	M	Н	М	-	Н	Н	Н	М
	_						file management's role of a				-		80	H	1 1	1	1 1	1 1	1	Λ	L	M	Н	М	-	Н	Н	Н	М
CLO-5	: Ident	ify the essent	tials o	finter process	com	municatio	n in an operating system, e	evaluate	e hyperviso	ors	3	85	75	1	1 1	1	1 1	1	1	_	L	M	Н	М	-	Н	Н	Н	M
CLO-6	. Analy opera	yze how operating environr	ating a	systems are c	onstri	ucted, and	alyze the features and aspe	ects of d	different	<u>gir</u>	3	80	70	ŀ	1 1	1 1	1 1	1	1 1	Л	L	M	Н	М	-	Н	Н	Н	М
Duratio	on (hou	r)		24	G		24		1.1	24					7		24	-		i					2	24			
	SLO-1	Introduction (OS):	n Ope	erating System	S	Process	concept : Introduction		ess Synchi ground	Synchronization - und						Memory managem				ament:									
S-1	SLO-2		syste	m ov <mark>erview</mark> m as a resour	се	0.50	states : Process creation cess termination	The C	Critical sec	ction prob	olem	1		Deadle	Deadlocks - System model Memory management: introduction			ι.											
	SLO-1	Operations loader, link		embler, Comp	ler,	Process	state transition diagram	Two p	orocess So	olutions		l di	II.	Deadlock characterization -															
S-2	SLO-2		cessin	erating System g and batch		Operation	on on process	Multi	process S	Solutions					Necessary conditions Logical Vs physical address			ss sp	ace										
	SLO-1	Batch: Simple, Multiprogramming											Resource Allocation Graph					ò	Swapping										
S-3	SLO-2	Multiproces parallel sys	100	Time Sharing,		Symmet	ric multiprocessing	Synch	hronization	n hardwa	dware solution Methods for handling deadlocks				Organization : physical and logical organization														
S-4	SLO-1	Distributed	l (clier	nt-server, peer	-to-	Concurr	ent process	Sema	phores –	Usage				Deadle	ock F	reve	entio	n - M	utua	ı	$\overline{}$	_		allo		n me	ethod	t	

	6	peer), Real-Time (hard, soft			exclusion, Hold and Wait		
	SLO-2	Clustering (symmetric, asymmetric, parallel)), Network,)			No Preemption, Circular Wait	Single partition allocation	
S 5-8		Lab 1: Comparison between various Operating Systems	Lab 4: Simulation of FCFS CPU scheduling algorithm	Lab 7: Write a procedure for timer interrupt handler	Lab 10: Program to implement Bankers Algorithm	Lab 13: multiple partition (dynamic)	
S-9		Microkernel: Architecture, Kernel mode, user mode, Monolithic, differences	CPU Scheduling: Process Scheduler (long, short, medium term)	Semaphores –Implementation	Deadlock Avoidance - Safe state	Multiple partition memory management :	
88	SLO-2	System Call Types	Scheduling criteria	Binary semaphores	Resource Allocation Graph Algorithm	contiguous (fixed, dynamic)	
S-10	SLO-1	((a) process control: fork(), exit(), wait()b)file manipulation: open(), read(), write(), close() (c)device mgt: ioctl(), read(), write()	(a) turnaround (b) waiting (c)	Classic Problems of Synchronization - The Bounded Buffer problem	Banker's Algorithm - Safety Algorithm	Contiguous Types: memory protection, allocation, fragmentation (c) partitioned	
	SLO-2	b)file manipulation: open(), close()	Scheduling Types: FCFS, SJF	The Readers - Writers Problem		Compaction	
S-11	SLO-1	read(), write()	Priority Scheduling: Preemptive,	The Dinning philosophers problem	Resource request algorithm	Paged memory management,	
	SLO-2		non-preemptive			Paging technique	
S-12	SLO-1 SLO-2	Operating System services		Critical Regions: Race condition and process synchronization	Examples	Segmentation Segmentation with paging	
S 13-16	SLO-1 SLO-2	Lab 2: Booting process in GNU/Linux OS	II an a Phoniv CPLI schoolilling	Lab 8: classical inter process communication problem (Producer consumer)	Lab 11: Program to implement memory allocation with pages	Lab 14 : Simulation of FIFO page replacement algorithm	
C 17	SLO-1	System Programs: file	multilevel queue	Implementation of Critical region	Deadlock Detection - Single instance of each resource type	Domand nasina	
S-17	SLO-2	management status info	Imilillever leedback offere	Mutual Exclusion Algorithm: Peterson, Monitors	Several instances of a Resource type	Demand paging	
S-18	SLO-1	File modification, language support Loading and execution, communications,	multiple processor Scheduling	Producer consumer problem		Page replacement algorithms	
	SLO-2	Communications Threads: Single thread, Multi-thread			Process termination	Page Replacement - FIFO Page replacement	
	SLO-1	Operating System structure					
S-19	SLO-2	Layered approach Micro kernels	Real time scheduling	IPC : Inter process communication	Resource preemption	Optimal	
0.00		Multithreading	Performance comparison	Message passing	Concurrency mechanism	LRU page replacement	
S-20		Symmetric multiprocessing		Bounded Buffer Problem	Comparison between deadlock and starvation	Thrashing	
S 21-24	SLO-1 SLO-2	Lab 3: Multi-thread Programming	CPU scheduling algorithm	Lab 9: Write a procedure to make message passing in inter process communication	Lab 12: Simulation of FIFO page replacement algorithm	Lab 15: Simulation of optimal page replacement algorithm	

	1.	Abraham Silberschatz, Peter Baer Galvin, Greg Gagne, (2013), "Operating Systems", 9th Ed., 3. Andrew S. Tanenbaum, Herbert Bos, (2015), "Modern Operating Systems", 4th	
Learning	5-655-7	John Wiley & Sons Ed., Pearson	
Resources	2.	William Stallings, (2012), "Operating Systems-Internals and Design Principles", 7th Ed., 4. Bryant O'Hallaxn, (2015), "Computer systems- A Programmer's Perspective",	
		Prentice Hall Pearson	

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Level	Bloom's Level of	CLA -	1 (10%)		s Learning Asse 2 (10%)	•	3 (20%)	CLA - 4	(10%)#	Final Examination (50% weightage)		
	Thinking	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	Theory	Practice	
Laval 1	Remember	200/	20%	15%	15%	15%	15%	15%	15%	15%	150/	
_evel 1	Understand	20%	20%	1376	1376	1376	13 /6	13 /6	10 /0	13 /0	15%	
Level 2	Apply	20%	20%	20%	20%	20%	20%	20%	20%	20%	20%	
Level Z	Analyze	2076	2076	20%	2076	20 %	2076	2076	2070	2076	2076	
Level 3	Evaluate	10%	10%	15%	15%	15%	15%	15%	15%	15%	15%	
	Create	10%	10%	15%	1370	1376	13%	13%	15%	1376	1376	
	Total	10	0 %	10	0 %	10	0 %	100) %	10	0 %	

CLA – 4 can be from any combination of these: Assignments, Seminars, Tech Talks, Mini-Projects, Case-Studies, Self-Study, MOOCs, Certifications, Conf. Paper etc.,

Course Designers		
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