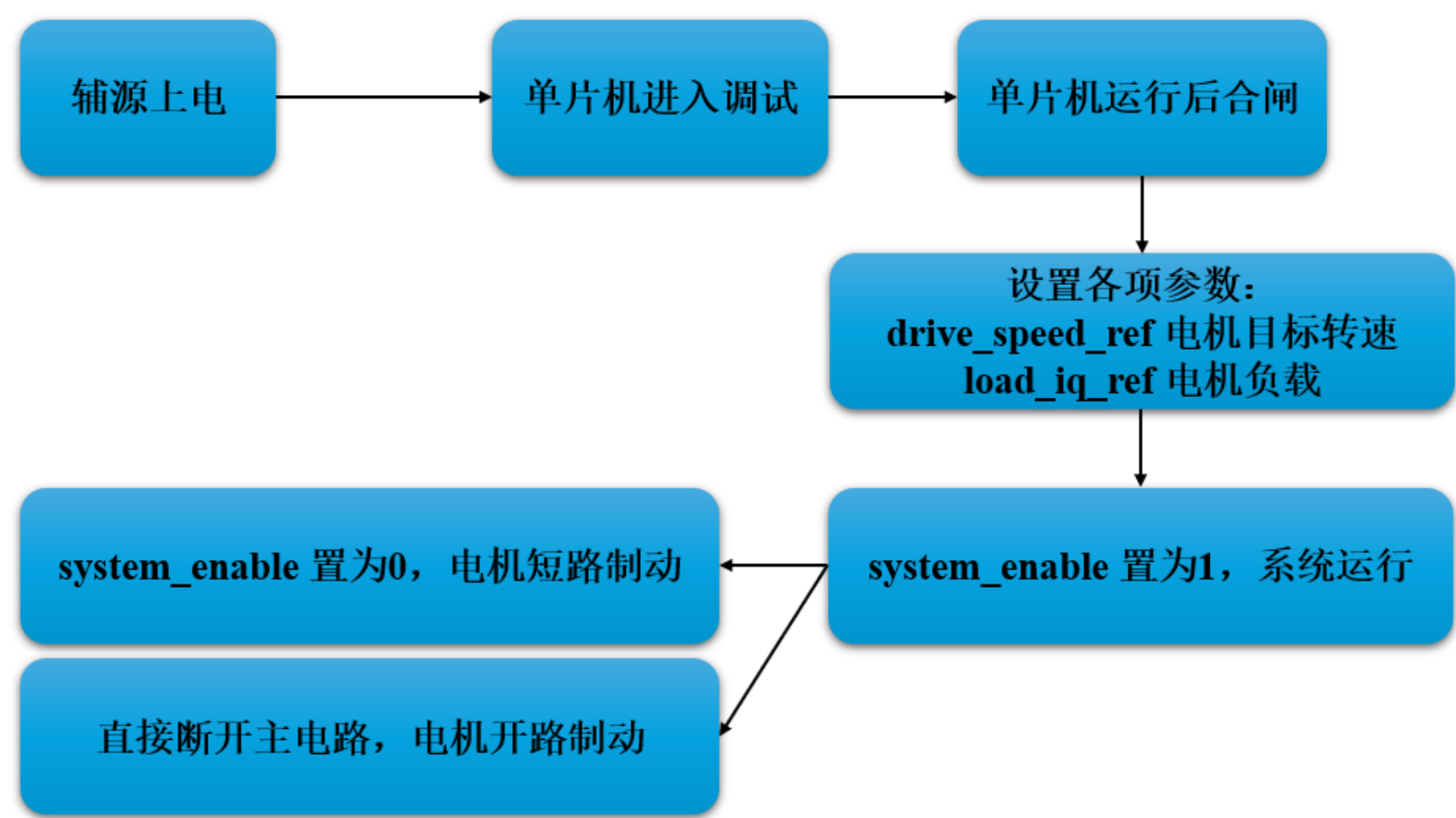


# Chapter1 快速上手

## 1. 运行流程



# Chapter2 CubeMX 配置指南

## 1. 旋转变压器(AD2S1210)配置

### GPIO 配置

GPIO 参考配置如下:

- 驱动电机旋转变压器:

引脚	默认输出电平(GPIO output level)	GPIO模式(GPIO mode)	上拉/下拉(GPIO Pull-up/Pull down)	输出速率(Maximum output speed)	旋变引脚
PA11	High	Output Push Pull	Pull-up	High	AD2S1_PCS
PA15(JTDI)	Low	Output Push Pull	Pull-up	Low	AD2S1_A0
PB2	High	Output Push Pull	Pull-up	Low	AD2S1_RD
PB6	Low	Output Push Pull	Pull-up	Low	AD2S1_DIR
PB7	High	Output Push Pull	Pull-up	High	SPI1_CS
PB9	Low	Output Push Pull	Pull-up	Low	AD2S1_A1
PF0	Low	Output Push Pull	Pull-up	High	AD2S1_SAM
PF1	High	Output Push Pull	Pull-up	Low	AD2S1_RESET

- 负载电机旋转变压器：

引脚	默认输出电平(GPIO output level)	GPIO模式(GPIO mode)	上拉/下拉(GPIO Pull-up/Pull down)	输出速率(Maximum output speed)	旋变引脚
PB10	High	Output Push Pull	Pull-up	Low	AD2S2_PCS
PB12	Low	Output Push Pull	Pull-up	Low	AD2S2_A0
PC2_C	Low	Output Push Pull	Pull-up	Low	AD2S2_SAM
PC3_C	High	Output Push Pull	Pull-up	Low	AD2S2_RESET
PC13	High	Output Push Pull	Pull-up	Low	AD2S2_RD
PE0	Low	Output Push Pull	Pull-up	Low	AD2S2_DIR
PE1	High	Output Push Pull	Pull-up	Low	SPI2_CS
PE4	Low	Output Push Pull	Pull-up	Low	AD2S2_A1

## SPI 配置

SPI 参考配置如下，一般配置 SPI 波特率在 25 MBits/s 以下：

SPI1 Mode and Configuration

Mode

ModeFull-Duplex Master

Hardware NSS SignalDisable

Configuration

Reset Configuration

Parameter Settings

User Constants

NVIC Settings

DMA Settings

GPIO Settings

Configure the below parameters :

Search (Ctrl+F)

Basic Parameters

Frame Format

Data Size

First Bit

Clock Parameters

Prescaler (for Baud Rate)

Baud Rate

Clock Polarity (CPOL)

Clock Phase (CPHA)

Advanced Parameters

CRC Calculation

NSSP Mode

NSS Signal Type

Fifo Threshold

Tx Crc Initialization Pattern

Rx Crc Initialization Pattern

Nss Polarity

Master Ss Idleness

Master Inter Data Idleness

Master Receiver Auto Susp

Master Keep Io State

IO Swap

Motorola

8 Bits

MSB First

8

22.916666 MBits/s

Low

2 Edge

Disabled

Enabled

Software

Fifo Threshold 01 Data

All Zero Pattern

All Zero Pattern

Nss Polarity Low

00 Cycle

00 Cycle

Disable

Master Keep Io State Disable

Disabled

8位数据

高位先行

预分频器(选择波特率)

CPOL 选择 Low

CPHA 选择 2 Edge

其余选项保持默认

SPI 的引脚:

引脚	SPI引脚
PB3(JTDO/TRACESWO)	SPI1_SCK
PB4(NJTRST)	SPI1_MISO
PB5	SPI1_MOSI
PB13	SPI2_SCK
PB14	SPI2_MISO
PB15	SPI2_MOSI

## 2. 逆变器配置

### TIM 配置

### 引脚分配

TIM1 负责负载电机的逆变器控制，TIM8 负责驱动电机的逆变器控制。TIM 的引脚分配如下：

引脚	TIM引脚
PA7	TIM8_CH1N
PB0	TIM8_CH2N
PB1	TIM8_CH3N
PC6	TIM8_CH1
PC7	TIM8_CH2
PC8	TIM8_CH3
PE8	TIM1_CH1N
PE9	TIM1_CH1
PE10	TIM1_CH2N
PE11	TIM1_CH2
PE12	TIM1_CH3N
PE13	TIM1_CH3

CHx 对应逆变器上桥臂，CHxN 对应逆变器下桥臂。

对应的 TIM 配置如下：

TIM1 Mode and Configuration

Mode

Slave Mode

Disable

▼

Trigger Source

Disable

▼

Clock Source

Internal Clock

▼

Channel1

PWM Generation CH1 CH1N

1通道互补输出(U相)

▼

Channel2

PWM Generation CH2 CH2N

2通道互补输出(V相)

▼

Channel3

PWM Generation CH3 CH3N

3通道互补输出(W相)

▼

Channel4

Disable

▼

Channel5

Disable

▼

Channel6

Disable

▼

Combined Channels

Disable

▼

Activate-Break-Input

Disable

▼

Activate-Break-Input-2

Disable

▼

Use ETR as Clearing Source

Disable

▼

☐ XOR activation

☐ One Pulse Mode

TIM8 Mode and Configuration

Mode

Slave Mode

Disable

▼

Trigger Source

Disable

▼

Clock Source

Internal Clock

▼

Channel1

PWM Generation CH1 CH1N

1通道互补输出(U相)

▼

Channel2

PWM Generation CH2 CH2N

2通道互补输出(V相)

▼

Channel3

PWM Generation CH3 CH3N

3通道互补输出(W相)

▼

Channel4

PWM Generation No Output

4通道不输出，用于触发ADC采样

▼

Channel5

Disable

▼

Channel6

Disable

▼

Combined Channels

Disable

▼

Activate-Break-Input

Disable

▼

Activate-Break-Input-2

Disable

▼

Use ETR as Clearing Source

Disable

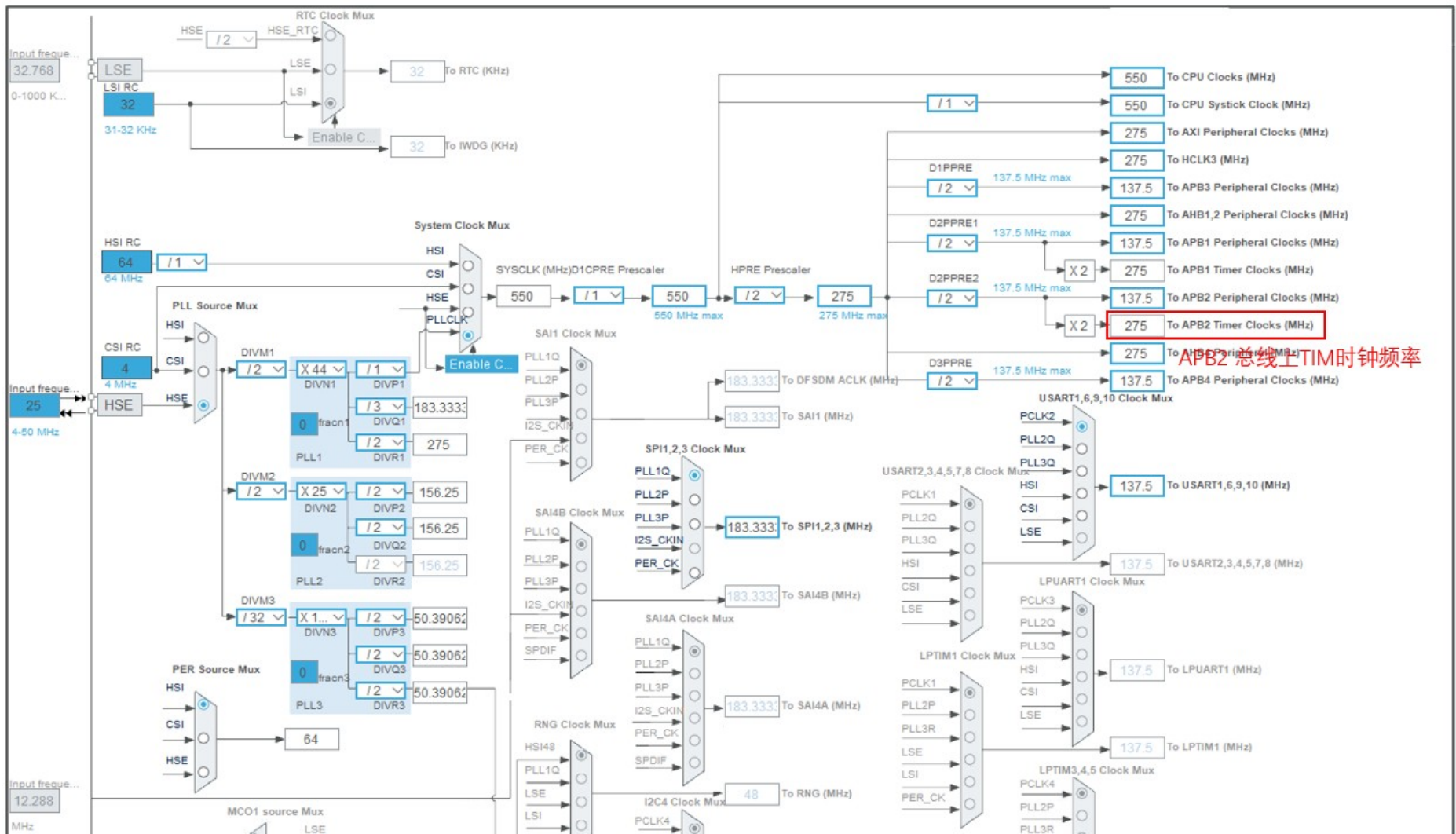
▼

☐ XOR activation

☐ One Pulse Mode

时基单元配置

在时钟树中可以看到 TIM1 和 TIM8 的输入频率均为 275MHz(TIM1 和 TIM8 在 APB2 总线上):



时基单元配置如下:

TIM8 Mode and Configuration

Configuration

Reset Configuration

Parameter Settings

User Constants

NVIC Settings

DMA Settings

GPIO Settings

Configure the below parameters :

Search (Ctrl+F)

Counter Settings

Trigger Output (TRGO) Parameters

Break And Dead Time management - BRK Configuration

Break And Dead Time management - BRK2 Configuration

Break And Dead Time management - Output Configuration

Clear Input

PWM Generation Channel 1 and 1N

PWM Generation Channel 2 and 2N

PWM Generation Channel 3 and 3N

PWM Generation Channel 4

Prescaler (PSC - 16 bits value)

Counter Mode

Counter Period (AutoReload Register - 16 bits value )

Internal Clock Division (CKD)

Repetition Counter (RCR - 16 bits value)

auto-reload preload

11-1

Center Aligned mode1

1250

No Division

1

Disable

TIM8 预分频值(PSC)

TIM8 计数模式

TIM8 重装载值(ARR)

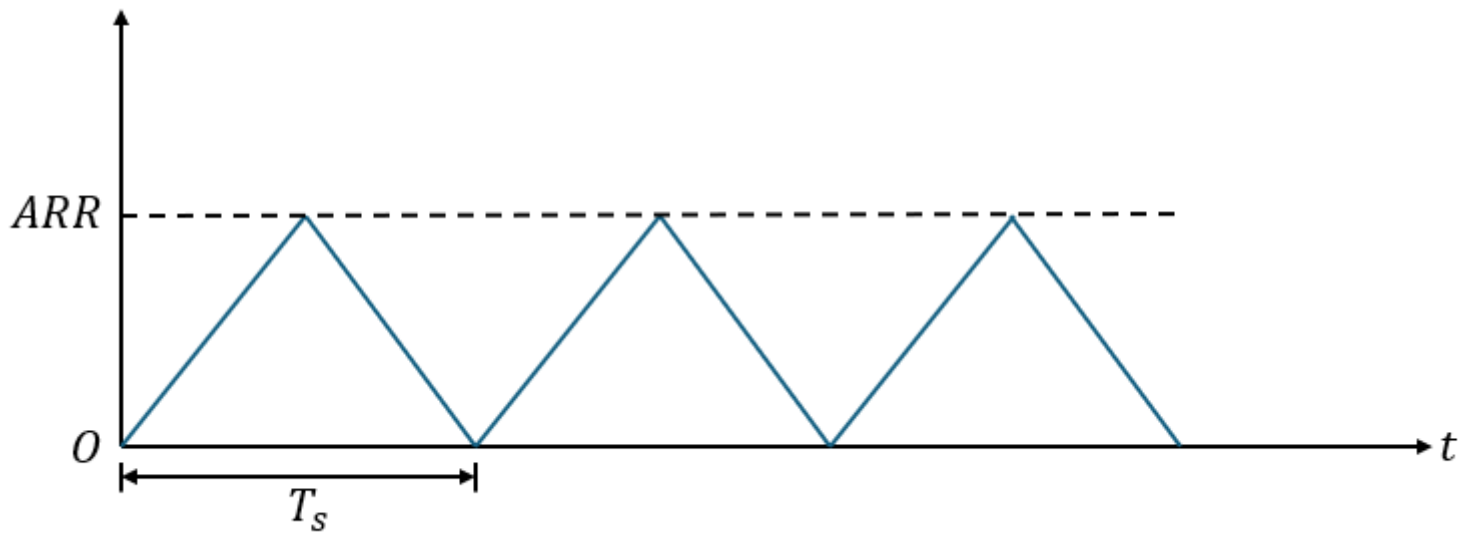
TIM8 重复计数器值(RCR)

不使能ARR影子寄存器

1. 定时器单次计数频率:

$$f_{TIM} = \frac{f_{APB}}{PSC + 1} = \frac{275MHz}{11} = 25MHz$$

2. 由于使用中心对齐模式, PWM 载波波形如下:

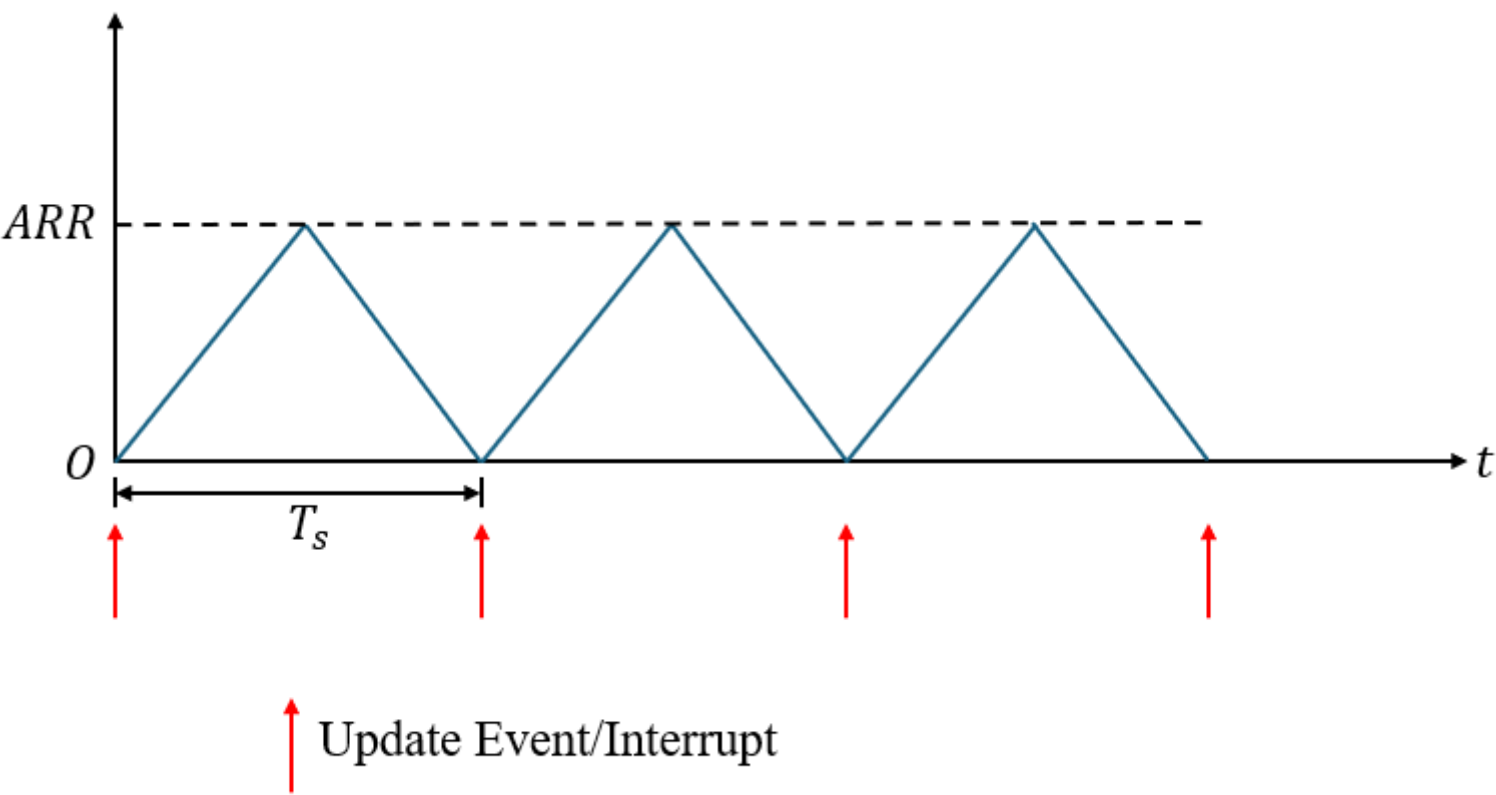




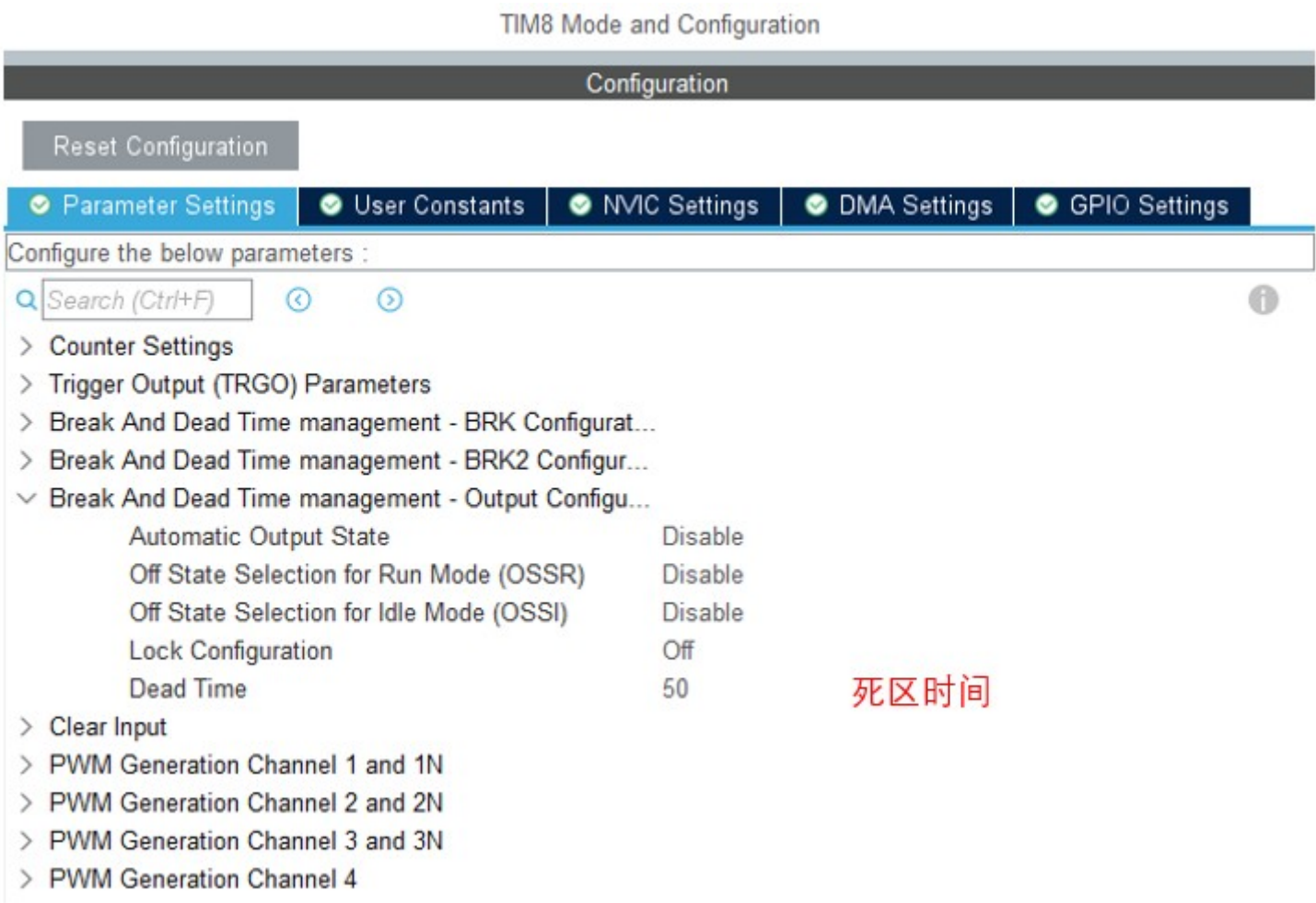
载波频率(逆变器开关频率):

$$f_s = \frac{f_{TIM}}{2 \times ARR} = \frac{25MHz}{2 \times 1250} = 10kHz$$

3. 配置重复计数器 RCR 为 1, 此时 TIM 定时器更新事件/更新中断在下图所示的地方产生:



死区配置



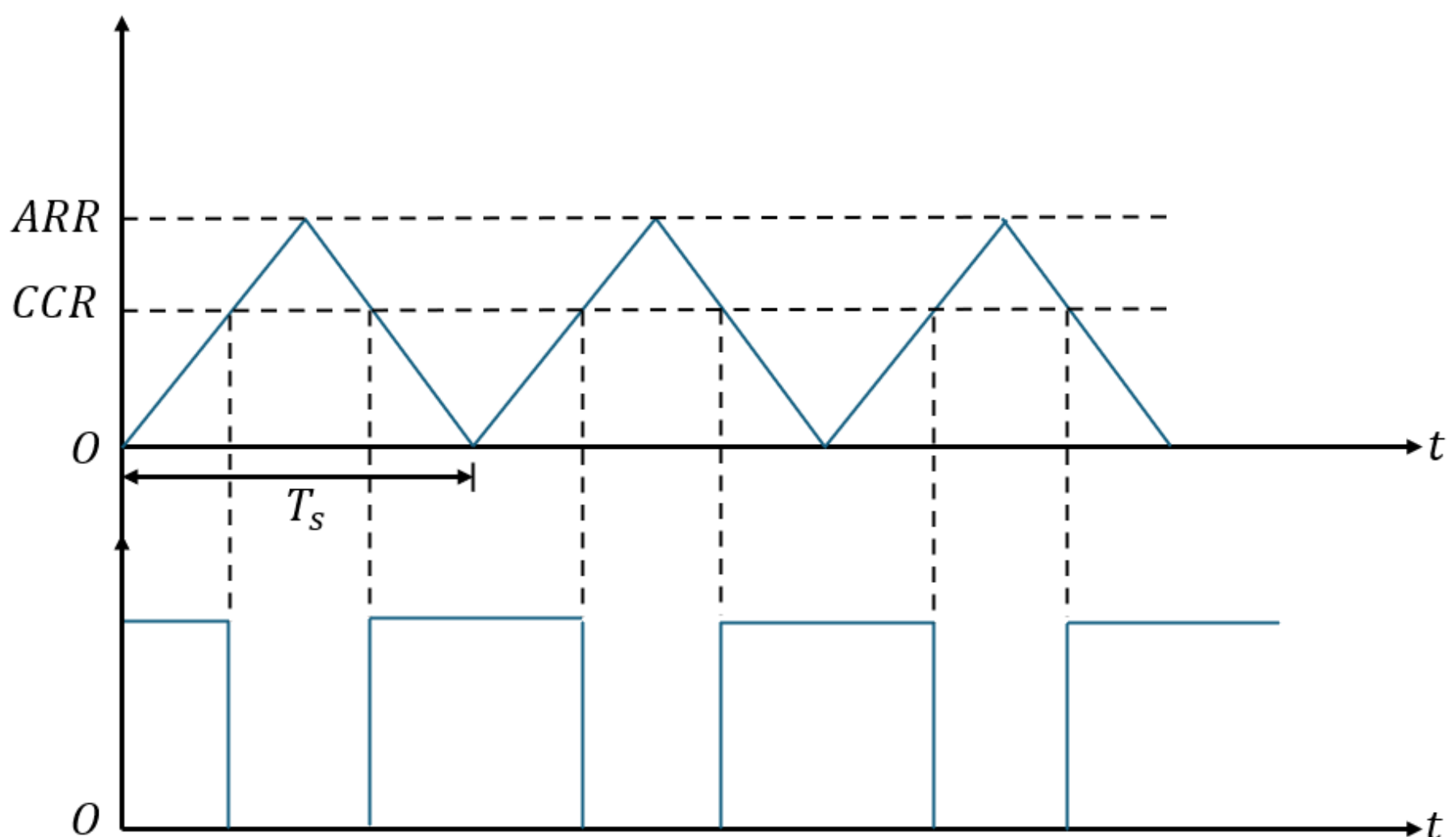
死区时间:

$$T_s = Dead\_Time \times \frac{1}{f_{TIM}} = 50 \times \frac{1}{275MHz} = 181ns$$

PWM 输出配置

Configuration																																																	
Reset Configuration																																																	
✓ Parameter Settings	✓ User Constants																																																
✓ NVIC Settings	✓ DMA Settings																																																
✓ GPIO Settings																																																	
Configure the below parameters :																																																	
<input type="text" value="Search (Ctrl+F)"/>																																																	
<ul style="list-style-type: none"> <li>&gt; Counter Settings</li> <li>&gt; Trigger Output (TRGO) Parameters</li> <li>&gt; Break And Dead Time management - BRK Configura...</li> <li>&gt; Break And Dead Time management - BRK2 Configur...</li> <li>&gt; Break And Dead Time management - Output Config...</li> <li>&gt; Clear Input</li> <li>✓ PWM Generation Channel 1 and 1N           <table border="0"> <tr> <td>Mode</td> <td>PWM mode 1</td> </tr> <tr> <td>Pulse (16 bits value)</td> <td>0</td> </tr> <tr> <td>Output compare preload</td> <td>Enable</td> </tr> <tr> <td>Fast Mode</td> <td>Enable</td> </tr> <tr> <td>CH Polarity</td> <td>High</td> </tr> <tr> <td>CHN Polarity</td> <td>High</td> </tr> <tr> <td>CH Idle State</td> <td>Reset</td> </tr> <tr> <td>CHN Idle State</td> <td>Reset</td> </tr> </table> </li> <li>✓ PWM Generation Channel 2 and 2N           <table border="0"> <tr> <td>Mode</td> <td>PWM mode 1</td> </tr> <tr> <td>Pulse (16 bits value)</td> <td>0</td> </tr> <tr> <td>Output compare preload</td> <td>Enable</td> </tr> <tr> <td>Fast Mode</td> <td>Enable</td> </tr> <tr> <td>CH Polarity</td> <td>High</td> </tr> <tr> <td>CHN Polarity</td> <td>High</td> </tr> <tr> <td>CH Idle State</td> <td>Reset</td> </tr> <tr> <td>CHN Idle State</td> <td>Reset</td> </tr> </table> </li> <li>✓ PWM Generation Channel 3 and 3N           <table border="0"> <tr> <td>Mode</td> <td>PWM mode 1</td> </tr> <tr> <td>Pulse (16 bits value)</td> <td>0</td> </tr> <tr> <td>Output compare preload</td> <td>Enable</td> </tr> <tr> <td>Fast Mode</td> <td>Enable</td> </tr> <tr> <td>CH Polarity</td> <td>High</td> </tr> <tr> <td>CHN Polarity</td> <td>High</td> </tr> <tr> <td>CH Idle State</td> <td>Reset</td> </tr> <tr> <td>CHN Idle State</td> <td>Reset</td> </tr> </table> </li> </ul>		Mode	PWM mode 1	Pulse (16 bits value)	0	Output compare preload	Enable	Fast Mode	Enable	CH Polarity	High	CHN Polarity	High	CH Idle State	Reset	CHN Idle State	Reset	Mode	PWM mode 1	Pulse (16 bits value)	0	Output compare preload	Enable	Fast Mode	Enable	CH Polarity	High	CHN Polarity	High	CH Idle State	Reset	CHN Idle State	Reset	Mode	PWM mode 1	Pulse (16 bits value)	0	Output compare preload	Enable	Fast Mode	Enable	CH Polarity	High	CHN Polarity	High	CH Idle State	Reset	CHN Idle State	Reset
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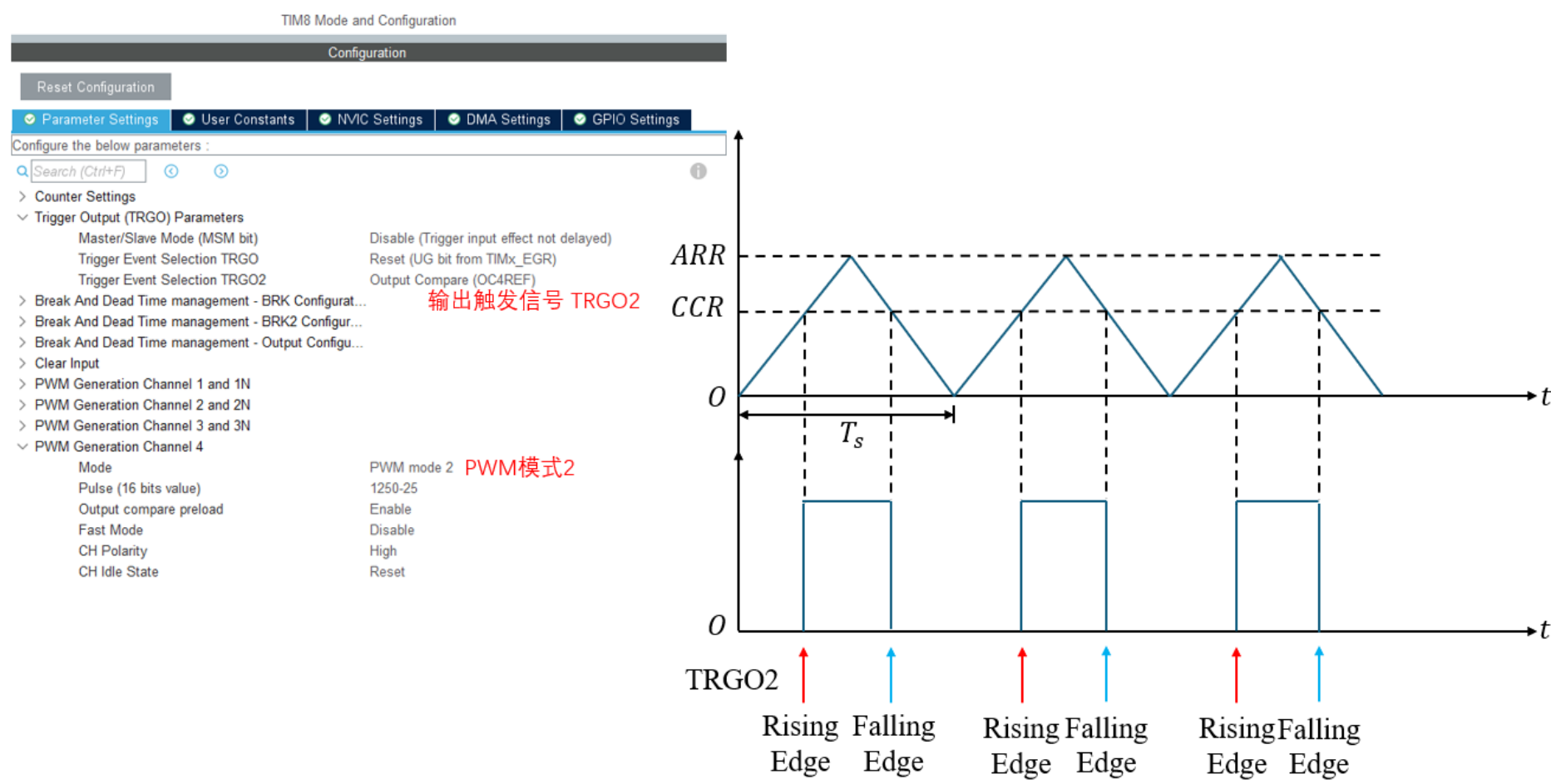
此时的 PWM 波形(逆变器上桥臂开关信号)如下:



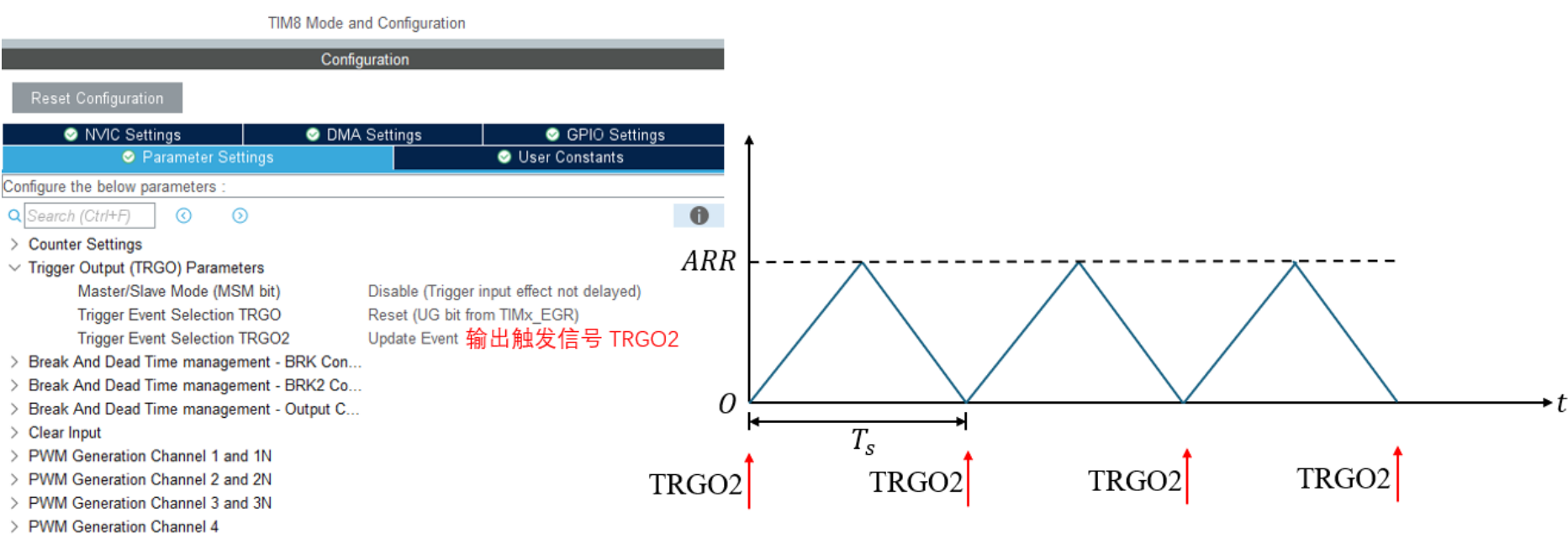
ADC 触发信号配置

配置模式1

使用 PWM 通道 4 形成 TRGO2 触发信号。



配置模式2



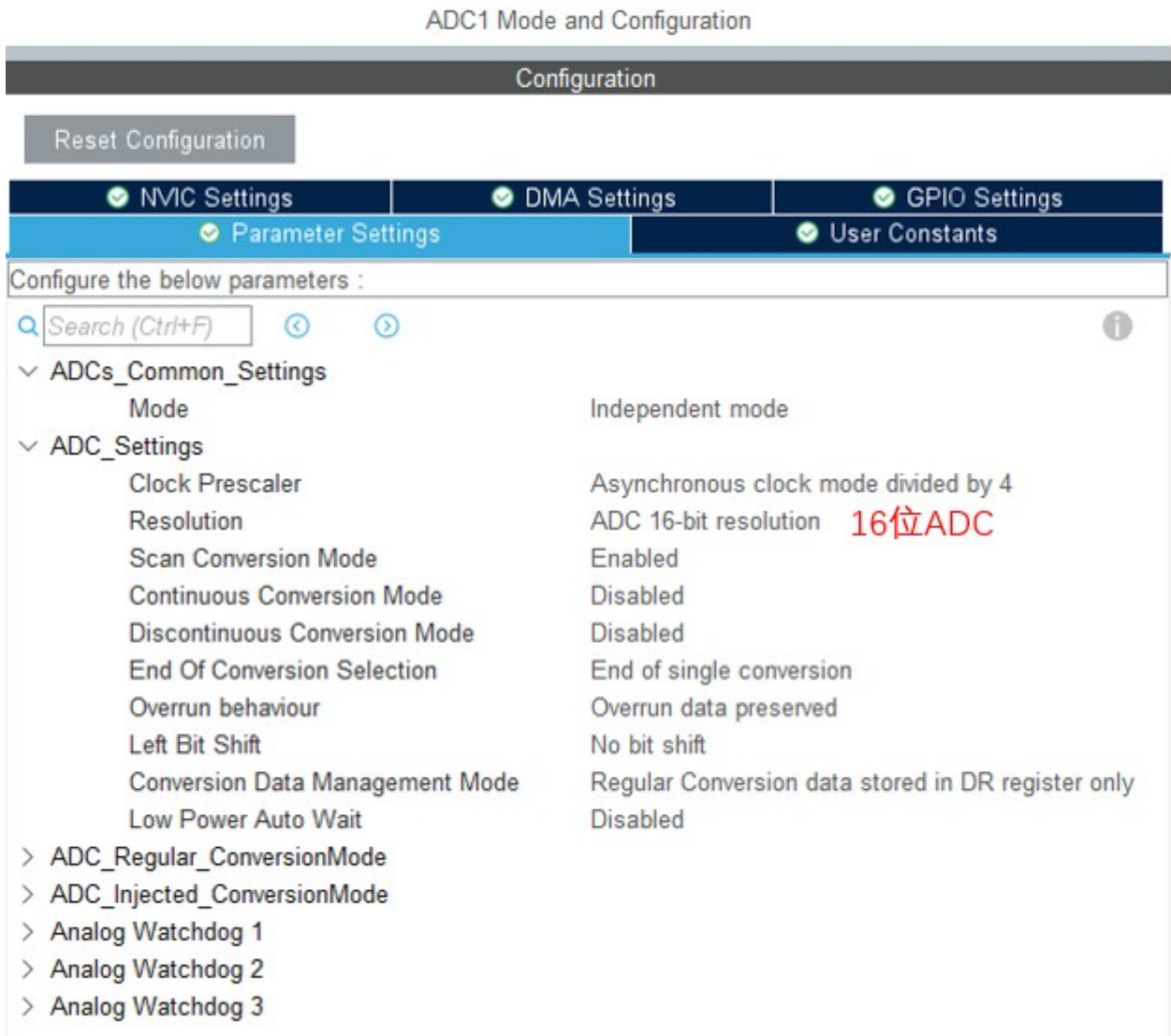
3. 采样配置

引脚分配

引脚	ADC引脚	用途
PA2	ADC1_INP14	负载电机u相
PA3	ADC1_INP15	负载电机v相
PC4	ADC1_INP4	驱动电机v相
PC5	ADC1_INP8	驱动电机u相



ADC基础配置



注入组配置

ADC 的注入组可以使用采用事件触发机制，且优先级最高，适合进行电流采样。



ADC1 Mode and Configuration

Configuration

Reset Configuration

✓ NVIC Settings    ✓ DMA Settings    ✓ GPIO Settings

✓ Parameter Settings    ✓ User Constants

Configure the below parameters :

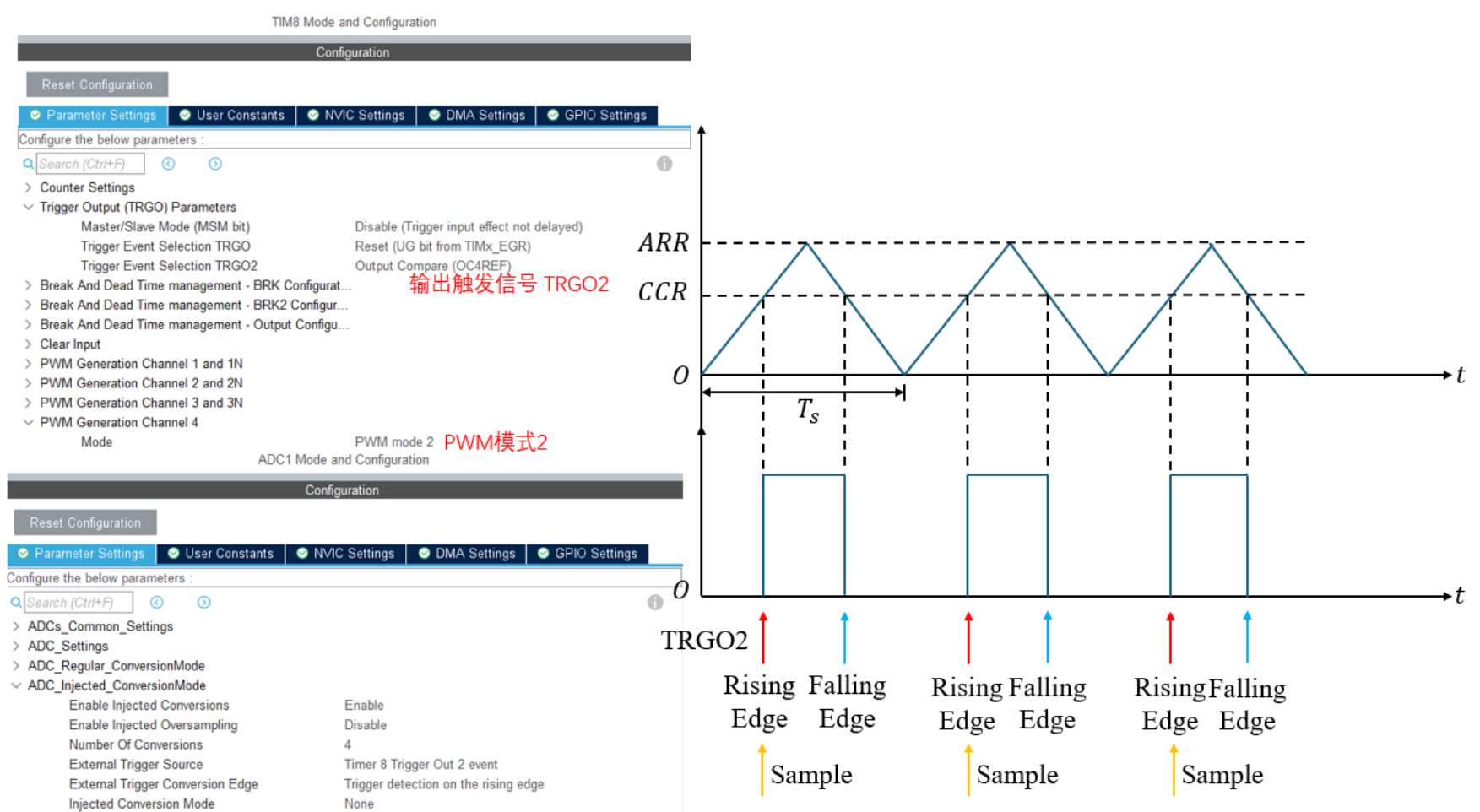
Search (Ctrl+F)

ADC\_Injected\_ConversionMode

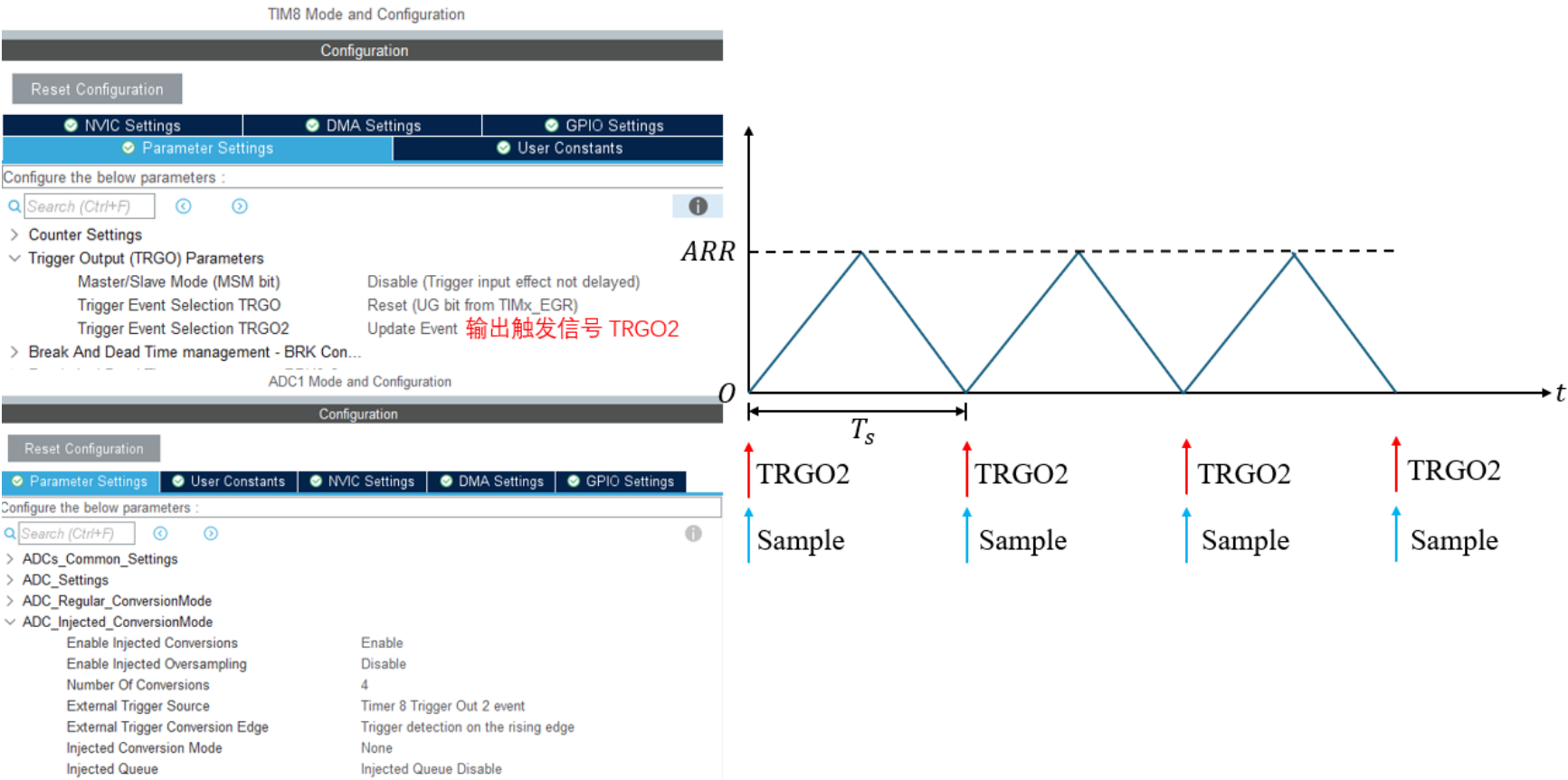
Enable Injected Conversions	Enable
Enable Injected Oversampling	Disable
Number Of Conversions	4
External Trigger Source	Timer 8 Capture Compare 4 event
External Trigger Conversion Edge	Trigger detection on the rising edge
Injected Conversion Mode	None
Injected Queue	Injected Queue Disable
Rank	1
Channel	Channel 4
Sampling Time	1.5 Cycles
Offset Number	No offset
Offset Signed Saturation	Disable
Injected Offset Saturation	Disable
Rank	2
Channel	Channel 8
Sampling Time	1.5 Cycles
Offset Number	No offset
Offset Signed Saturation	Disable
Injected Offset Saturation	Disable
Rank	3
Channel	Channel 14
Sampling Time	1.5 Cycles
Offset Number	No offset
Offset Signed Saturation	Disable
Injected Offset Saturation	Disable
Rank	4
Channel	Channel 15
Sampling Time	1.5 Cycles
Offset Number	No offset
Offset Signed Saturation	Disable
Injected Offset Saturation	Disable

- 配置模式1

ADC 的触发事件可以为 Capture Compare 4 event :



- 配置模式2

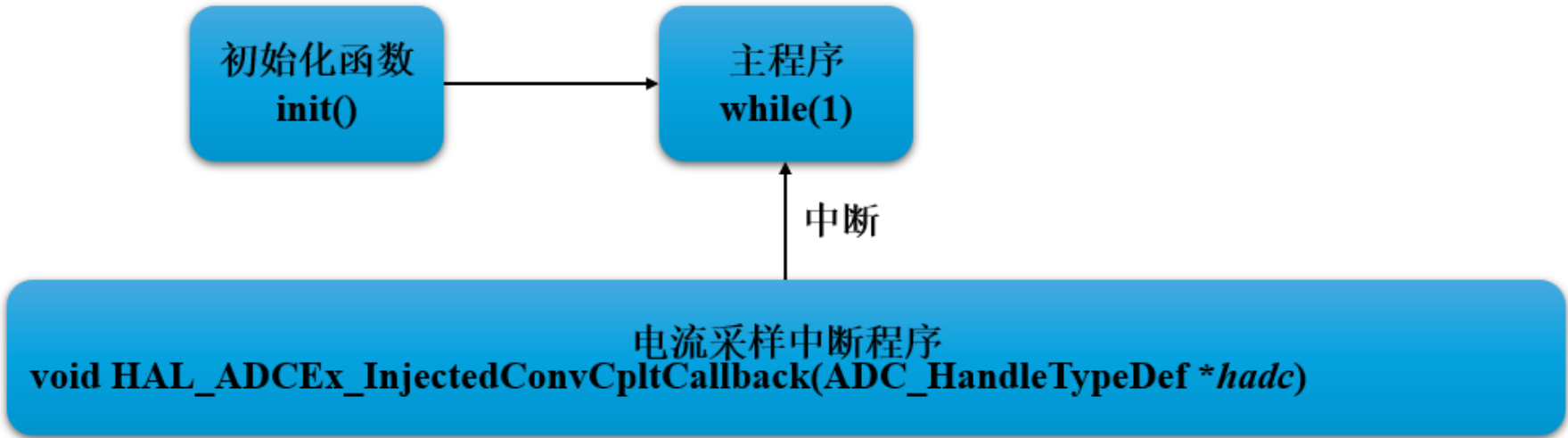


采样完成后会触发注入组采样中断，在 CubeMX 里面使能中断：

ADC1 Mode and Configuration			
Configuration			
Reset Configuration			
Parameter Settings	User Constants	NVIC Settings	DMA Settings
NVIC Interrupt Table			
ADC1 and ADC2 global interrupts	Enabled	Preemption Priority	Sub Priority
	<input checked="" type="checkbox"/>	0	0

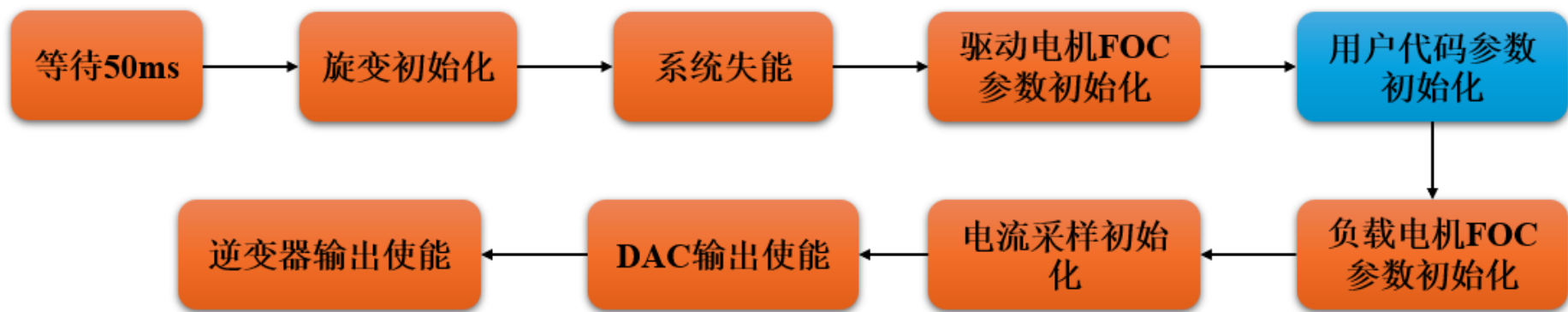
# Chapter3 用户代码编写指南

## 1. 代码结构



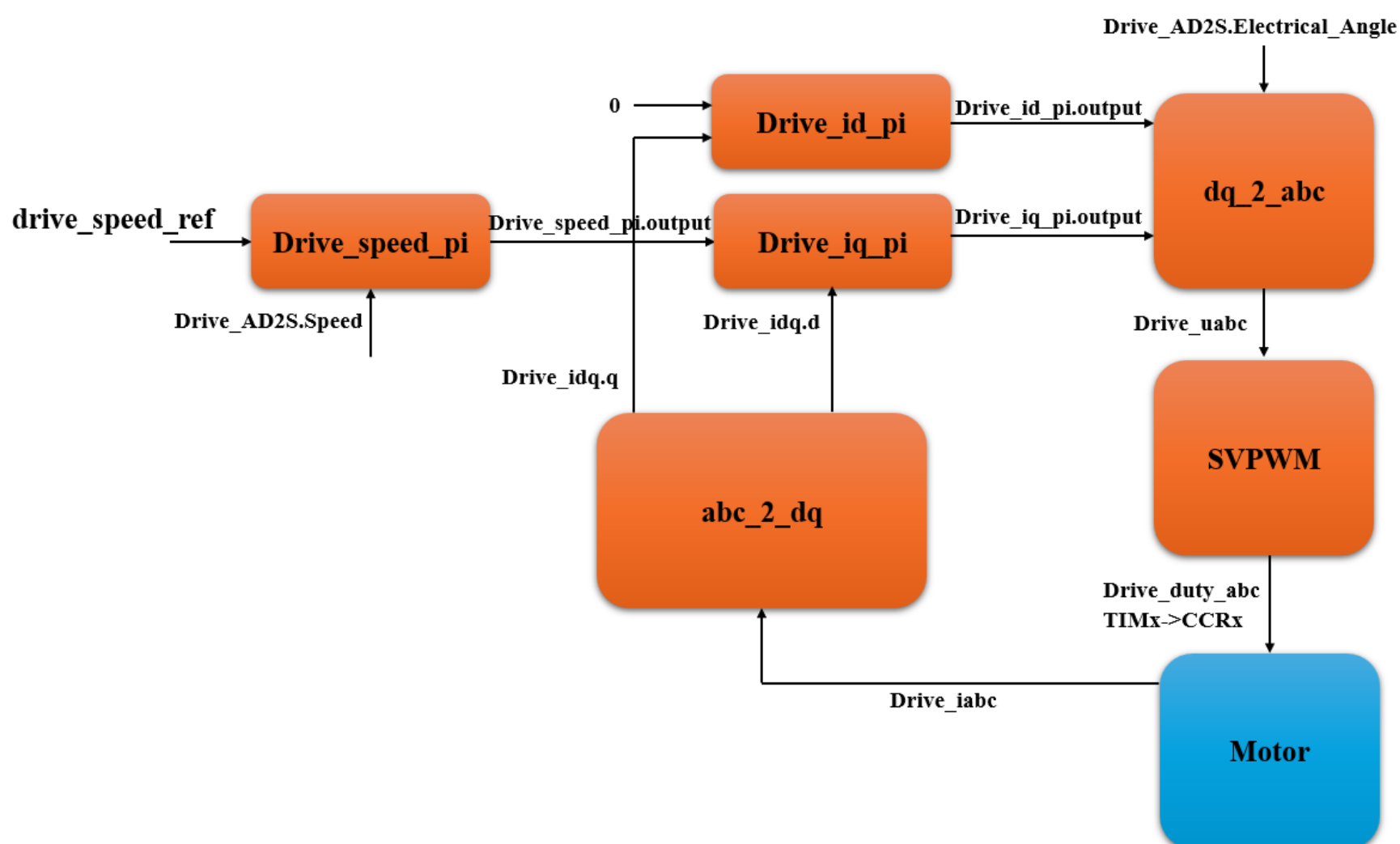
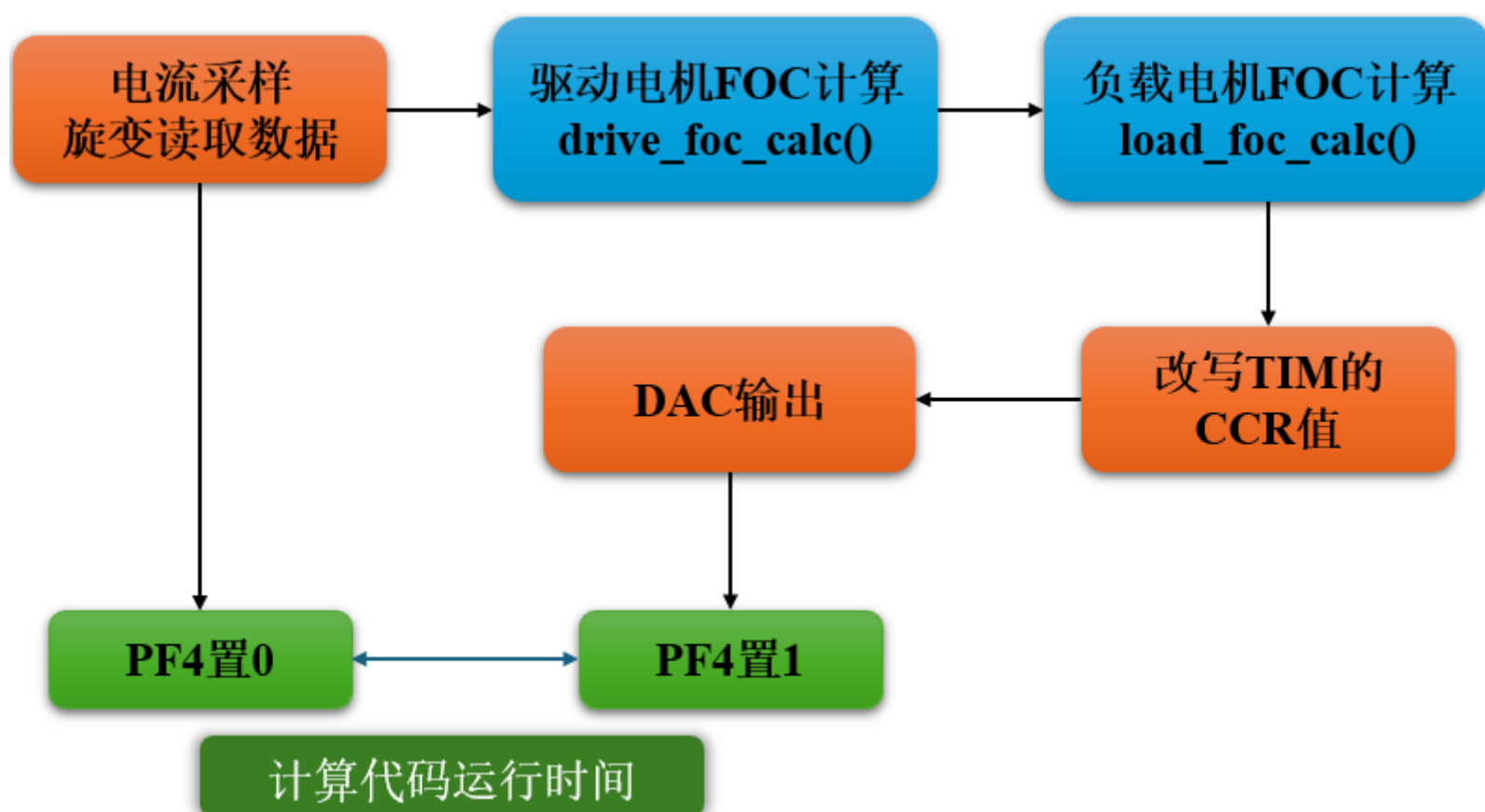
## 初始化程序

```
1 /**
2  * @brief  Init Program
3  */
4 static void init(void)
5 {
6     // User init
7     user_init();
8     // Hardware init
9     hw_init();
10 }
```



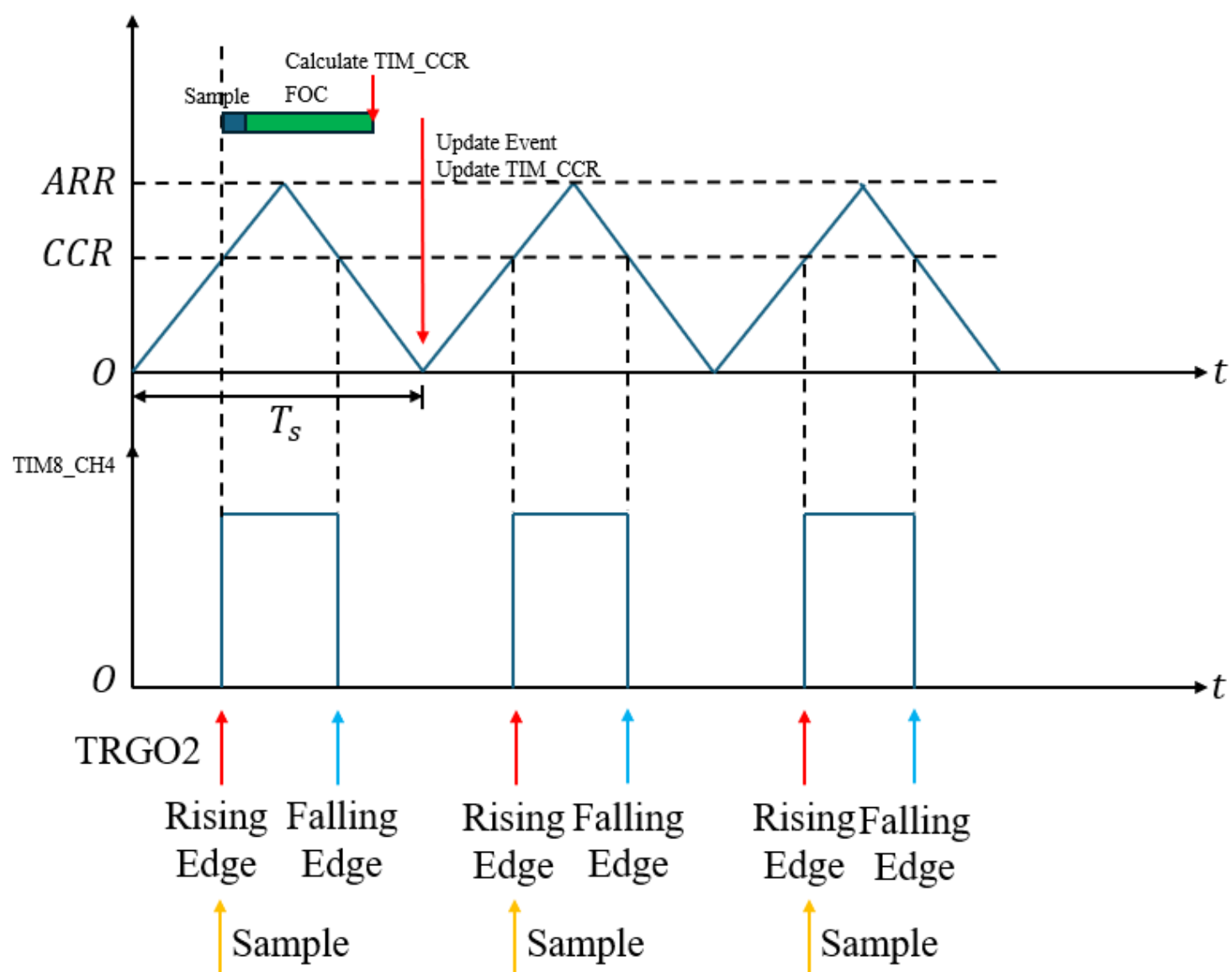
## 电流采样中断程序

```
1  /**
2   * @brief   ADC Injected Channel interrupt Callback function
3   */
4  void HAL_ADCEx_InjectedConvCpltCallback(ADC_HandleTypeDef *hadc)
5  {
6      HAL_GPIO_WritePin(GPIOF, GPIO_PIN_4, 0); // Caculate running time
7
8      /*****
9       * @brief   Sample Calculate
10      */
11     hw_curr_sample(hadc);
12
13     // Angle and Speed Sample
14     AD2S1210_Angle_Get(); // Angle Sample
15     AD2S1210_Speed_Get(SYSTEM_SAMPLE_TIME); // Speed Sample
16     /*****
17      * @brief   FOC Calculate
18      */
19     drive_foc_calc();
20     load_foc_calc();
21
22     /*****
23      * @brief   Voltage-Source Inverter Control
24      */
25     if (system_enable == 0) {
26         TIM8->CCR1 = 0;
27         TIM8->CCR2 = 0;
28         TIM8->CCR3 = 0;
29         TIM1->CCR1 = 0;
30         TIM1->CCR2 = 0;
31         TIM1->CCR3 = 0;
32     } else {
33         TIM8->CCR1 = Drive_duty_abc.dutya * TIM8->ARR;
34         TIM8->CCR2 = Drive_duty_abc.dutyb * TIM8->ARR;
35         TIM8->CCR3 = Drive_duty_abc.dutyc * TIM8->ARR;
36         TIM1->CCR1 = Load_duty_abc.dutya * TIM1->ARR;
37         TIM1->CCR2 = Load_duty_abc.dutyb * TIM1->ARR;
38         TIM1->CCR3 = Load_duty_abc.dutyc * TIM1->ARR;
39     }
40
41     /*****
42      * @brief   DAC Output
43      */
44     hw_dac_output();
45     HAL_DAC_SetValue(&hdac1, DAC_CHANNEL_1, DAC_ALIGN_12B_R, system_dac_value1);
46     HAL_DAC_SetValue(&hdac1, DAC_CHANNEL_2, DAC_ALIGN_12B_R, system_dac_value2);
47
48     HAL_GPIO_WritePin(GPIOF, GPIO_PIN_4, 1); // Caculate running time
49 }
```

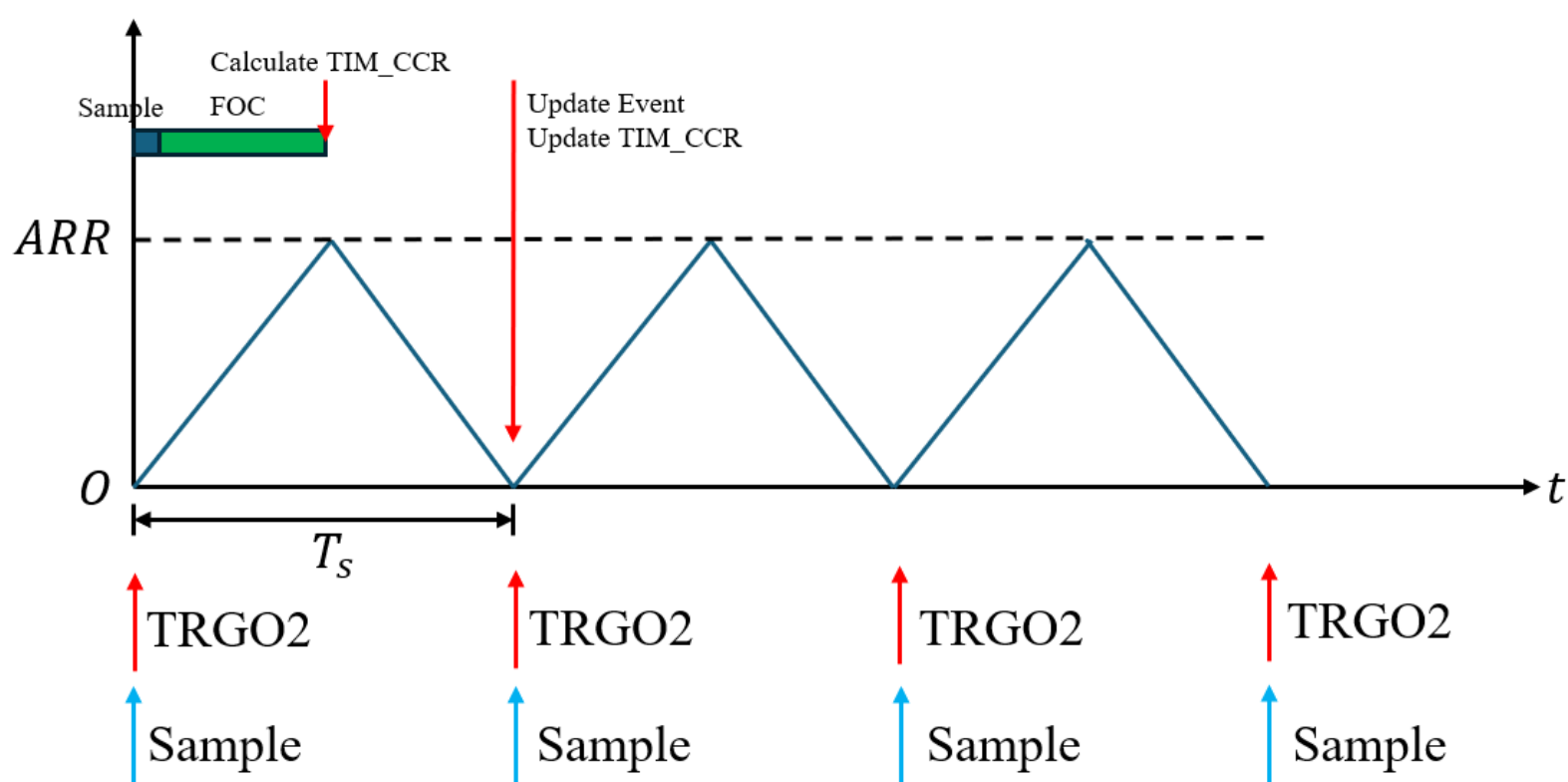


不同配置下的采样-更新时序:





- 配置模式2



## 2. 用户编写代码

用户应当在 `userpara.c / userpara.h` , `drivefoc.c / drivefoc.h` , `userinit.c / userinit.h` 三组文件中编写用户自定义代码。

### `userpara.c/h` 文件

该文件进行全局变量定义，将全局变量单独列出一个文件以便于调试时观察变量。

### `userinit.c/h` 文件

该文件内是用户代码参数初始化函数 `user_init()` 。

用户在该函数内编写代码以便进行算法参数初始化。

## `user_transfunc.c/h` 文件

该文件编写离散传递函数的实现(控制单元)。

## `drivefoc.c/h` 文件

该文件内是用于进行驱动电机 FOC 算法的函数 `drive_foc_calc()`。

用户在该函数内编写 **针对驱动电机的无感算法**。

## `loadfoc.c/h` 文件

该文件内是用于进行负载电机 FOC 算法的函数 `load_foc_calc()`。

用户在该函数内编写针对于负载电机的控制算法。