

Understanding and using SAT and SMT solvers

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The traveller Eve's problem

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Eve is eager to make scientific visits.

- She has 100 travel wishes A_1, \dots, A_{100} .
- She is allowed to make only 5 travels.
- She wants to be physically at A_1 .
- To coordinate a project, she needs to visit either A_2 or A_3 .
- Travel A_i costs C_i EUR.
- Eve can spend up to C EUR.
- Travel A_i takes T_i days.
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$$\left(\bigwedge_{i=1}^{100} \left((a_i = 0 \wedge c_i = 0 \wedge t_i = 0) \vee (a_i = 1 \wedge c_i = C_i \wedge t_i = T_i) \right) \right) \wedge$$
$$\left(\sum_{i=1}^{100} a_i \leq 5 \right) \wedge (a_1 = 1) \wedge (a_2 = 1 \vee a_3 = 1) \wedge \left(\sum_{i=1}^{100} c_i \leq C \right) \wedge \left(\sum_{i=1}^{100} t_i \geq T \right)$$

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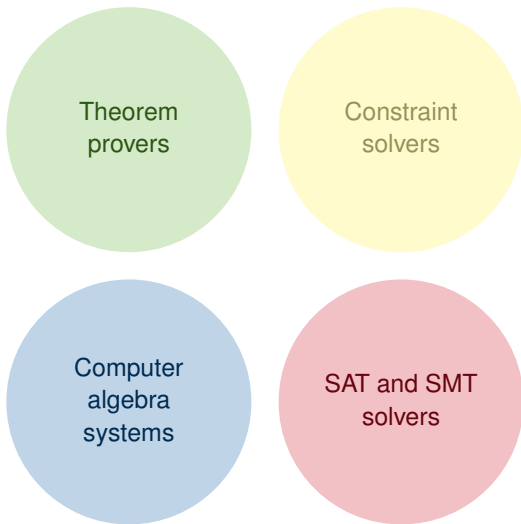
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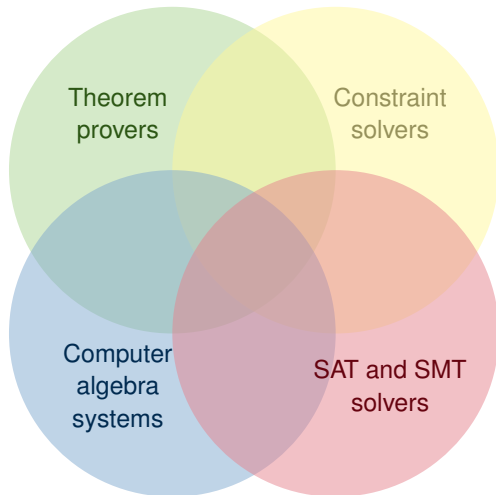
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Logic: Linear real arithmetic.

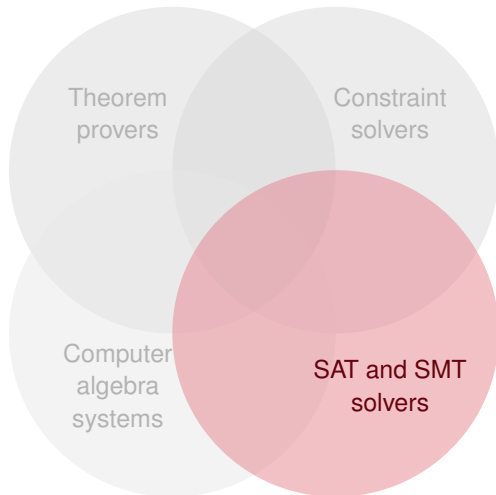
Some technologies for satisfiability checking



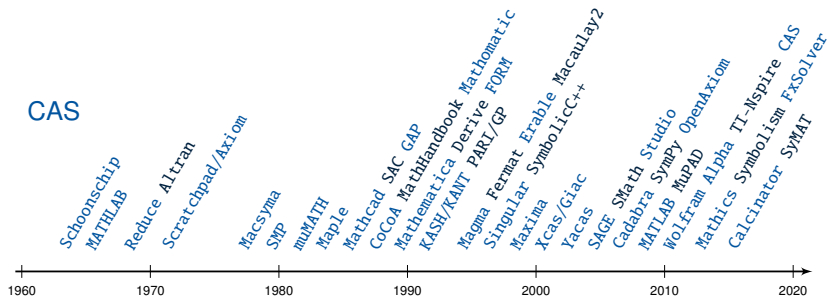
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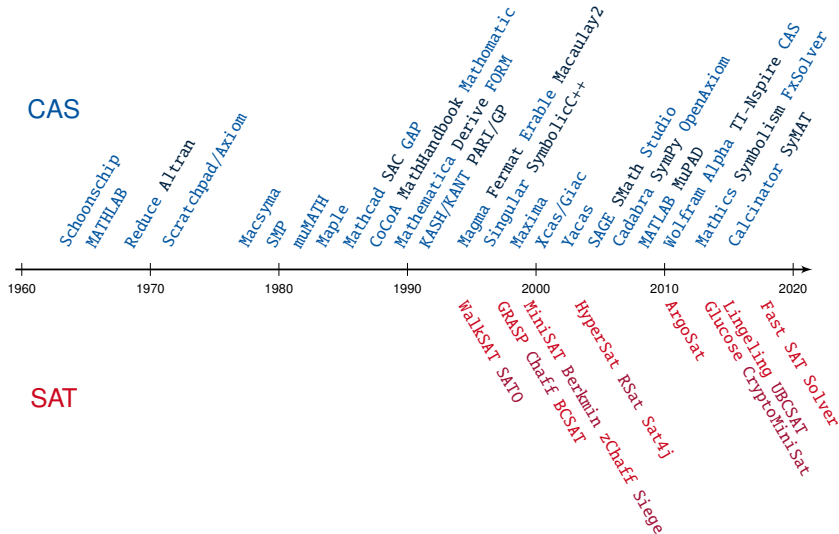
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Tool development



Tool development



Success story: SAT-solving

- Practical problems with millions of variables are solvable.
- A wide range of applications, e.g., verification, synthesis, combinatorial optimisation, etc.

Satisfiability checking for propositional logic

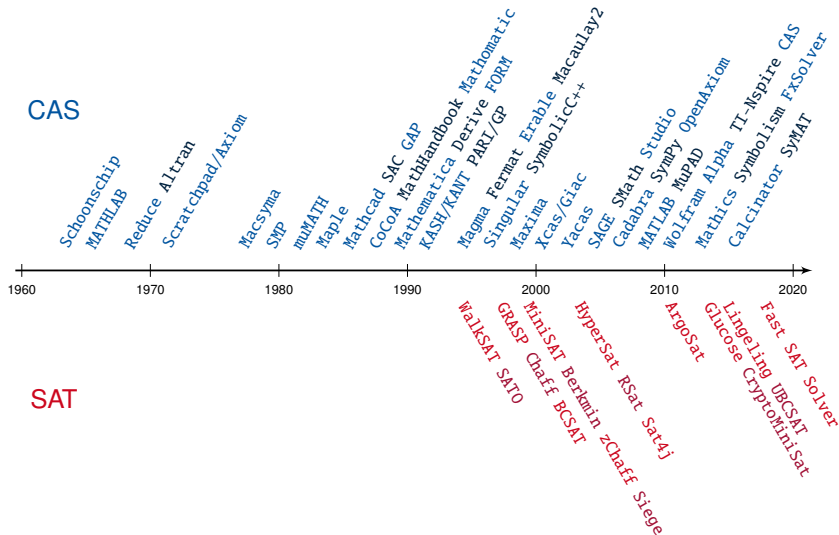
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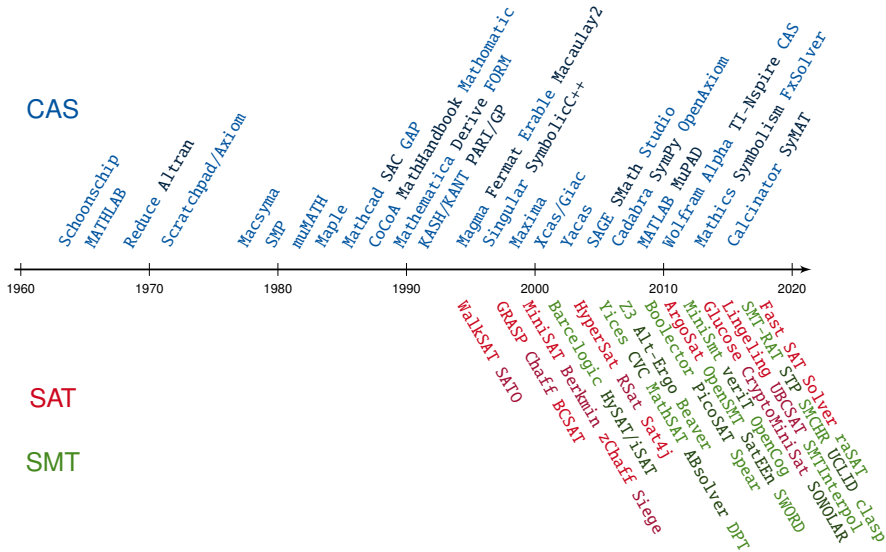
Community support:

- Standard input language.
- Large benchmark library.
- Competitions since 2002.
- SAT Live! forum as community platform, dedicated conferences, journals, etc.

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Satisfiability modulo theories (SMT) solving:

- Propositional logic is sometimes too weak for modelling.
- Increase expressiveness: **quantifier-free (QF) fragments of first-order logic over various theories.**

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- SAT solving
 - Exploration (also called enumeration)
 - Boolean constraint propagation (BCP)
 - Conflict resolution and backtracking
 - Exploration revisited
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Syntax of propositional logic

Abstract syntax of well-formed propositional formulae:

$$\varphi := a \mid (\neg\varphi) \mid (\varphi \wedge \varphi)$$

where AP is a set of (atomic) **propositions** (Boolean variables) and $a \in AP$.

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Syntactic sugar:

$$\begin{aligned}\perp &:= (a \wedge \neg a) \\ \top &:= (a \vee \neg a) \\ (\varphi_1 \vee \varphi_2) &:= \neg((\neg\varphi_1) \wedge (\neg\varphi_2)) \\ (\varphi_1 \rightarrow \varphi_2) &:= ((\neg\varphi_1) \vee \varphi_2) \\ (\varphi_1 \leftrightarrow \varphi_2) &:= ((\varphi_1 \rightarrow \varphi_2) \wedge (\varphi_2 \rightarrow \varphi_1)) \\ (\varphi_1 \oplus \varphi_2) &:= (\varphi_1 \leftrightarrow (\neg\varphi_2))\end{aligned}$$

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p	q	$\neg p$	$p \wedge q$	$p \vee q$	$p \rightarrow q$	$p \leftrightarrow q$	$p \oplus q$
0	0	1	0	0	1	1	0
0	1	1	0	1	1	0	1
1	0	0	0	1	0	0	1
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Each possible assignment is covered by a line of the truth table.

α **satisfies** φ iff in the line for α and the column for φ the entry is 1.

Conjunctive normal form

- A **literal** is either a variable or the negation of a variable.
- A **clause** is a disjunction of literals.
- A formula in **Conjunctive Normal Form (CNF)** is a conjunction of clauses.

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- A **literal** is either a variable or the negation of a variable.
- A **clause** is a disjunction of literals.
- A formula in **Conjunctive Normal Form (CNF)** is a conjunction of clauses.
- Every propositional logic formula can be converted to an **equi-satisfiable** CNF in **linear** time and space on the cost of (linearly many) new variables.

Tseitin's CNF encoding

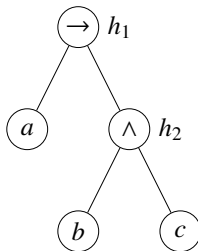
Consider the formula $\varphi = (a \rightarrow (b \wedge c))$.

Tseitin's encoding:

$$(h_1 \leftrightarrow (a \rightarrow h_2)) \wedge$$

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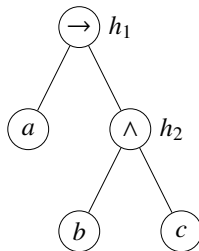
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- Each node's encoding has a CNF representation with 3 or 4 clauses.

$$h_1 \leftrightarrow (a \rightarrow h_2) \text{ in CNF: } (h_1 \vee a) \wedge (h_1 \vee \neg h_2) \wedge (\neg h_1 \vee \neg a \vee h_2)$$

$$h_2 \leftrightarrow (b \wedge c) \text{ in CNF: } (\neg h_2 \vee b) \wedge (\neg h_2 \vee c) \wedge (h_2 \vee \neg b \vee \neg c)$$

Satisfiability problem

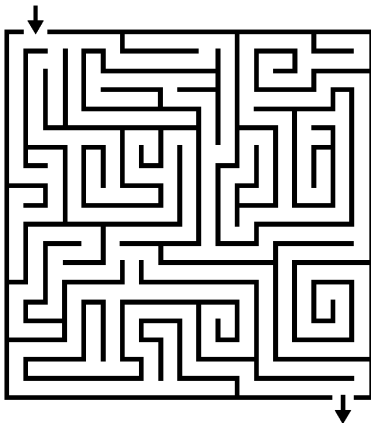
Given:

- Propositional logic formula φ in CNF.

Question:

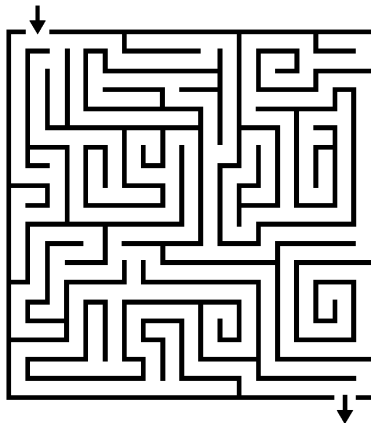
- Is φ satisfiable?
(Is there a model for φ ?)

SAT solving: The DPLL+CDCL idea

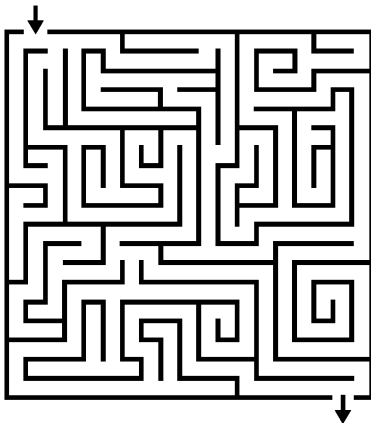


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Proof system



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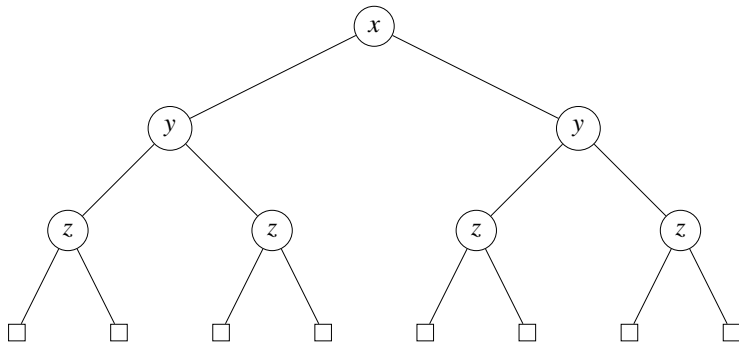
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$$\underbrace{(\neg x \vee y \vee z)}_{c_1} \wedge \underbrace{(y \vee \neg z)}_{c_2} \wedge \underbrace{(\neg x \vee \neg y)}_{c_3}$$

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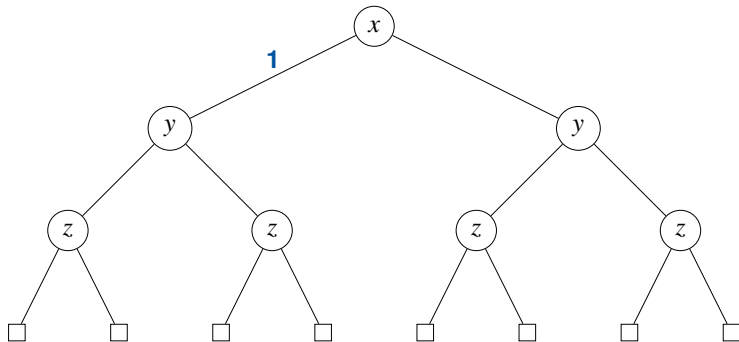
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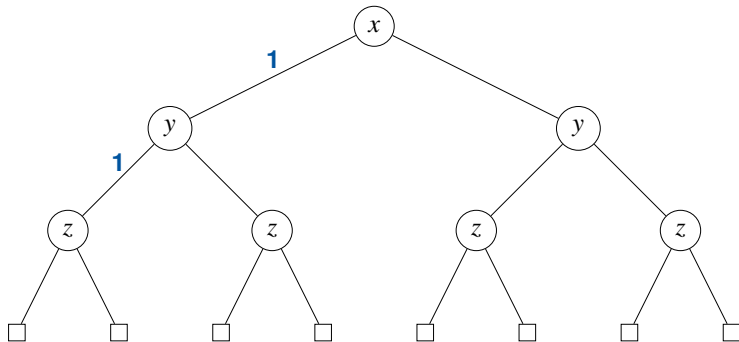
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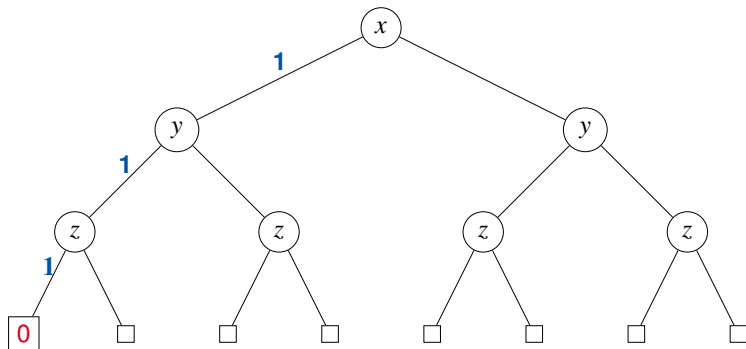
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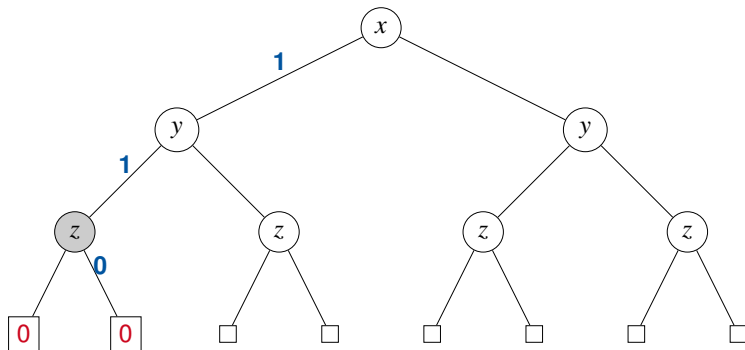
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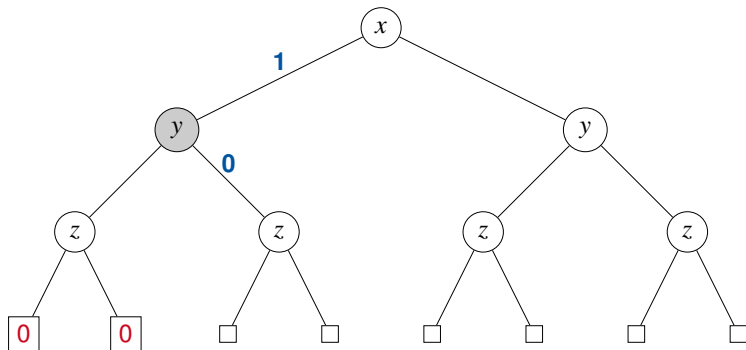
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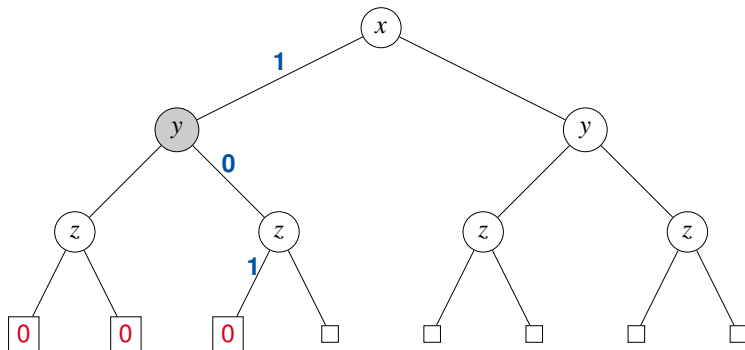
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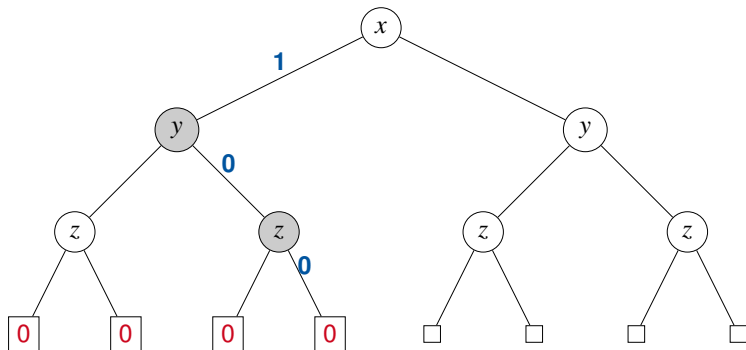
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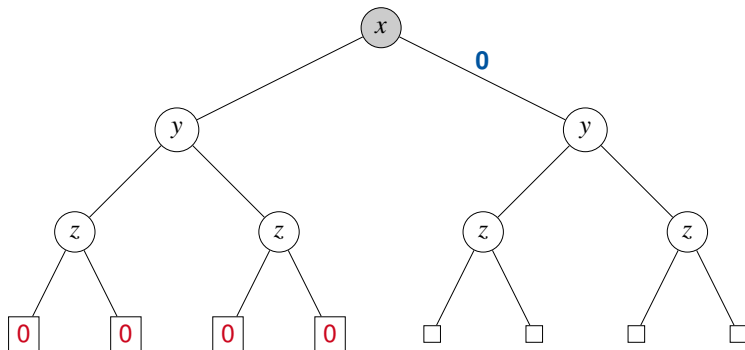
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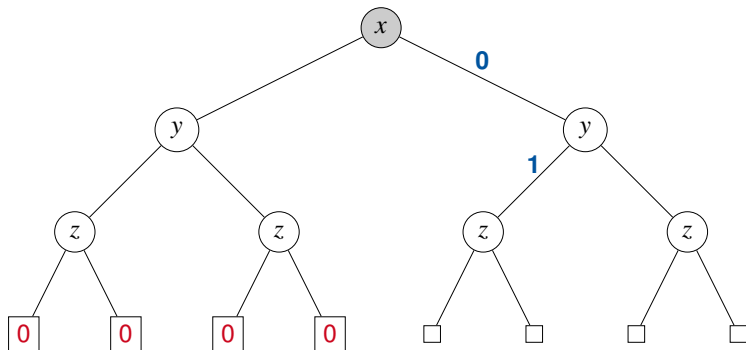
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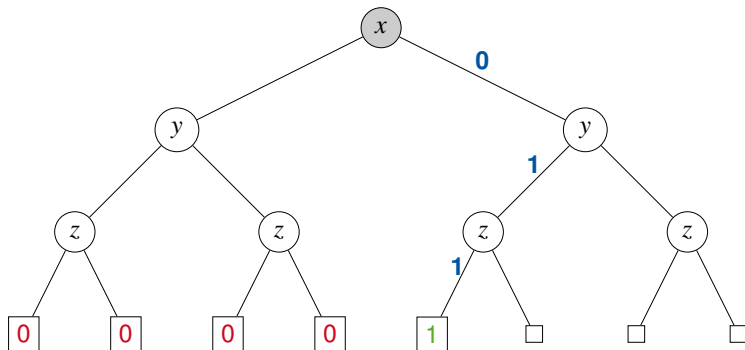
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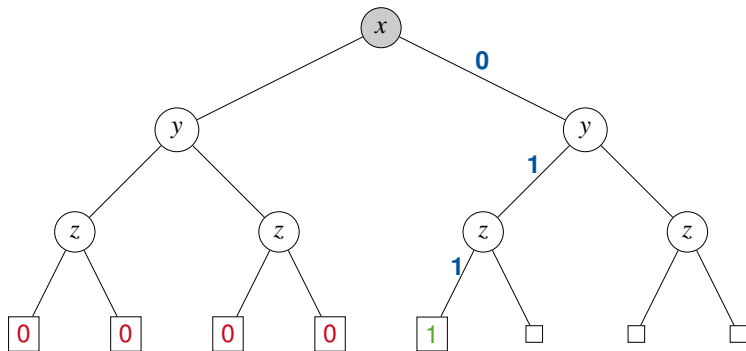
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For unsatisfiable problems, all assignments need to be checked.
For satisfiable problems, variable and sign ordering might strongly influence the running time.

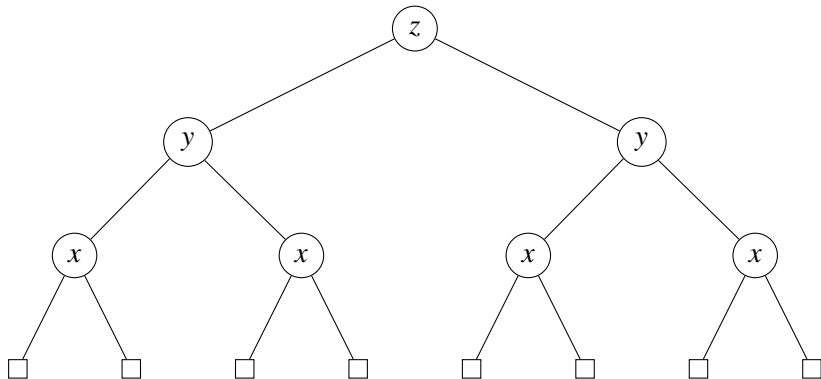
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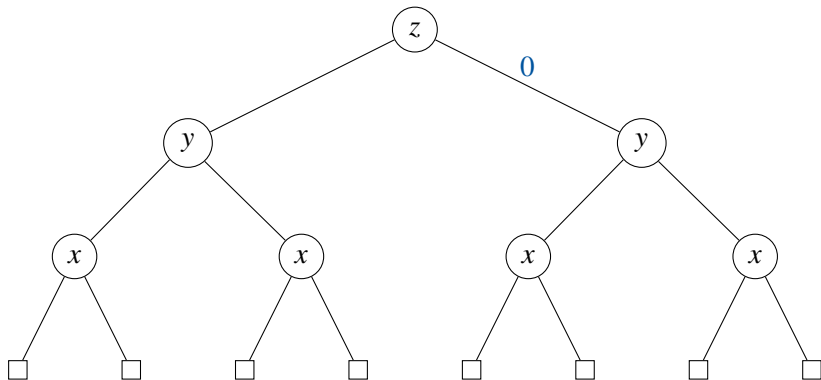
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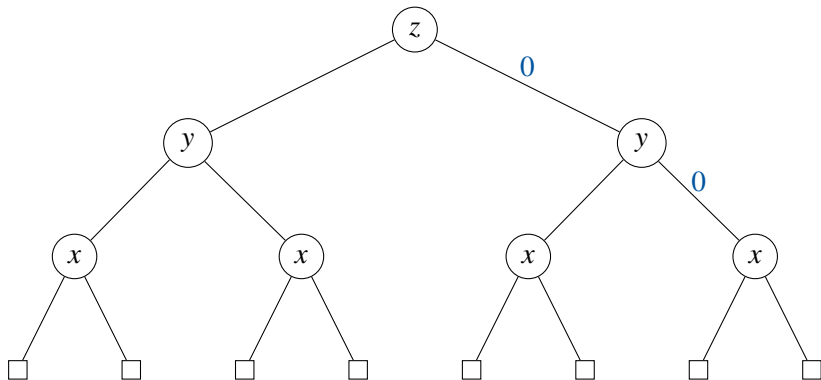
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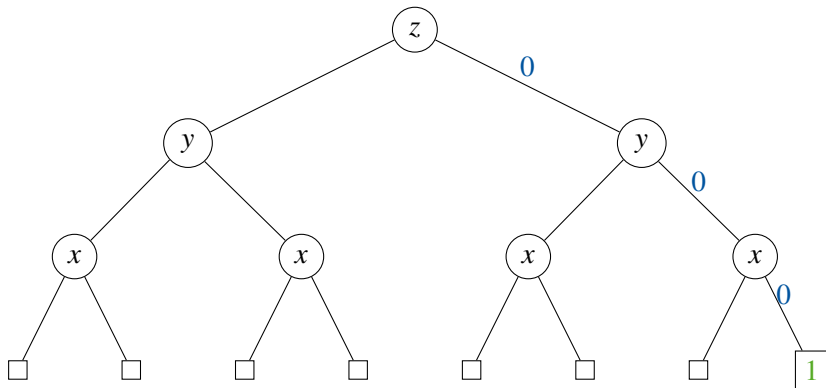
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satisfied: at least one literal is satisfied

unsatisfied: all literals are assigned but none are satisfied

unit: all but one literals are assigned but none are satisfied

unresolved: all other cases

Example :

x_1	x_2	x_3	$c = (x_1 \vee x_2 \vee x_3)$
1	0		satisfied
0	0	0	unsatisfied
0	0		unit
	0		unresolved

BCP: Unit clauses are used to imply consequences of decisions.

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Some notations:

Decision Level (DL) is a counter for decisions

Antecedent(ℓ): unit clause implying the value of literal ℓ (nil if decision)

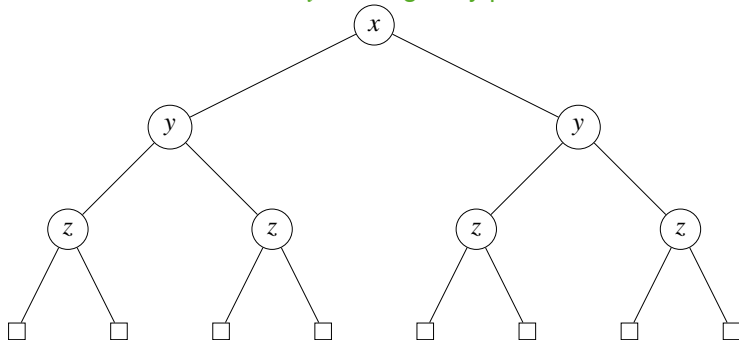
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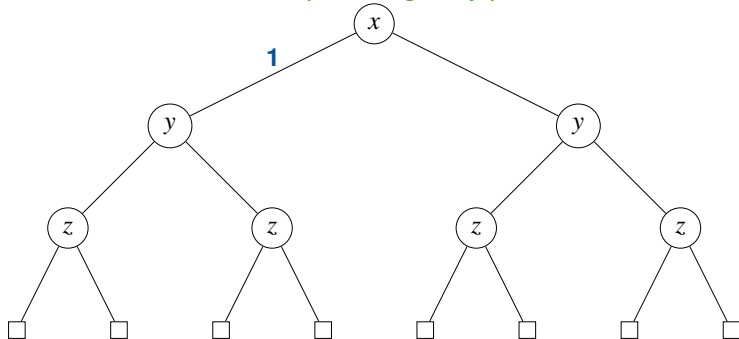
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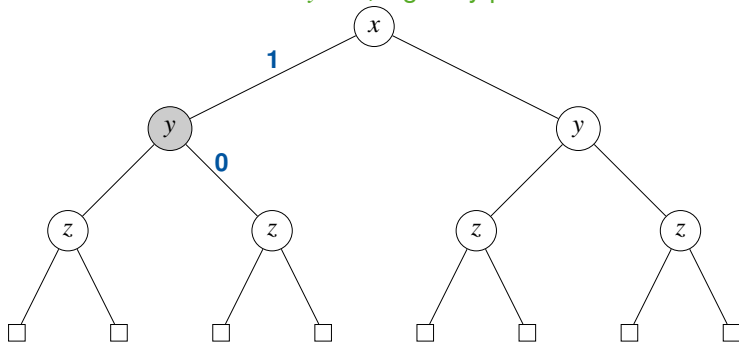
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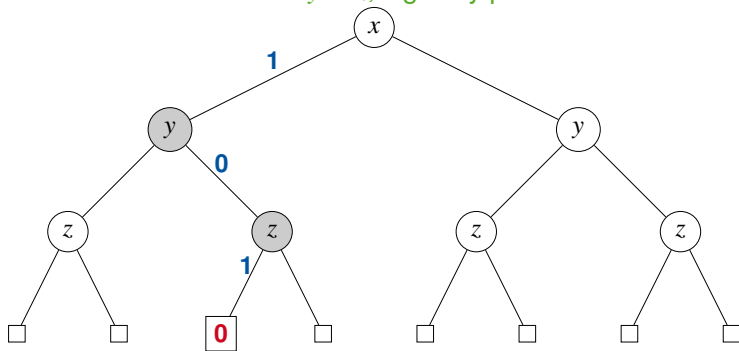
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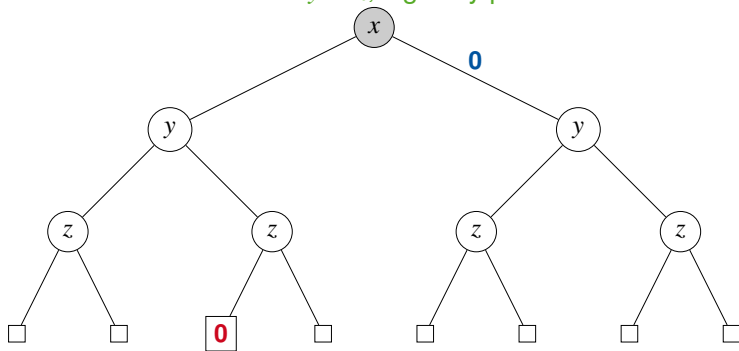
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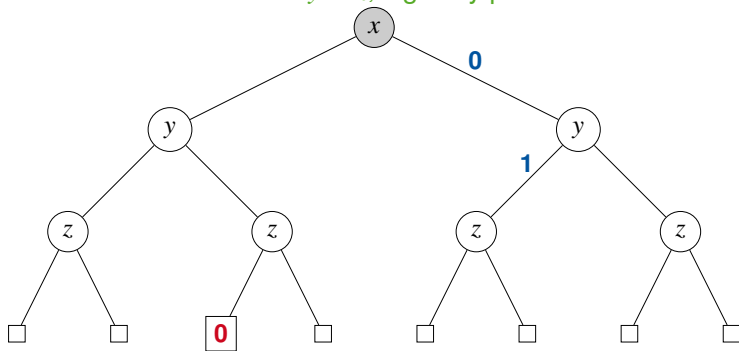
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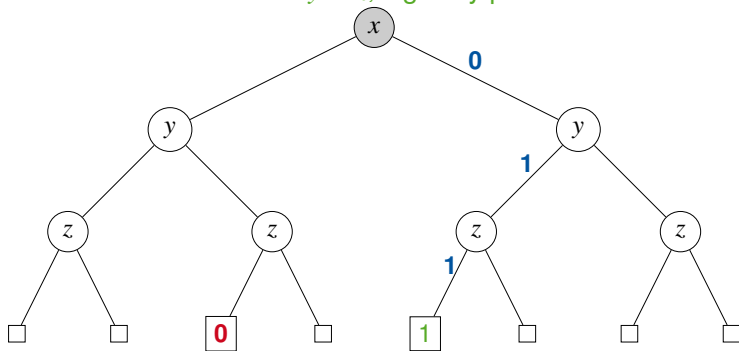
Static variable order $x < y < z$, sign: try positive first



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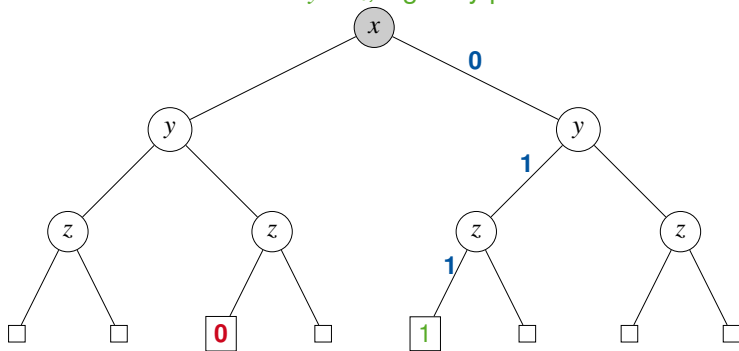
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Efficient propagation with the **watched literal scheme**.

- SAT solving
 - Exploration (also called enumeration)
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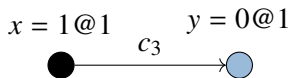
$$x = 1@1$$



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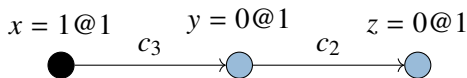
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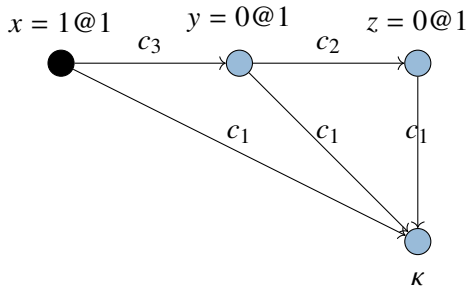
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Implication graph: Example

Decisions: { }

$$c_1 = (\neg x_1 \vee x_2)$$

$$c_2 = (\neg x_1 \vee x_3 \vee x_7)$$

$$c_3 = (\neg x_2 \vee \neg x_3 \vee x_4)$$

$$c_4 = (\neg x_4 \vee x_5 \vee x_8)$$

$$c_5 = (\neg x_4 \vee x_6 \vee x_9)$$

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Implication graph: Example

Decisions: $\{x_7 = 0@1\}$ }

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$$c_3 = (\neg x_2 \vee \neg x_3 \vee x_4)$$

$$c_4 = (\neg x_4 \vee x_5 \vee x_8)$$

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$x_7 = 0@1$

Implication graph: Example

Decisions: $\{x_7 = 0@1, x_8 = 0@2\}$ }

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Implication graph: Example

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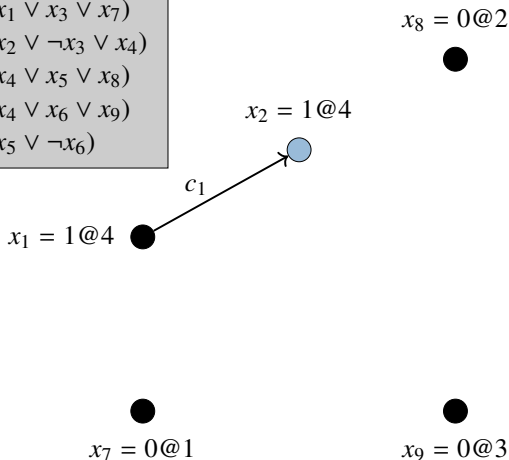
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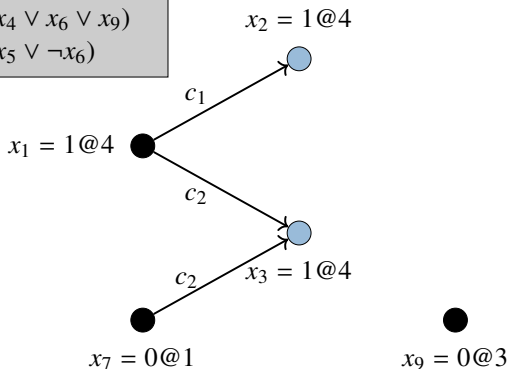
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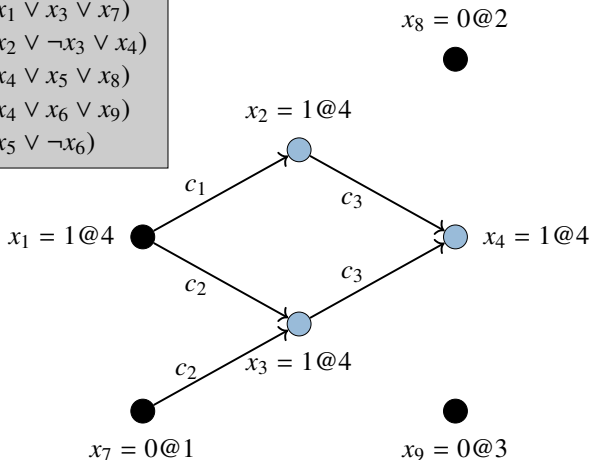
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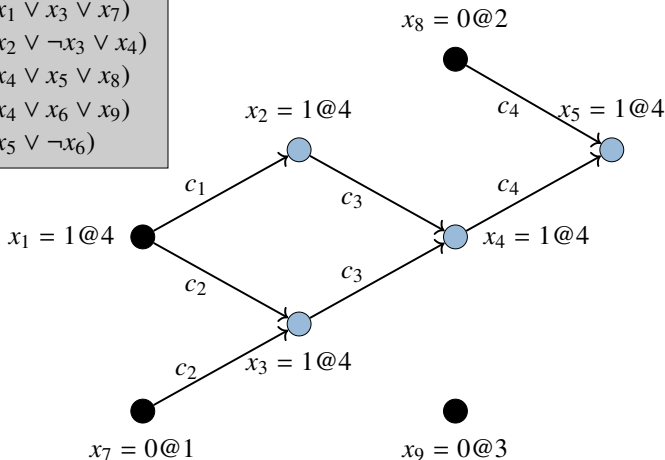
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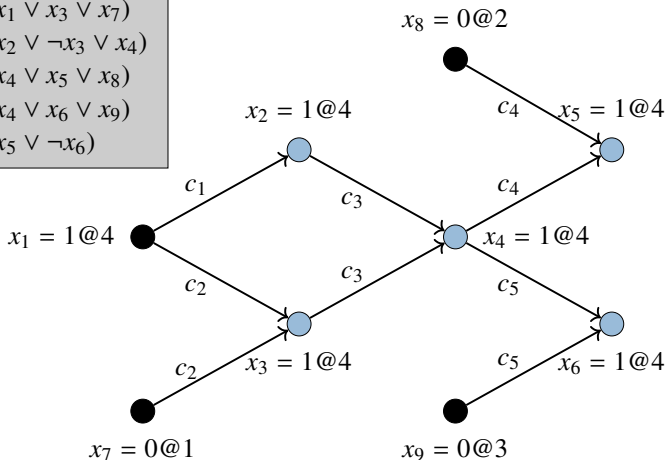
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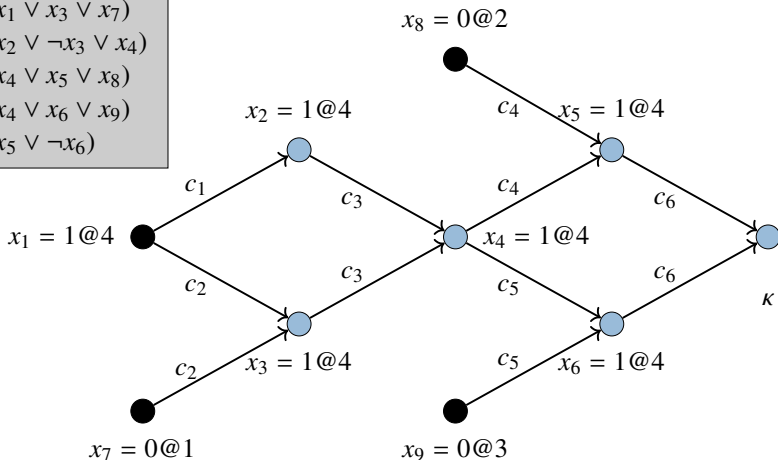
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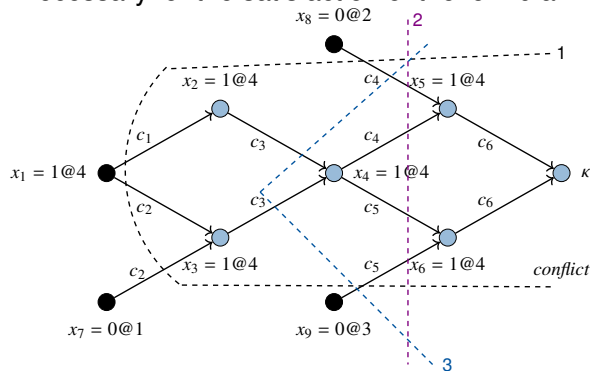
A node set $C \subseteq V$ is a **cut** in G iff there exists $V' \subseteq V$ such that

- each predecessor of κ is reachable from V' in G ,
- $\kappa \notin V'$,
- C consists of those nodes in V' that are sources of an edge with target not in V' :

$$C = \{s \in V' \mid \exists t \in V \setminus V'. (s, t) \in E\} .$$

Conflict resolution

- Assume an implication graph $G = (V, E, L)$ with $\kappa \in V$.
- Let C be a cut in G .
- $(\bigvee_{n \in C} \neg \text{literal}(n))$ is called a **conflict clause**:
it is false under the current assignment but its satisfaction is necessary for the satisfaction of the formula.



$$1. (x_8 \vee \neg x_1 \vee x_7 \vee x_9)$$

$$2. (x_8 \vee \neg x_4 \vee x_9)$$

$$3. (x_8 \vee \neg x_2 \vee \neg x_3 \vee x_9)$$

⋮

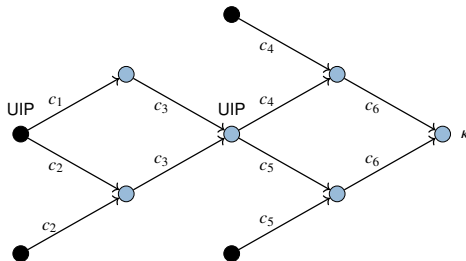
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- An **asserting clause** is a conflict clause with a single literal from the current decision level.
Backtracking (to the right level) makes it a **unit clause**.
- Modern solvers consider only asserting clauses.

Conflict resolution

- Assume an implication graph G with a conflict node κ .
A **unique implication point (UIP)** for κ in G is a node $n \neq \kappa$ in G such that **all paths from the last decision to κ go through n** .
- The **first UIP** is the UIP closest to the conflict node.
- If a cut C contains a UIP then the clause $(\bigvee_{n \in C} \neg literal(n))$ is asserting.



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A: The formula is unsatisfiable.

The DPLL+CDCL algorithm

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if (!BCP()) return UNSAT;
while (true)
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    if (!decide()) return SAT;
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Boolean constraint propagation.
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Conflict resolution and backtracking. Return false if impossible.

Conflict clauses and (binary) resolution

- The (binary) resolution is a sound (and complete) inference rule:

$$\frac{(\beta \vee a_1 \vee \dots \vee a_n) \quad (\neg\beta \vee b_1 \vee \dots \vee b_m)}{(a_1 \vee \dots \vee a_n \vee b_1 \vee \dots \vee b_m)} \text{(Binary Resolution)}$$

- Example:

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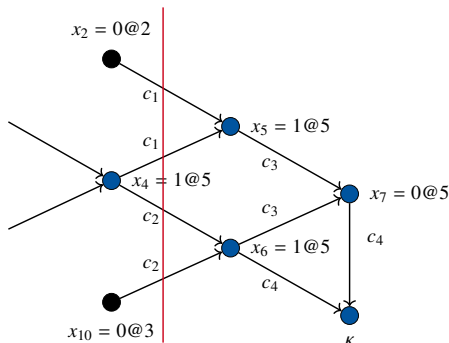
$$\frac{(x_1 \vee x_2) \quad (\neg x_1 \vee x_3 \vee x_4)}{(x_2 \vee x_3 \vee x_4)}$$

What is the relation of binary resolution and conflict clauses?

Conflict clauses and (binary) resolution

- Consider the following example:

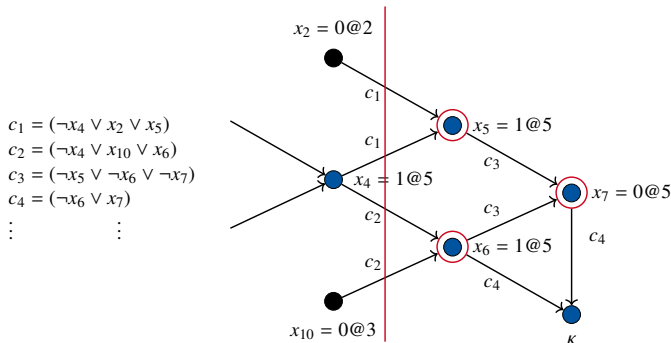
$$\begin{aligned}c_1 &= (\neg x_4 \vee x_2 \vee x_5) \\c_2 &= (\neg x_4 \vee x_{10} \vee x_6) \\c_3 &= (\neg x_5 \vee \neg x_6 \vee \neg x_7) \\c_4 &= (\neg x_6 \vee x_7) \\&\vdots \qquad \qquad \vdots\end{aligned}$$



- Asserting conflict clause: $c_5 : (x_2 \vee \neg x_4 \vee x_{10})$

Conflict clauses and (binary) resolution

- Assignment order: x_4, x_5, x_6, x_7 Conflict clause: $c_5 : (x_2 \vee \neg x_4 \vee x_{10})$



- Starting with the conflicting clause, apply resolution with the antecedent of the last assigned literal, until we get an asserting clause:
 - $T1 = \text{Res}(c_4, c_3, x_7) = (\neg x_5 \vee \neg x_6)$
 - $T2 = \text{Res}(T1, c_2, x_6) = (\neg x_4 \vee \neg x_5 \vee x_{10})$
 - $T3 = \text{Res}(T2, c_1, x_5) = (x_2 \vee \neg x_4 \vee x_{10})$

Finding the asserting conflict clause

```
bool resolve_conflict() {  
    if (current_decision_level = 0) then { return false; }  
    cl := current_conflicting_clause;  
    while (cl is not asserting) do {  
        lit := last_assigned_literal(cl);  
        var := variable_of_literal(lit);  
        ante := antecedent(var);  
        cl := resolve(cl, ante, var);  
    }  
    add_clause_to_database(cl);  
    return true;  
}
```

Applied to our example:

name	<i>cl</i>	<i>lit</i>	<i>var</i>	<i>ante</i>
<i>c</i> ₄	$(\neg x_6 \vee x_7)$	<i>x</i> ₇	<i>x</i> ₇	<i>c</i> ₃
	$(\neg x_5 \vee \neg x_6)$	$\neg x_6$	<i>x</i> ₆	<i>c</i> ₂
	$(\neg x_4 \vee x_{10} \vee \neg x_5)$	$\neg x_5$	<i>x</i> ₅	<i>c</i> ₁
<i>c</i> ₅	$(\neg x_4 \vee x_2 \vee x_{10})$			

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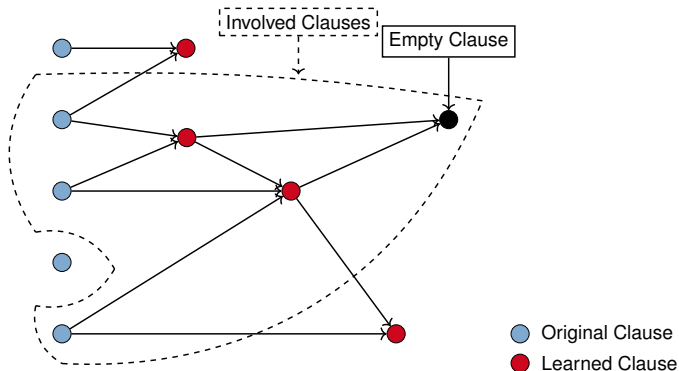
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- The set of those original clauses that were used for resolution in conflict analysis during SAT-solving (inclusively the last conflict at decision level 0) gives us an unsatisfiable core which is in general much smaller.
- However, this unsatisfiable core is still not always minimal (i.e., we can remove clauses from it still having an unsatisfiable core).

The resolution graph

A **resolution graph** gives us more information to get a minimal unsatisfiable core.



Theorem

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Proof.

Define a partial order on partial assignments: $\alpha < \beta$ iff either α is an extension of β or α has more assignments at the smallest decision level at that α and β do not agree.

BCP decreases the order, conflict-driven backtracking also. Since the order always decreases during the search, the theorem holds. □

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Decision heuristics: VSIDS

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 - Gives priority to variables involved in recent conflicts.
 - “Involved” can have different definitions. We take those variables that occur in clauses used for conflict resolution.
- 1 Each variable has a **counter** initialized to 0.
 - 2 We define an **increment** value (e.g., 1).
 - 3 When a **conflict** occurs, we increase the counter of each variable, that occurs in at least one clause used for conflict resolution, by the increment value.
Afterwards we increase the increment value (e.g., by 1).
 - 4 For decisions, the unassigned variable with the **highest counter** is chosen.
 - 5 Periodically, all the counters and the increment value are **divided** by a constant.

VSIDS is a 'quasi-static' strategy:

- **static** because it doesn't depend on current assignment
- **dynamic** because it gradually changes. Variables that appear in recent conflicts have higher priority.

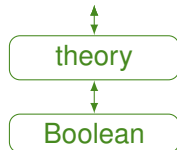
This strategy is a **conflict-driven** decision strategy.

"...employing this strategy dramatically (i.e., an order of magnitude) improved performance..."

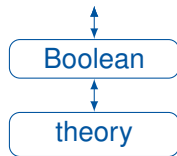
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Three SMT solving approaches

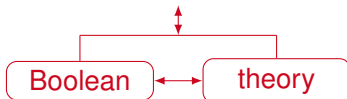
Eager SMT solving



Lazy SMT solving

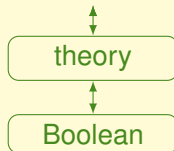


Model-constructing
satisfiability calculus
(MCSAT)

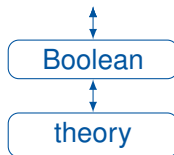


Three SMT solving approaches

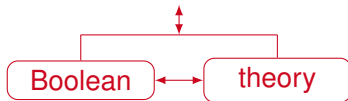
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Eager example [Bryant and Velev, 2000]

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$$\varphi^E = x_1 = x_2 \wedge x_2 = x_3 \wedge x_1 \neq x_3$$

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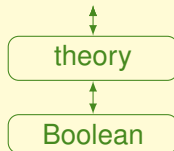
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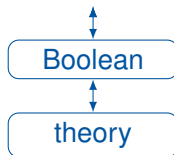
Similar approaches are available for uninterpreted functions, bit-vector arithmetic (“bit-blasting”), floating-point arithmetic and others.

Three SMT solving approaches

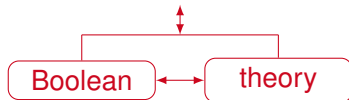
Eager SMT solving



Lazy SMT solving

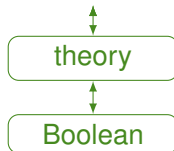


Model-constructing
satisfiability calculus
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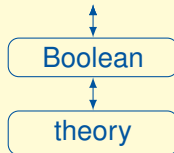


Three SMT solving approaches

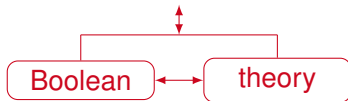
Eager SMT solving



Lazy SMT solving



Model-constructing
satisfiability calculus
(MCSAT)



The Xmas problem

There are three types of Xmas presents Santa Claus can make.

- Santa Claus wants to reduce the overhead by making only two types.
- He needs at least 100 presents.
- He needs at least 5 of either type 1 or type 2.
- He needs at least 10 of the third type.
- Each present of type 1, 2, and 3 need 1, 2, resp. 5 minutes to make.
- Santa Claus is late, and he has only 3 hours left.
- Each present of type 1, 2, and 3 costs 3, 2, resp. 1 EUR.
- He has 300 EUR for presents in total.

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$$(p_1 = 0 \vee p_2 = 0 \vee p_3 = 0) \wedge p_1 + p_2 + p_3 \geq 100 \wedge \\ (p_1 \geq 5 \vee p_2 \geq 5) \wedge p_3 \geq 10 \wedge p_1 + 2p_2 + 5p_3 \leq 180 \wedge \\ 3p_1 + 2p_2 + p_3 \leq 300$$

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Logic:

The Xmas problem

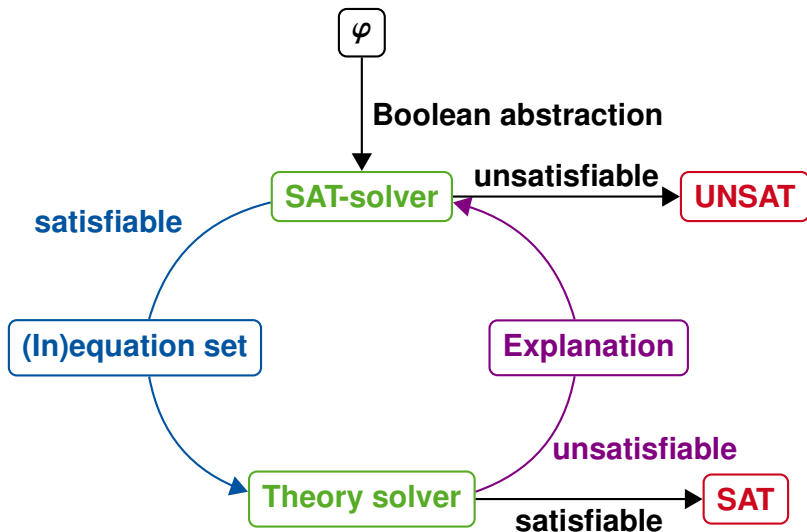
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Logic: First-order logic over the integers with addition.

Full lazy SMT solving

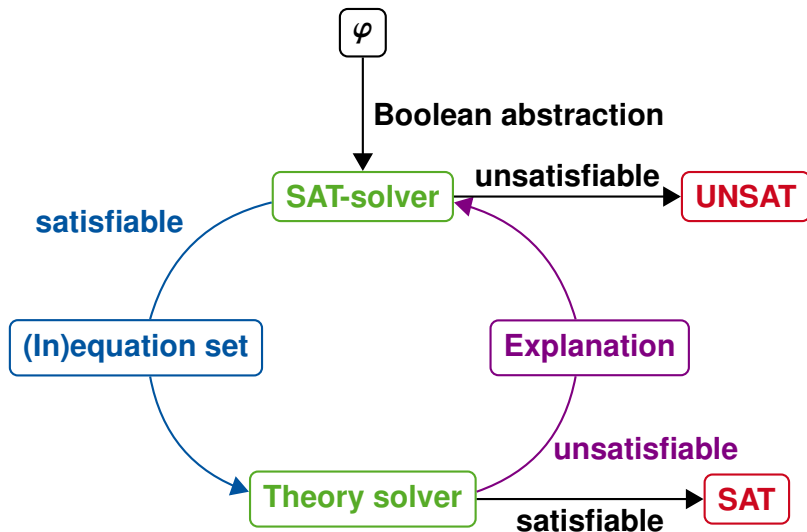


$$\begin{aligned} & \underbrace{(p_1 = 0)}_{a_1} \vee \underbrace{(p_2 = 0)}_{a_2} \vee \underbrace{(p_3 = 0)}_{a_3} \wedge \underbrace{(p_1 + p_2 + p_3 \geq 100)}_{a_4} \wedge \\ & \underbrace{(p_1 \geq 5)}_{a_5} \vee \underbrace{(p_2 \geq 5)}_{a_6} \wedge \underbrace{(p_3 \geq 10)}_{a_7} \wedge \underbrace{(p_1 + 2p_2 + 5p_3 \leq 180)}_{a_8} \wedge \\ & \underbrace{(3p_1 + 2p_2 + p_3 \leq 300)}_{a_9} \end{aligned}$$

$$\begin{array}{c} \underbrace{(p_1 = 0 \vee p_2 = 0 \vee p_3 = 0)}_{a_1} \wedge \underbrace{p_1 + p_2 + p_3 \geq 100}_{a_4} \wedge \\ \underbrace{(p_1 \geq 5 \vee p_2 \geq 5)}_{a_5} \wedge \underbrace{p_3 \geq 10}_{a_7} \wedge \underbrace{p_1 + 2p_2 + 5p_3 \leq 180}_{a_8} \wedge \\ \underbrace{3p_1 + 2p_2 + p_3 \leq 300}_{a_9} \end{array}$$

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9$$

Full lazy SMT solving



$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9$$

Assume a fixed variable order: a_1, \dots, a_9

Assignment to decision variables: false

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9$$

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$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9$$

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$DL0 : a_4 : 1$

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9$$

Assume a fixed variable order: a_1, \dots, a_9

Assignment to decision variables: false

$DL0 : a_4 : 1, a_7 : 1$

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9$$

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DL0 : $a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1$

DL1 :

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9$$

Assume a fixed variable order: a_1, \dots, a_9

Assignment to decision variables: false

$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1$

$DL1 : a_1 : 0$

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9$$

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$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1$

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$DL2 :$

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9$$

Assume a fixed variable order: a_1, \dots, a_9

Assignment to decision variables: false

$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1$

$DL1 : a_1 : 0$

$DL2 : a_2 : 0$

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Assume a fixed variable order: a_1, \dots, a_9

Assignment to decision variables: false

DL0 : $a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1$

DL1 : $a_1 : 0$

DL2 : $a_2 : 0, a_3 : 1$

DL3 :

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9$$

Assume a fixed variable order: a_1, \dots, a_9

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$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1$

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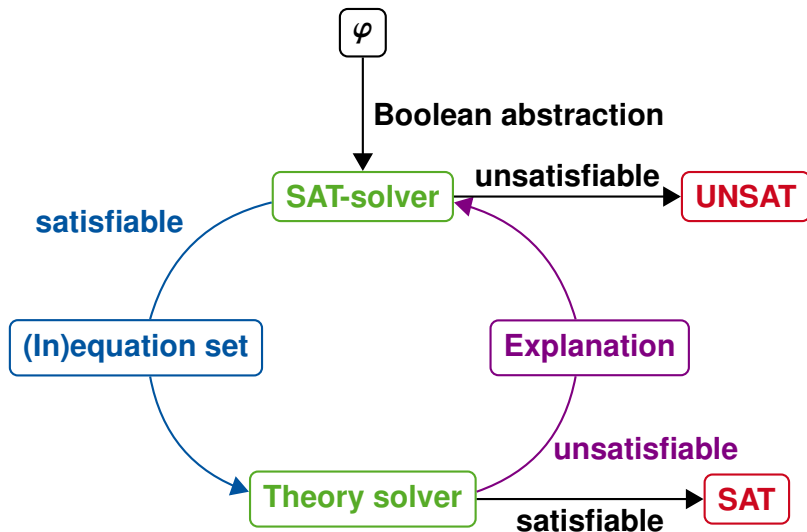
DL1 : $a_1 : 0$

DL2 : $a_2 : 0, a_3 : 1$

DL3 : $a_5 : 0, a_6 : 1$

Solution found for the Boolean abstraction.

Full lazy SMT solving



Theory solving

$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1$ $DL1 : a_1 : 0$
 $DL2 : a_2 : 0, a_3 : 1$ $DL3 : a_5 : 0, a_6 : 1$

Theory solving

$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1$ $DL1 : a_1 : 0$

$DL2 : a_2 : 0, a_3 : 1$ $DL3 : a_5 : 0, a_6 : 1$

True theory constraints: $a_4, a_7, a_8, a_9, a_3, a_6$

Theory solving

$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1$ $DL1 : a_1 : 0$

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True theory constraints: $a_4, a_7, a_8, a_9, a_3, a_6$

$$\begin{aligned} & (\underbrace{p_1 = 0}_{a_1} \vee \underbrace{p_2 = 0}_{a_2} \vee \underbrace{p_3 = 0}_{a_3}) \wedge \underbrace{p_1 + p_2 + p_3 \geq 100}_{a_4} \wedge \\ & (\underbrace{p_1 \geq 5}_{a_5} \vee \underbrace{p_2 \geq 5}_{a_6}) \wedge \underbrace{p_3 \geq 10}_{a_7} \wedge \underbrace{p_1 + 2p_2 + 5p_3 \leq 180}_{a_8} \wedge \\ & \underbrace{3p_1 + 2p_2 + p_3 \leq 300}_{a_9} \end{aligned}$$

Theory solving

$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1$ $DL1 : a_1 : 0$

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True theory constraints: $a_4, a_7, a_8, a_9, a_3, a_6$

$$\begin{aligned} & \underbrace{(p_1 = 0)}_{a_1} \vee \underbrace{(p_2 = 0)}_{a_2} \vee \underbrace{(p_3 = 0)}_{a_3} \wedge \underbrace{(p_1 + p_2 + p_3 \geq 100)}_{a_4} \wedge \\ & \underbrace{(p_1 \geq 5)}_{a_5} \vee \underbrace{(p_2 \geq 5)}_{a_6} \wedge \underbrace{(p_3 \geq 10)}_{a_7} \wedge \underbrace{(p_1 + 2p_2 + 5p_3 \leq 180)}_{a_8} \wedge \\ & \underbrace{(3p_1 + 2p_2 + p_3 \leq 300)}_{a_9} \end{aligned}$$

Encoding:

$$\begin{array}{lll} a_4 : p_1 + p_2 + p_3 \geq 100 & a_7 : p_3 \geq 10 & a_8 : p_1 + 2p_2 + 5p_3 \leq 180 \\ a_9 : 3p_1 + 2p_2 + p_3 \leq 300 & a_3 : p_3 = 0 & a_6 : p_2 \geq 5 \end{array}$$

Is the conjunction of the following constraints satisfiable?

$$a_4 : p_1 + p_2 + p_3 \geq 100$$

$$a_7 : p_3 \geq 10$$

$$a_8 : p_1 + 2p_2 + 5p_3 \leq 180$$

$$a_9 : 3p_1 + 2p_2 + p_3 \leq 300$$

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No.

Is the conjunction of the following constraints satisfiable?

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$$a_3 : p_3 = 0$$

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No.

Reason:

Is the conjunction of the following constraints satisfiable?

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$$a_8 : p_1 + 2p_2 + 5p_3 \leq 180$$

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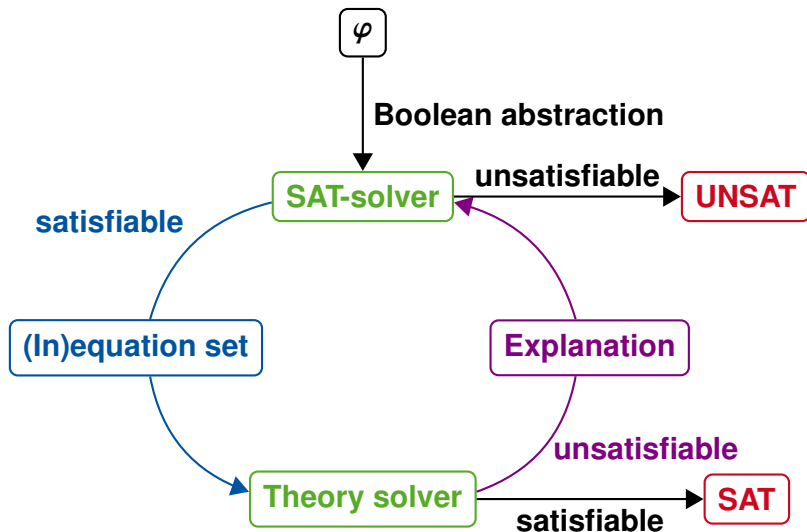
$$a_3 : p_3 = 0$$

$$a_6 : p_2 \geq 5$$

No.

Reason: $\underbrace{p_3 = 0}_{a_3} \wedge \underbrace{p_3 \geq 10}_{a_7}$ are conflicting.

Full lazy SMT solving



Add clause $(\neg a_3 \vee \neg a_7)$.

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9 \wedge (\neg a_3 \vee \neg a_7)$$

$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1$

$DL1 : a_1 : 0$

$DL2 : a_2 : 0, a_3 : 1$

$DL3 : a_5 : 0, a_6 : 1$

Add clause $(\neg a_3 \vee \neg a_7)$.

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9 \wedge (\neg a_3 \vee \neg a_7)$$

DL0 : $a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1$

DL1 : $a_1 : 0$

DL2 : $a_2 : 0, a_3 : 1$

DL3 : $a_5 : 0, a_6 : 1$

Conflict resolution is simple, since the new clause is already an asserting one.

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9 \wedge (\neg a_3 \vee \neg a_7)$$

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DL0 : $a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1, a_3 : 0$

DL1 : $a_1 : 0$

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DL1 : $a_1 : 0, a_2 : 1$

DL2 :

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9 \wedge (\neg a_3 \vee \neg a_7)$$

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DL2 : $a_5 : 0$

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DL1 : $a_1 : 0, a_2 : 1$

DL2 : $a_5 : 0, a_6 : 1$

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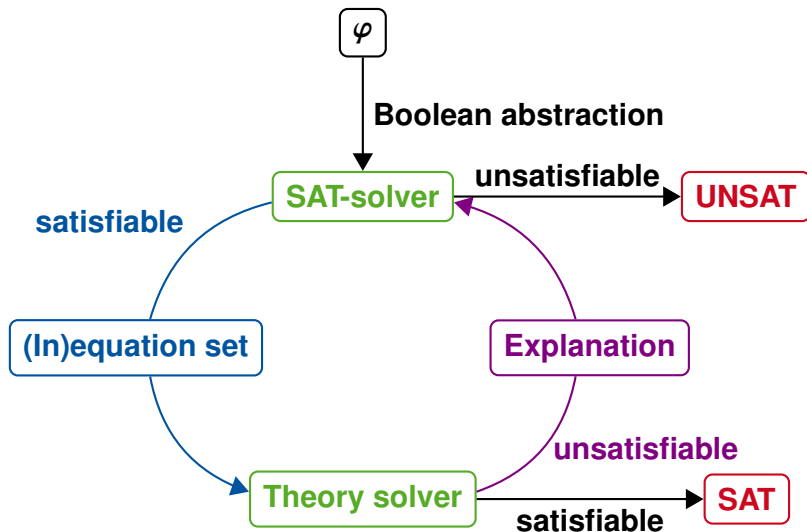
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DL2 : $a_5 : 0, a_6 : 1$

Solution found for the Boolean abstraction.

Full lazy SMT solving



$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1, a_3 : 0$ $DL1 : a_1 : 0, a_2 : 1$
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Theory solving

$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1, a_3 : 0$ $DL1 : a_1 : 0, a_2 : 1$

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Theory solving

$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1, a_3 : 0$ $DL1 : a_1 : 0, a_2 : 1$

$DL2 : a_5 : 0, a_6 : 1$

True theory constraints: $a_4, a_7, a_8, a_9, a_2, a_6$

$$\begin{aligned} & (\underbrace{p_1 = 0}_{a_1} \vee \underbrace{p_2 = 0}_{a_2} \vee \underbrace{p_3 = 0}_{a_3}) \wedge \underbrace{p_1 + p_2 + p_3 \geq 100}_{a_4} \wedge \\ & (\underbrace{p_1 \geq 5}_{a_5} \vee \underbrace{p_2 \geq 5}_{a_6}) \wedge \underbrace{p_3 \geq 10}_{a_7} \wedge \underbrace{p_1 + 2p_2 + 5p_3 \leq 180}_{a_8} \wedge \\ & \underbrace{3p_1 + 2p_2 + p_3 \leq 300}_{a_9} \wedge (\neg a_3 \vee \neg a_7) \end{aligned}$$

Theory solving

$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1, a_3 : 0$ $DL1 : a_1 : 0, a_2 : 1$

$DL2 : a_5 : 0, a_6 : 1$

True theory constraints: a_4, a_7, a_8, a_2, a_6

$$\begin{aligned} & \underbrace{(p_1 = 0)}_{a_1} \vee \underbrace{p_2 = 0}_{a_2} \vee \underbrace{p_3 = 0}_{a_3} \wedge \underbrace{p_1 + p_2 + p_3 \geq 100}_{a_4} \wedge \\ & \underbrace{(p_1 \geq 5)}_{a_5} \vee \underbrace{p_2 \geq 5}_{a_6} \wedge \underbrace{p_3 \geq 10}_{a_7} \wedge \underbrace{p_1 + 2p_2 + 5p_3 \leq 180}_{a_8} \wedge \\ & \underbrace{3p_1 + 2p_2 + p_3 \leq 300}_{a_9} \wedge (\neg a_3 \vee \neg a_7) \end{aligned}$$

Encoding:

$$\begin{array}{lll} a_4 : p_1 + p_2 + p_3 \geq 100 & a_7 : p_3 \geq 10 & a_8 : p_1 + 2p_2 + 5p_3 \leq 180 \\ a_9 : 3p_1 + 2p_2 + p_3 \leq 300 & a_2 : p_2 = 0 & a_6 : p_2 \geq 5 \end{array}$$

Is the conjunction of the following constraints satisfiable?

$$a_4 : p_1 + p_2 + p_3 \geq 100$$

$$a_7 : p_3 \geq 10$$

$$a_8 : p_1 + 2p_2 + 5p_3 \leq 180$$

$$a_9 : 3p_1 + 2p_2 + p_3 \leq 300$$

$$a_2 : p_2 = 0$$

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$$a_8 : p_1 + 2p_2 + 5p_3 \leq 180$$

$$a_9 : 3p_1 + 2p_2 + p_3 \leq 300$$

$$a_2 : p_2 = 0$$

$$a_6 : p_2 \geq 5$$

No.

Is the conjunction of the following constraints satisfiable?

$$a_4 : p_1 + p_2 + p_3 \geq 100$$

$$a_7 : p_3 \geq 10$$

$$a_8 : p_1 + 2p_2 + 5p_3 \leq 180$$

$$a_9 : 3p_1 + 2p_2 + p_3 \leq 300$$

$$a_2 : p_2 = 0$$

$$a_6 : p_2 \geq 5$$

No.

Reason:

Is the conjunction of the following constraints satisfiable?

$$a_4 : p_1 + p_2 + p_3 \geq 100$$

$$a_7 : p_3 \geq 10$$

$$a_8 : p_1 + 2p_2 + 5p_3 \leq 180$$

$$a_9 : 3p_1 + 2p_2 + p_3 \leq 300$$

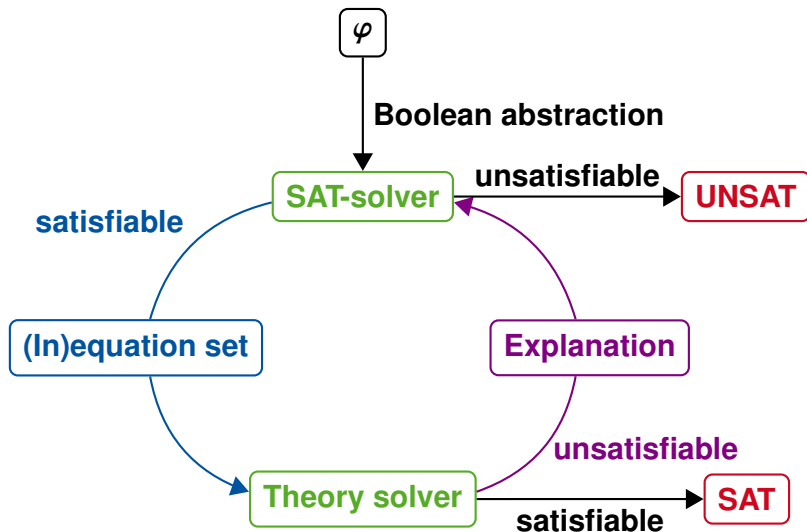
$$a_2 : p_2 = 0$$

$$a_6 : p_2 \geq 5$$

No.

Reason: $\underbrace{p_2 = 0}_{a_2} \wedge \underbrace{p_2 \geq 5}_{a_6}$ are conflicting.

Full lazy SMT solving



Add clause $(\neg a_2 \vee \neg a_6)$.

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9 \wedge (\neg a_3 \vee \neg a_7) \wedge$$
$$(\neg a_2 \vee \neg a_6)$$

DL0 : $a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1, a_3 : 0$

DL1 : $a_1 : 0, a_2 : 1$

DL2 : $a_5 : 0, a_6 : 1$

Add clause $(\neg a_2 \vee \neg a_6)$.

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9 \wedge (\neg a_3 \vee \neg a_7) \wedge$$
$$(\neg a_2 \vee \neg a_6)$$

DL0 : $a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1, a_3 : 0$

DL1 : $a_1 : 0, a_2 : 1$

DL2 : $a_5 : 0, a_6 : 1$

Conflict resolution is simple, since the new clause is already an asserting one.

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9 \wedge (\neg a_3 \vee \neg a_7) \wedge$$
$$(\neg a_2 \vee \neg a_6)$$

DL0 : $a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1, a_3 : 0$

DL1 : $a_1 : 0, a_2 : 1$

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9 \wedge (\neg a_3 \vee \neg a_7) \wedge$$
$$(\neg a_2 \vee \neg a_6)$$

DL0 : $a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1, a_3 : 0$

DL1 : $a_1 : 0, a_2 : 1, a_6 : 0$

$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9 \wedge (\neg a_3 \vee \neg a_7) \wedge$$
$$(\neg a_2 \vee \neg a_6)$$

DL0 : $a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1, a_3 : 0$

DL1 : $a_1 : 0, a_2 : 1, a_6 : 0, a_5 : 1$

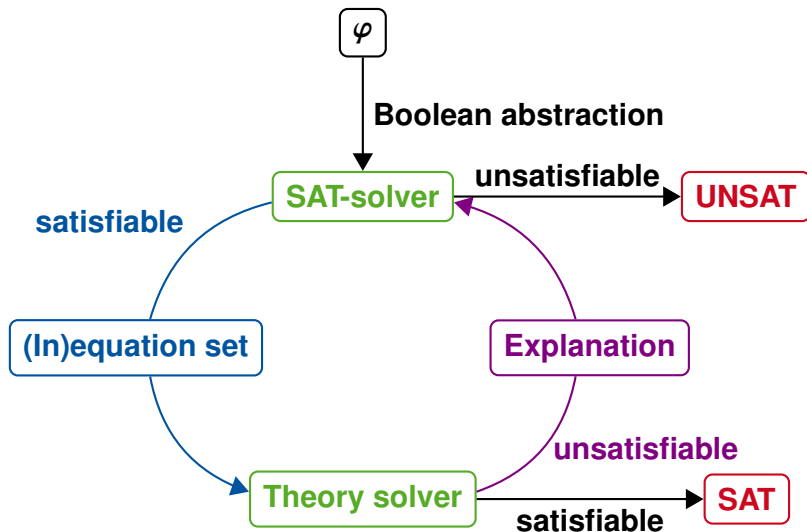
$$(a_1 \vee a_2 \vee a_3) \wedge a_4 \wedge (a_5 \vee a_6) \wedge a_7 \wedge a_8 \wedge a_9 \wedge (\neg a_3 \vee \neg a_7) \wedge$$
$$(\neg a_2 \vee \neg a_6)$$

DL0 : $a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1, a_3 : 0$

DL1 : $a_1 : 0, a_2 : 1, a_6 : 0, a_5 : 1$

Solution found for the Boolean abstraction.

Full lazy SMT solving



$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1, a_3 : 0$ $DL1 : a_1 : 0, a_2 : 1, a_6 : 0, a_5 : 1$

Theory solving

$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1, a_3 : 0$ $DL1 : a_1 : 0, a_2 : 1, a_6 : 0, a_5 : 1$

True theory constraints: $a_4, a_7, a_8, a_9, a_2, a_5$

Theory solving

$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1, a_3 : 0$ $DL1 : a_1 : 0, a_2 : 1, a_6 : 0, a_5 : 1$

True theory constraints: $a_4, a_7, a_8, a_9, a_2, a_5$

$$\begin{aligned} & \underbrace{(p_1 = 0)}_{a_1} \vee \underbrace{p_2 = 0}_{a_2} \vee \underbrace{p_3 = 0}_{a_3} \wedge \underbrace{p_1 + p_2 + p_3 \geq 100}_{a_4} \wedge \\ & \underbrace{(p_1 \geq 5)}_{a_5} \vee \underbrace{p_2 \geq 5}_{a_6} \wedge \underbrace{p_3 \geq 10}_{a_7} \wedge \underbrace{p_1 + 2p_2 + 5p_3 \leq 180}_{a_8} \wedge \\ & \underbrace{3p_1 + 2p_2 + p_3 \leq 300}_{a_9} \wedge (\neg a_3 \vee \neg a_7) \wedge (\neg a_2 \vee \neg a_6) \end{aligned}$$

Theory solving

$DL0 : a_4 : 1, a_7 : 1, a_8 : 1, a_9 : 1, a_3 : 0$ $DL1 : a_1 : 0, a_2 : 1, a_6 : 0, a_5 : 1$

True theory constraints: $a_4, a_7, a_8, a_9, a_2, a_5$

$$\begin{aligned} & \underbrace{(p_1 = 0)}_{a_1} \vee \underbrace{p_2 = 0}_{a_2} \vee \underbrace{p_3 = 0}_{a_3} \wedge \underbrace{p_1 + p_2 + p_3 \geq 100}_{a_4} \wedge \\ & \underbrace{(p_1 \geq 5)}_{a_5} \vee \underbrace{p_2 \geq 5}_{a_6} \wedge \underbrace{p_3 \geq 10}_{a_7} \wedge \underbrace{p_1 + 2p_2 + 5p_3 \leq 180}_{a_8} \wedge \\ & \underbrace{3p_1 + 2p_2 + p_3 \leq 300}_{a_9} \wedge (\neg a_3 \vee \neg a_7) \wedge (\neg a_2 \vee \neg a_6) \end{aligned}$$

Encoding:

$$\begin{array}{lll} a_4 : p_1 + p_2 + p_3 \geq 100 & a_7 : p_3 \geq 10 & a_8 : p_1 + 2p_2 + 5p_3 \leq 180 \\ a_9 : 3p_1 + 2p_2 + p_3 \leq 300 & a_2 : p_2 = 0 & a_5 : p_1 \geq 5 \end{array}$$

Is the conjunction of the following constraints satisfiable?

$$a_4 : p_1 + p_2 + p_3 \geq 100$$

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Is the conjunction of the following constraints satisfiable?

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$$a_8 : p_1 + 2p_2 + 5p_3 \leq 180$$

$$a_9 : 3p_1 + 2p_2 + p_3 \leq 300$$

$$a_2 : p_2 = 0$$

$$a_5 : p_1 \geq 5$$

Yes.

Is the conjunction of the following constraints satisfiable?

$$a_4 : p_1 + p_2 + p_3 \geq 100$$

$$a_7 : p_3 \geq 10$$

$$a_8 : p_1 + 2p_2 + 5p_3 \leq 180$$

$$a_9 : 3p_1 + 2p_2 + p_3 \leq 300$$

$$a_2 : p_2 = 0$$

$$a_5 : p_1 \geq 5$$

Yes. E.g.,

Is the conjunction of the following constraints satisfiable?

$$a_4 : p_1 + p_2 + p_3 \geq 100$$

$$a_7 : p_3 \geq 10$$

$$a_8 : p_1 + 2p_2 + 5p_3 \leq 180$$

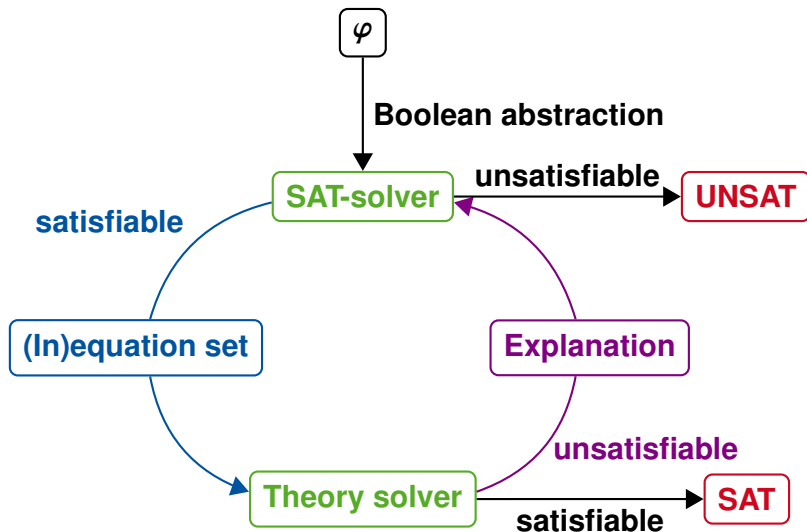
$$a_9 : 3p_1 + 2p_2 + p_3 \leq 300$$

$$a_2 : p_2 = 0$$

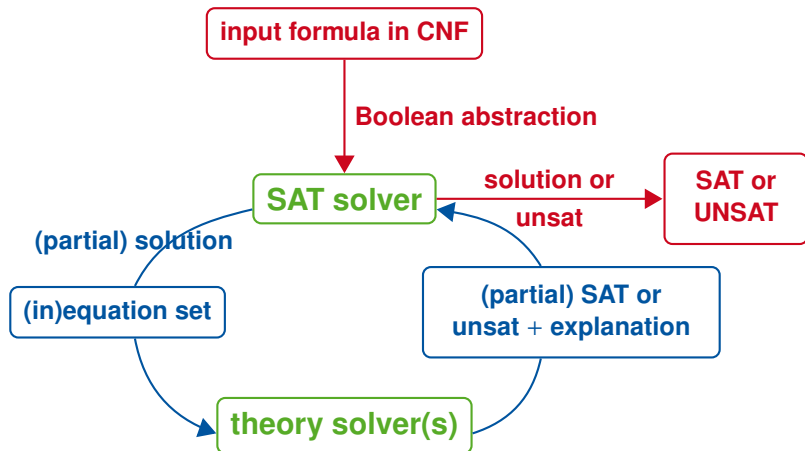
$$a_5 : p_1 \geq 5$$

Yes. E.g., $p_1 = 90$, $p_2 = 0$, $p_3 = 10$ is a solution.

Full lazy SMT solving



Less lazy SMT solving

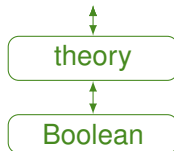


Requirements on the theory solver

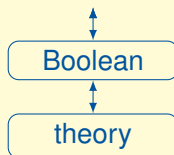
- 1 **Incrementality**: In less lazy solving we extend the set of constraints. The solver should make use of the previous satisfiability check for the check of the extended set.
- 2 **(Preferably minimal) infeasible subsets**: Compute a reason for unsatisfaction
- 3 **Backtracking**: The theory solver should be able to remove constraints in inverse chronological order.

Three SMT solving approaches

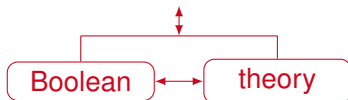
Eager SMT solving



Lazy SMT solving

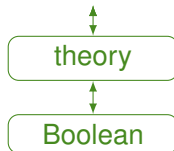


Model-constructing
satisfiability calculus
(MCSAT)

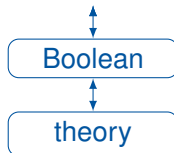


Three SMT solving approaches

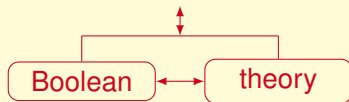
Eager SMT solving



Lazy SMT solving



Model-constructing
satisfiability calculus
(MCSAT)



The DPLL+CDCL idea [Davis et al., '60/61] [Marques-Silva et al., '96]

Exploration: \mathbb{B} -decision
Look-ahead: \mathbb{B} -propagation
Proof system: \mathbb{B} -conflict resolution

The DPLL+CDCL idea [Davis et al., '60/61] [Marques-Silva et al., '96]

Exploration: **B-decision**

Look-ahead: **B-propagation**

Proof system: **B-conflict resolution**

$$(a \vee b \vee c) \wedge (a \vee b \vee \neg c)$$

The DPLL+CDCL idea [Davis et al., '60/61] [Marques-Silva et al., '96]

Exploration: \mathbb{B} -decision

Look-ahead: \mathbb{B} -propagation

Proof system: \mathbb{B} -conflict resolution

$$(a \vee b \vee c) \wedge (a \vee b \vee \neg c)$$

\mathbb{B} -propagate -

The DPLL+CDCL idea [Davis et al., '60/61] [Marques-Silva et al., '96]

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Proof system: \mathbb{B} -conflict resolution

$$(a \vee b \vee c) \wedge (a \vee b \vee \neg c)$$

\mathbb{B} -propagate -

\mathbb{B} -decision $a = \text{false}$

The DPLL+CDCL idea [Davis et al., '60/61] [Marques-Silva et al., '96]

Exploration: \mathbb{B} -decision

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Proof system: \mathbb{B} -conflict resolution

$$(a \vee b \vee c) \wedge (a \vee b \vee \neg c)$$

\mathbb{B} -propagate -

\mathbb{B} -decision *$a = \text{false}$*

\mathbb{B} -propagate -

The DPLL+CDCL idea [Davis et al., '60/61] [Marques-Silva et al., '96]

Exploration: \mathbb{B} -decision

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Proof system: \mathbb{B} -conflict resolution

$$(a \vee b \vee c) \wedge (a \vee b \vee \neg c)$$

\mathbb{B} -propagate -

\mathbb{B} -decision *a = false*

\mathbb{B} -propagate -

\mathbb{B} -decision *b = false*

The DPLL+CDCL idea [Davis et al., '60/61] [Marques-Silva et al., '96]

Exploration: \mathbb{B} -decision

Look-ahead: \mathbb{B} -propagation

Proof system: \mathbb{B} -conflict resolution

$$(a \vee b \vee c) \wedge (a \vee b \vee \neg c)$$

\mathbb{B} -propagate	-
\mathbb{B} -decision	$a = \text{false}$
\mathbb{B} -propagate	-
\mathbb{B} -decision	$b = \text{false}$
\mathbb{B} -propagate	$c = \text{true}$ ⚡

The DPLL+CDCL idea [Davis et al., '60/61] [Marques-Silva et al., '96]

Exploration: \mathbb{B} -decision

Look-ahead: \mathbb{B} -propagation

Proof system: \mathbb{B} -conflict resolution

$$(a \vee b \vee c) \wedge (a \vee b \vee \neg c)$$

\mathbb{B} -propagate	-
\mathbb{B} -decision	$a = \text{false}$
\mathbb{B} -propagate	-
\mathbb{B} -decision	$b = \text{false}$
\mathbb{B} -propagate	$c = \text{true}$ ⚡
\mathbb{B} -conflict resolution	$(a \vee b)$

The MCSAT idea [de Moura, Jovanović, VMCAI'13]

Exploration:	B-decision	T-decision
Look-ahead:	B-propagation	T-propagation
Proof system:	B-conflict resolution	T-conflict resolution

$$(a \vee b \vee c) \wedge (a \vee b \vee \neg c)$$

B-propagate	-
B-decision	$a = \text{false}$
B-propagate	-
B-decision	$b = \text{false}$
B-propagate	$c = \text{true}$ ⚡
B-conflict resolution	$(a \vee b)$

The MCSAT idea [de Moura, Jovanović, VMCAI'13]

Exploration: \mathbb{B} -decision

\mathbb{T} -decision

Look-ahead: \mathbb{B} -propagation

\mathbb{T} -propagation

Proof system: \mathbb{B} -conflict resolution

\mathbb{T} -conflict resolution

$$(a \vee b \vee c) \wedge (a \vee b \vee \neg c)$$

$$\dots x \cdot y^2 < 0 \dots$$

\mathbb{B} -propagate

-

\mathbb{B} -decision

$a = \text{false}$

\mathbb{B} -propagate

-

\mathbb{B} -decision

$b = \text{false}$

\mathbb{B} -propagate

$c = \text{true}$ ⚡

\mathbb{B} -conflict resolution

$(a \vee b)$

The MCSAT idea [de Moura, Jovanović, VMCAI'13]

Exploration: \mathbb{B} -decision

\mathbb{T} -decision

Look-ahead: \mathbb{B} -propagation

\mathbb{T} -propagation

Proof system: \mathbb{B} -conflict resolution

\mathbb{T} -conflict resolution

$$(a \vee b \vee c) \wedge (a \vee b \vee \neg c)$$

$$\dots x \cdot y^2 < 0 \dots$$

\mathbb{B} -propagate

-

\mathbb{B} -propagate

-

\mathbb{B} -decision

$a = \text{false}$

\mathbb{B} -propagate

-

\mathbb{B} -decision

$b = \text{false}$

\mathbb{B} -propagate

$c = \text{true}$ ⚡

\mathbb{B} -conflict resolution

$(a \vee b)$

The MCSAT idea [de Moura, Jovanović, VMCAI'13]

Exploration:	B-decision	T-decision
Look-ahead:	B-propagation	T-propagation
Proof system:	B-conflict resolution	T-conflict resolution

$$(a \vee b \vee c) \wedge (a \vee b \vee \neg c)$$

$$\dots x \cdot y^2 < 0 \dots$$

B-propagate	-	B-propagate	-
B-decision	$a = \text{false}$	B-decision	$x \cdot y^2 < 0$
B-propagate	-		
B-decision	$b = \text{false}$		
B-propagate	$c = \text{true}$ ⚡		
B-conflict resolution	$(a \vee b)$		

The MCSAT idea [de Moura, Jovanović, VMCAI'13]

Exploration:	\mathbb{B} -decision	\mathbb{T} -decision
Look-ahead:	\mathbb{B} -propagation	\mathbb{T} -propagation
Proof system:	\mathbb{B} -conflict resolution	\mathbb{T} -conflict resolution

$$(a \vee b \vee c) \wedge (a \vee b \vee \neg c)$$

$$\dots x \cdot y^2 < 0 \dots$$

\mathbb{B} -propagate	-	\mathbb{B} -propagate	-
\mathbb{B} -decision	$a = \text{false}$	\mathbb{B} -decision	$x \cdot y^2 < 0$
\mathbb{B} -propagate	-	\mathbb{T} -propagate	$x \in (-\infty, \infty)$
\mathbb{B} -decision	$b = \text{false}$		
\mathbb{B} -propagate	$c = \text{true}$ ⚡		
\mathbb{B} -conflict resolution	$(a \vee b)$		

The MCSAT idea [de Moura, Jovanović, VMCAI'13]

Exploration:	\mathbb{B} -decision	\mathbb{T} -decision
Look-ahead:	\mathbb{B} -propagation	\mathbb{T} -propagation
Proof system:	\mathbb{B} -conflict resolution	\mathbb{T} -conflict resolution

$$(a \vee b \vee c) \wedge (a \vee b \vee \neg c)$$

$$\dots x \cdot y^2 < 0 \dots$$

\mathbb{B} -propagate	-	\mathbb{B} -propagate	-
\mathbb{B} -decision	$a = \text{false}$	\mathbb{B} -decision	$x \cdot y^2 < 0$
\mathbb{B} -propagate	-	\mathbb{T} -propagate	$x \in (-\infty, \infty)$
\mathbb{B} -decision	$b = \text{false}$	\mathbb{T} -decision	$x = 1$
\mathbb{B} -propagate	$c = \text{true}$ ⚡		
\mathbb{B} -conflict resolution	$(a \vee b)$		

The MCSAT idea [de Moura, Jovanović, VMCAI'13]

Exploration:	B-decision	T-decision
Look-ahead:	B-propagation	T-propagation
Proof system:	B-conflict resolution	T-conflict resolution

$$(a \vee b \vee c) \wedge (a \vee b \vee \neg c)$$

$$\dots x \cdot y^2 < 0 \dots$$

B-propagate	-	B-propagate	-
B-decision	$a = \text{false}$	B-decision	$x \cdot y^2 < 0$
B-propagate	-	T-propagate	$x \in (-\infty, \infty)$
B-decision	$b = \text{false}$	T-decision	$x = 1$
B-propagate	$c = \text{true}$ ⚡	T-propagate	$y \in \emptyset$ ⚡
B-conflict resolution	$(a \vee b)$		

The MCSAT idea [de Moura, Jovanović, VMCAI'13]

Exploration:	B -decision	T -decision
Look-ahead:	B -propagation	T -propagation
Proof system:	B -conflict resolution	T -conflict resolution

$$(a \vee b \vee c) \wedge (a \vee b \vee \neg c)$$

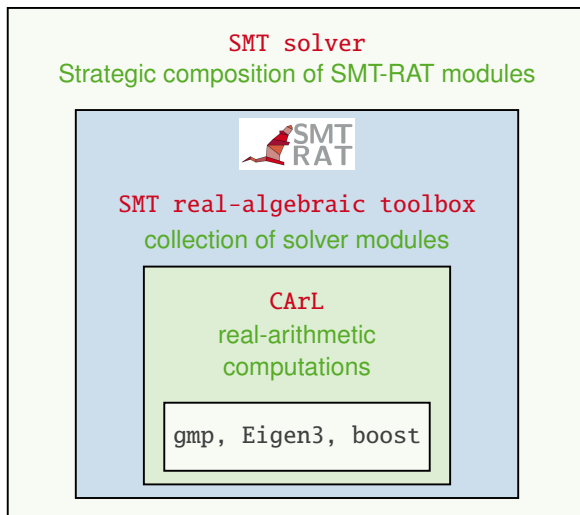
$$\dots x \cdot y^2 < 0 \dots$$

B -propagate	-	B -propagate	-
B -decision	$a = \text{false}$	B -decision	$x \cdot y^2 < 0$
B -propagate	-	T -propagate	$x \in (-\infty, \infty)$
B -decision	$b = \text{false}$	T -decision	$x = 1$
B -propagate	$c = \text{true}$ ⚡	T -propagate	$y \in \emptyset$ ⚡
B -conflict resolution	$(a \vee b)$	T -conflict resolution	$(x \cdot y^2 < 0 \rightarrow x < 0)$

Contents

- SAT solving
 - Exploration (also called enumeration)
 - Boolean constraint propagation (BCP)
 - Conflict resolution and backtracking
 - Exploration revisited
- SMT solving
 - Approaches
 - SMT-RAT
 - SMT-LIB
 - SMT solvers as integrated engines
 - Future challenges
- Hands-on material
 - SAT solving
 - SMT solving

Our SMT-RAT library [SAT'12, SAT'15]



- MIT licensed source code: github.com/smtrat/smtrat
- Documentation: smtrat.github.io

Solver modules in SMT-RAT [SAT'12, SAT'15]

CArL library: basic arithmetic datatypes and computations [Sapientia'18, NFM'11, CAI'11]

Basic modules

SAT solver

CNF converter

Preprocessing/simplifying modules

Non-algebraic decision procedures

Equalities and uninterpreted functions

Bit-vectors

Bit-blasting

Interval constraint propagation

Pseudo-Boolean formulas

Algebraic decision procedures

Gauß+Fourier-Motzkin, FMplex [GandALF'23]

Gröbner bases [CAI'13]

MCSAT (FM,VS,CAD) [2xSC²'19]

Simplex [ISSAC'21]

Cylindrical algebraic decomposition [SC²'21, CADE-24, JSC'19, SC²'17, 3 PhDs]

Cylindrical algebraic covering [SMT'23, JLAMP'21, SYNASC'21, PhD Kremer]

Virtual substitution [FCT'11, SC²'17, 1 PhD]

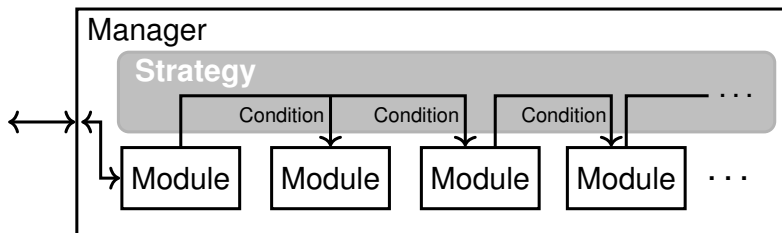
Subtropical satisfiability [NFM'23]

Generalized branch-and-bound [CASC'16]

Cube tests

Linearization

Strategic composition of solver modules in SMT-RAT



Contents

- SAT solving
 - Exploration (also called enumeration)
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 - SAT solving
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The Satisfiability Modulo Theories Library

The screenshot shows a web browser window with the title "SMT-LIB The Satisfiability Modulo Theories Library — Mozilla Firefox". The address bar shows the URL "smtlib.cs.uiowa.edu/index.shtml". The website has a navigation bar with links: Home, About, News, Standard, Benchmarks, Software, and Credits. The main heading is "SMT-LIB THE SATISFIABILITY MODULO THEORIES LIBRARY".

SMT-LIB is an international initiative aimed at facilitating research and development in [Satisfiability Modulo Theories](#) (SMT). Since its inception in 2003, the initiative has pursued these aims by focusing on the following concrete goals.

- Provide standard rigorous descriptions of background theories used in SMT systems.
- Develop and promote common input and output languages for [SMT solvers](#).
- Connect developers, researchers and users of SMT, and develop a community around it.
- Establish and make available to the research community a large library of benchmarks for SMT solvers.
- Collect and promote software tools useful to the SMT community.

This website provides access to the following main artifacts of the initiative.

- Documents describing the SMT-LIB input/output language for SMT solvers and its semantics;
- Specifications of background theories and *logics*;
- A large library of input problems, or benchmarks, written in the SMT-LIB language.
- Links to SMT solvers and related tools and utilities.

On the right side, there is a vertical navigation menu with links: Home, About, News, Standard, Language, Theories, Logics, Examples, Benchmarks, Software, Solvers, Utilities, Contact, Related, and Credits.

The Satisfiability Modulo Theories Library

The screenshot shows a web browser window with the title "SMT-LIB The Satisfiability Modulo Theories Library — Mozilla Firefox". The address bar shows the URL "smtlib.cs.uiowa.edu/language.shtml". The browser's bookmark bar is visible with "Most Visited" and "Getting Started". The website has a dark blue header with navigation links: Home, About, News, Standard, Benchmarks, Software, and Credits. Below the header is a large blue banner with the text "SMT-LIB" and "THE SATISFIABILITY MODULO THEORIES LIBRARY". The main content area is white and features a "Language" section with three entries: "The SMT-LIB Standard: Version 2.6", "The SMT-LIB Standard: Version 2.5", and "The SMT-LIB v2 Language and Tools: A Tutorial". Each entry includes the authors, the latest official release, and previous releases with links to PDFs and bib files. A "Previous Versions" section follows, mentioning subsumed versions and a deprecated version 2.0. A right-hand sidebar contains a vertical list of links: Home, About, News, Standard, Language, Theories, Logics, Examples, Benchmarks, Software, Solvers, Utilities, Contact, Related, and Credits.

SMT-LIB The Satisfiability Modulo Theories Library — Mozilla Firefox

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Log Word OC Pro FDi Who Jan me Impr The SMT Doc Err Jan LD #2 Am Lib strli The Mu Cor Me SMT x +

smtlib.cs.uiowa.edu/language.shtml

Most Visited Getting Started

Other Bookmarks

Home About News Standard Benchmarks Software Credits

SMT-LIB

THE SATISFIABILITY MODULO THEORIES LIBRARY

Language

The SMT-LIB Standard: Version 2.6, by Clark Barrett, Pascal Fontaine, and Cesare Tinelli.
Latest official release of Version 2.6 of the SMT-LIB standard. [pdf | bib]
Previous releases: 2021-04-02; 2017-07-18

The SMT-LIB Standard: Version 2.5, by Clark Barrett, Pascal Fontaine, and Cesare Tinelli.
Latest official release of Version 2.5 of the SMT-LIB standard. [pdf | bib]
Previous releases: 2015-05-28;

The SMT-LIB v2 Language and Tools: A Tutorial, by David R. Cok.
A tutorial on Version 2.0 of the language and on a number of SMT-LIB tools developed by the author. [pdf]

Previous Versions

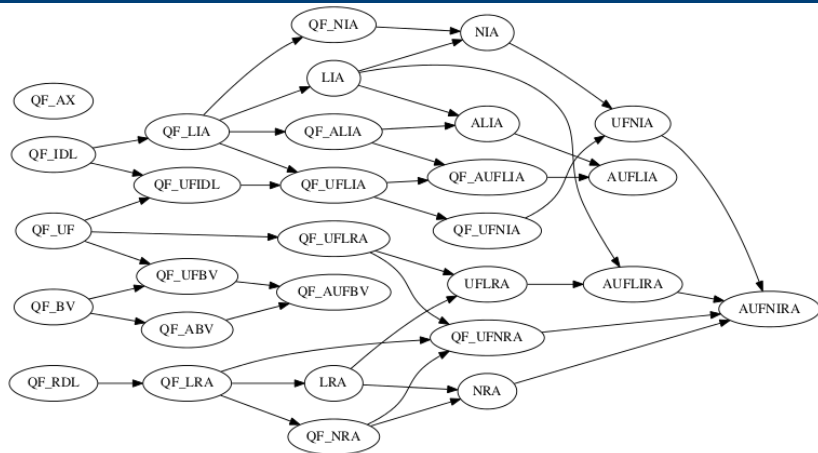
The following earlier versions of the standard are subsumed by the current one and so are deprecated. They are listed here only for historical reasons.

The SMT-LIB Standard: Version 2.0, by Clark Barrett, Aaron Stump, and Cesare Tinelli.
Last official release of Version 2.0 of the SMT-LIB standard (9 Sep 2012).
[pdf | bib]
Previous releases (Change log): Version 2.0 21-12-10; 28-08-10; 30-03-10.

The SMT-LIB Standard: Version 1.2, by Silvio Ranise and Cesare Tinelli.

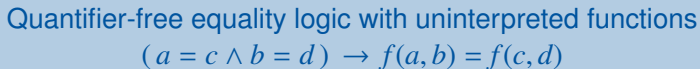
Home
About
News
Standard
Language
Theories
Logics
Examples
Benchmarks
Software
Solvers
Utilities
Contact
Related
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SMT-LIB theories



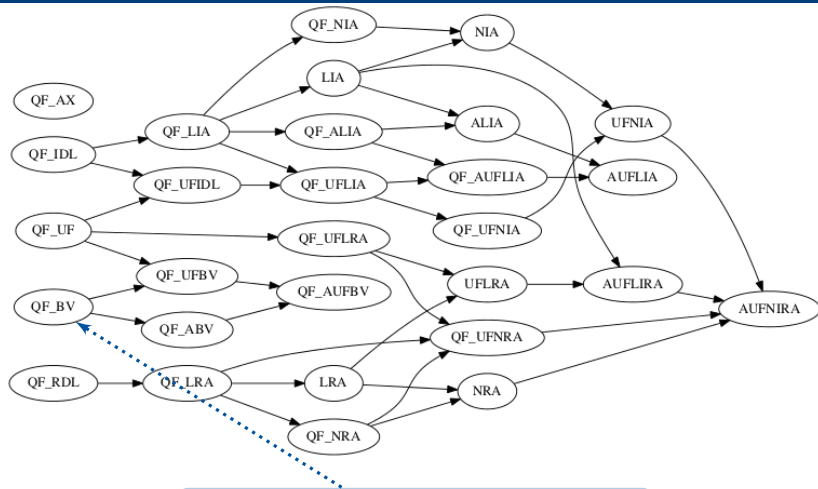
Source: <http://smtlib.cs.uiowa.edu/logics.shtml>

Erika Ábrahám -



78 / 108

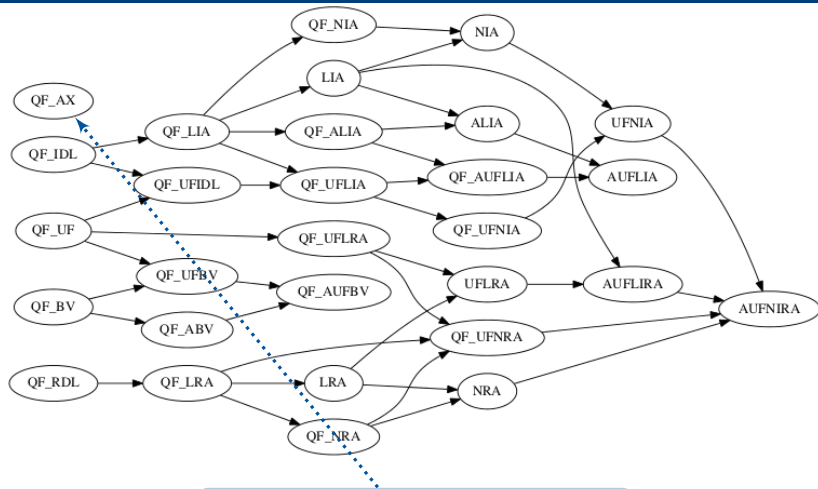
SMT-LIB theories



Quantifier-free bit-vector arithmetic
 $(a|b) \leq (a \& b)$

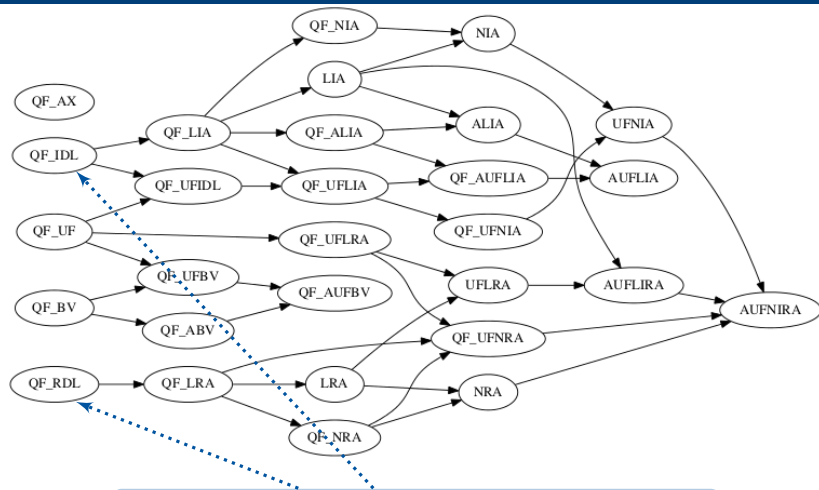
Source: <http://smtlib.cs.uiowa.edu/logics.shtml>

SMT-LIB theories



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SMT-LIB theories

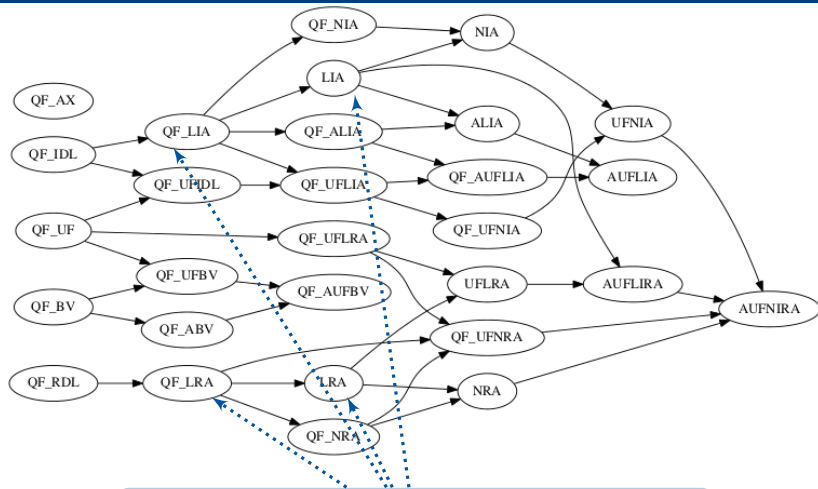


Quantifier-free integer/rational difference logic

$$x - y \sim 0, \sim \in \{<, \leq, =, \geq, >\}$$

Source: <http://smtlib.cs.uiowa.edu/logics.shtml>

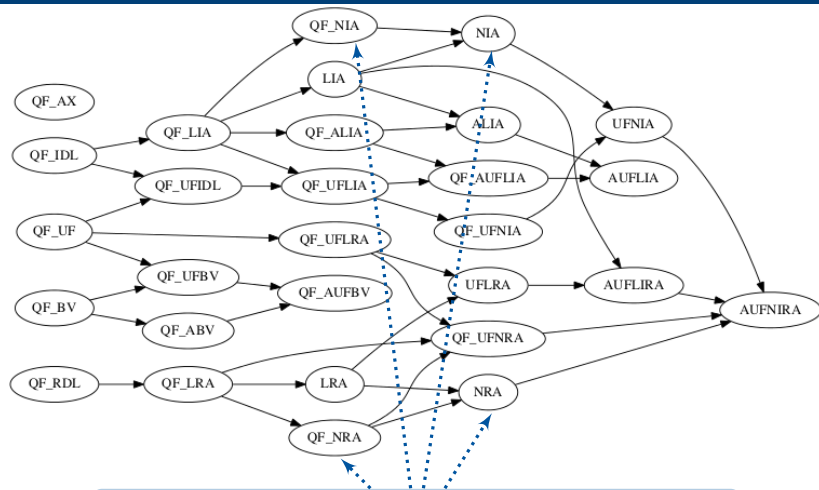
SMT-LIB theories



(Quantifier-free) real/integer linear arithmetic
 $3x + 7y = 8$

Source: <http://smtlib.cs.uiowa.edu/logics.shtml>

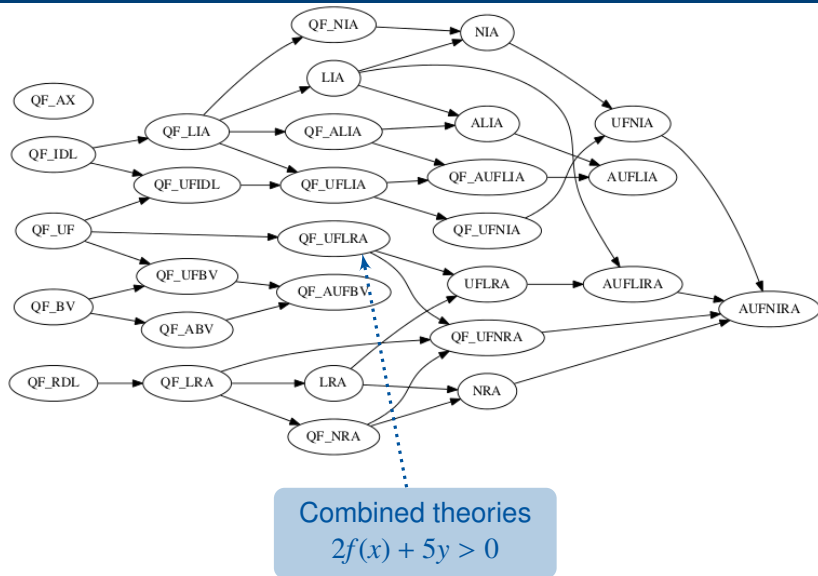
SMT-LIB theories



(Quantifier-free) real/integer non-linear arithmetic
 $x^2 + 2xy + y^2 \geq 0$

Source: <http://smtlib.cs.uiowa.edu/logics.shtml>

SMT-LIB theories

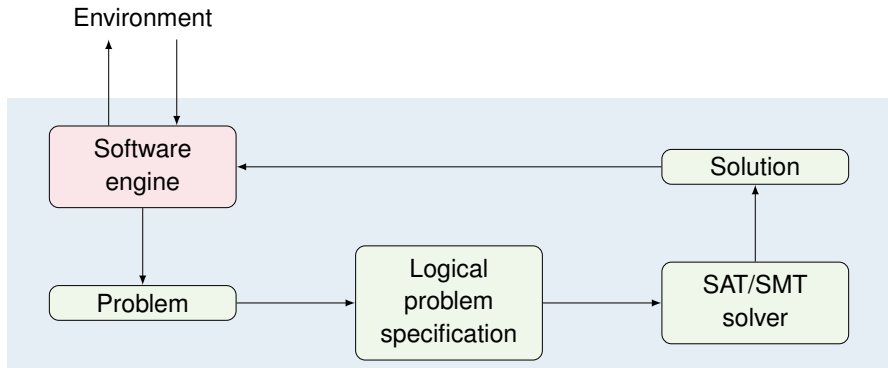


Source: <http://smtlib.cs.uiowa.edu/logics.shtml>

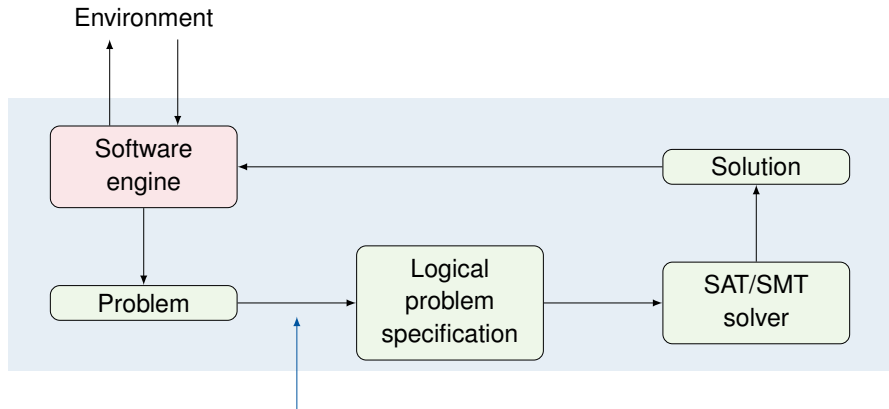
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Embedding SAT/SMT solvers

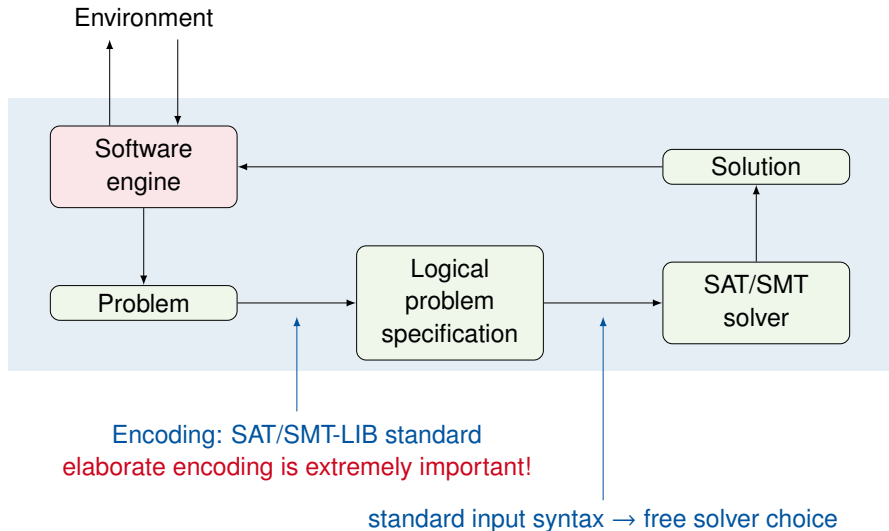


Embedding SAT/SMT solvers

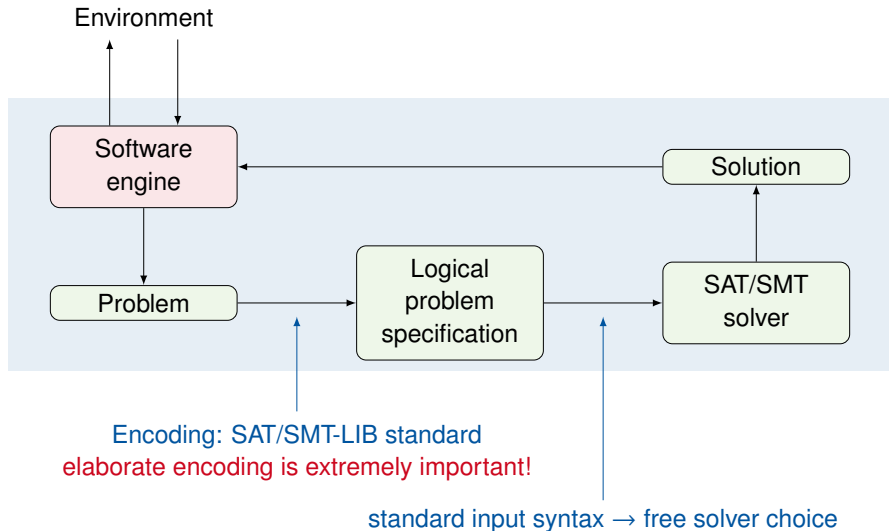


Encoding: SAT/SMT-LIB standard
elaborate encoding is extremely important!

Embedding SAT/SMT solvers

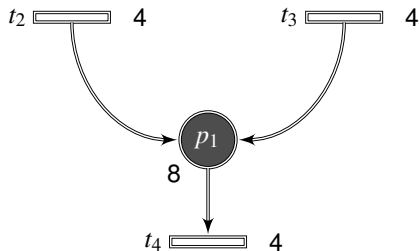


Embedding SAT/SMT solvers

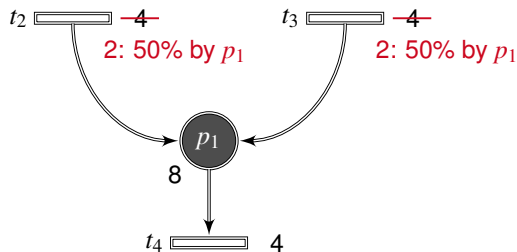


Next: some applications of **SMT** solvers

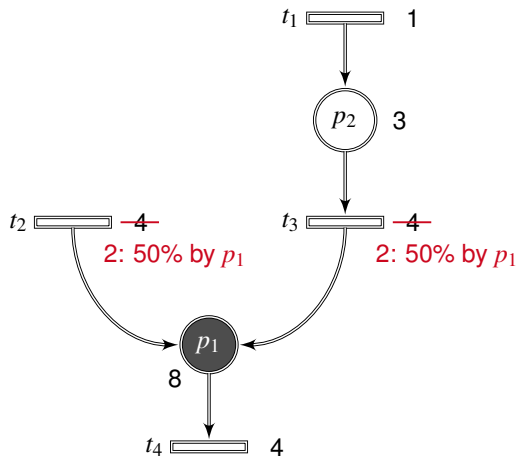
1. Rate adaption in hybrid Petri nets



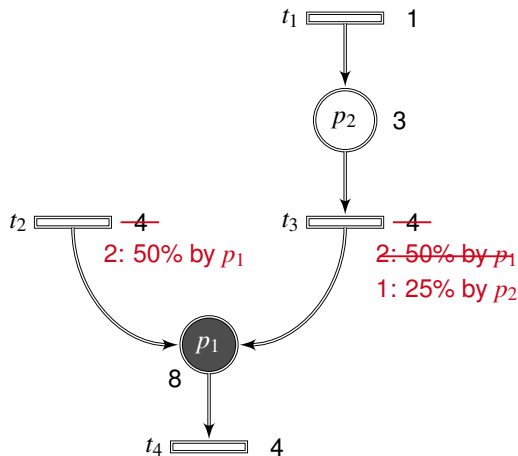
1. Rate adaption in hybrid Petri nets



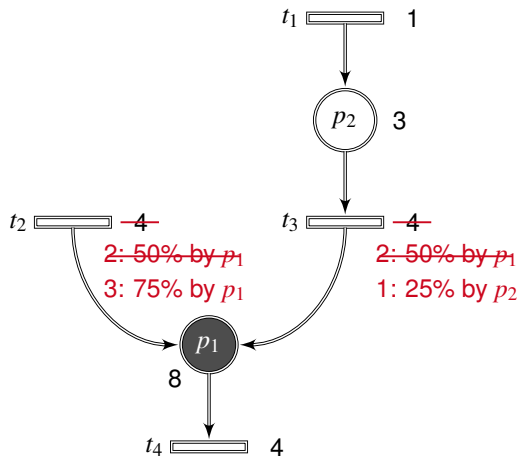
1. Rate adaption in hybrid Petri nets



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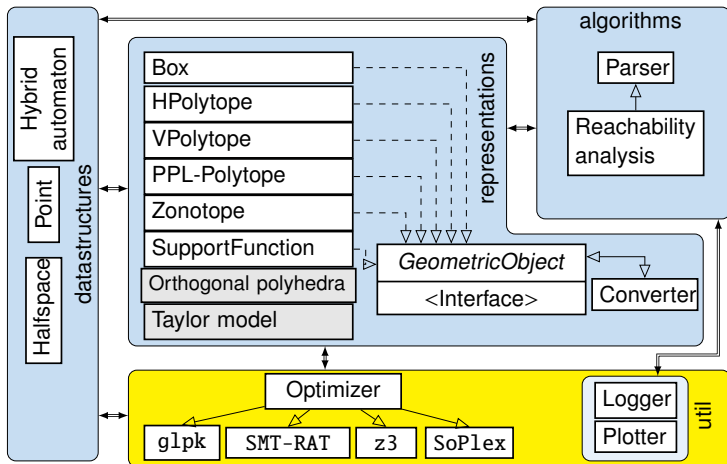
1. Rate adaption in hybrid Petri nets



1. SMT encoding of rate adaption fixedpoint

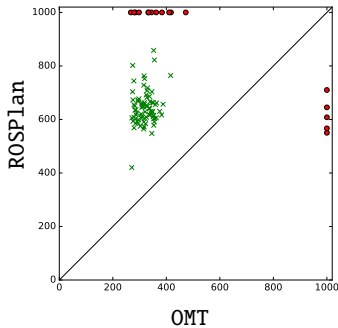
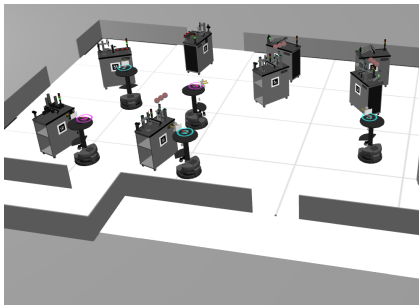
- (1) $\left[\bigwedge_{p \in P} 0 \leq \text{factor}_p \leq 1 \right] \wedge \left[\bigwedge_{t \in T_a} 0 \leq \text{factor}_t \leq 1 \right] \wedge$
- (2) $\left[\bigwedge_{t \in T_a} ((\text{owner}_t = \text{source}(t) \wedge \text{owner}_t \in P_{\text{empty}}) \vee (\text{owner}_t = \text{target}(t) \wedge \text{owner}_t \in P_{\text{full}})) \right] \wedge$
- (3) $\left[\bigwedge_{p \in P} \text{in}_p = (\sum_{t \in \text{In}(p) \cap T_a} \text{factor}_t \cdot \text{nominal_rate}(t)) + (\sum_{t \in \text{In}(p) \cap T_{na}} \text{nominal_rate}(t)) \wedge \right.$
 $\quad \text{out}_p = (\sum_{t \in \text{Out}(p) \cap T_a} \text{factor}_t \cdot \text{nominal_rate}(t)) + (\sum_{t \in \text{Out}(p) \cap T_{na}} \text{nominal_rate}(t)) \left. \right] \wedge$
- (4) $\left[\bigwedge_{p \in P_{\text{empty}}} \left((\text{factor}_p = 1 \vee \bigvee_{t \in \text{Out}(p)} \text{owner}_t = p) \wedge \right. \right.$
 $\quad \left(\bigwedge_{t \in \text{Out}(p)} (\text{owner}_t = p \rightarrow \text{factor}_t = \text{factor}_p) \wedge \right.$
 $\quad \left. (\text{owner}_t \neq p \rightarrow \text{factor}_t < \text{factor}_p) \right) \wedge$
 $\quad \left. \text{in}_p \geq \text{out}_p \wedge (\text{factor}_p < 1 \rightarrow \text{in}_p = \text{out}_p) \right] \wedge$
- (5) $\left[\bigwedge_{p \in P_{\text{full}}} \left((\text{factor}_p = 1 \vee \bigvee_{t \in \text{In}(p)} \text{owner}_t = p) \wedge \right. \right.$
 $\quad \left(\bigwedge_{t \in \text{In}(p)} (\text{owner}_t = p \rightarrow \text{factor}_t = \text{factor}_p) \wedge \right.$
 $\quad \left. (\text{owner}_t \neq p \rightarrow \text{factor}_t \leq \text{factor}_p) \right) \wedge$
 $\quad \left. \text{in}_p \leq \text{out}_p \wedge (\text{factor}_p < 1 \rightarrow \text{in}_p = \text{out}_p) \right] \wedge$

2. Reachability analysis for hybrid systems with HyPro



Source: E. Ábrahám, X. Chen, S. Sankaranarayanan, S. Schupp. PhD Chen, PhD Schupp, Information and Computation'22, IRI'18, SEFM'18, TACAS'18, NFM'17, QAPL'17, ARCH'15, CyPhy'15, NFM'15, FMCAD'14, CAV'13, FTSCS'13, NOLCOS'13, RTSS'12, EUROCAST'11, RP'11.

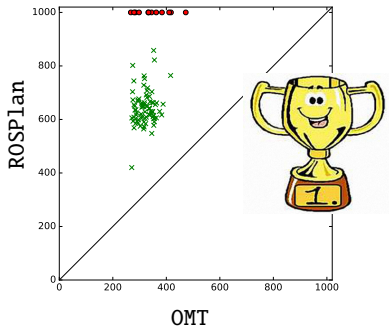
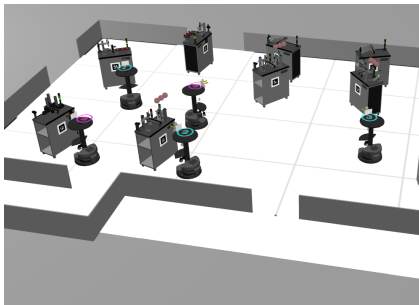
3. Planning with Optimization Modulo Theories



Source: E. Ábrahám, G. Lakemeyer, F. Leofante, T. D. Niemüller, A. Tacchella.

PhD Leofante, IJCAI'20, Information Systems Frontiers 2019, ECMS'19, AAAI'18, iFM'18, ICAPS'17, PlanRob'17, IRI'17.

3. Planning with Optimization Modulo Theories



Source: E. Ábrahám, G. Lakemeyer, F. Leofante, T. D. Niemüller, A. Tacchella.

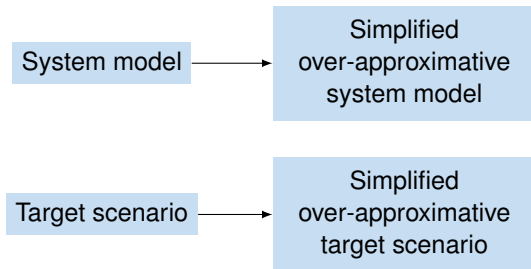
PhD Leofante, IJCAI'20, Information Systems Frontiers 2019, ECMS'19, AAAI'18, iFM'18, ICAPS'17, PlanRob'17, IRI'17.

4. Relevant domains for testing (Siemens)

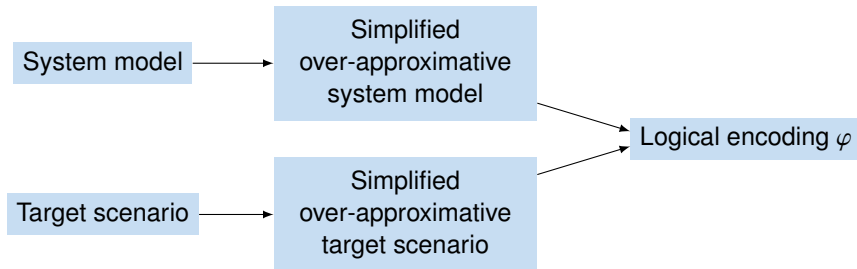
System model

Target scenario

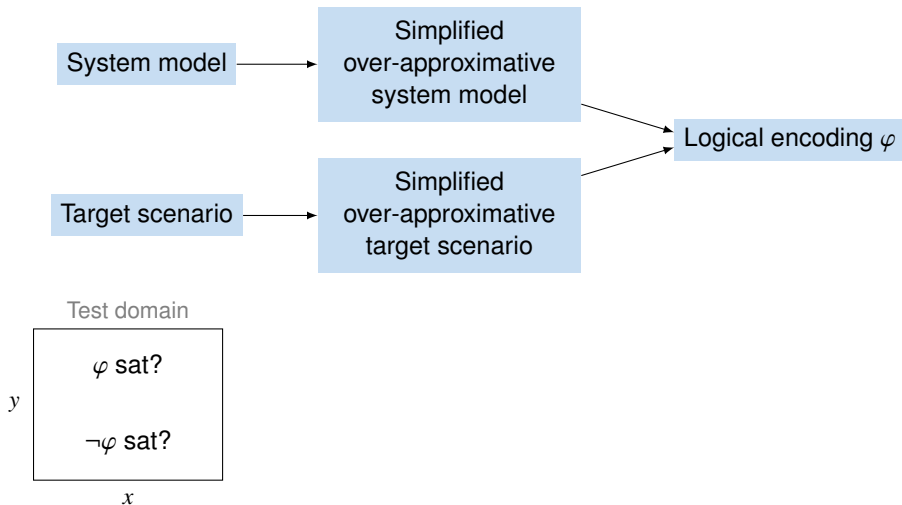
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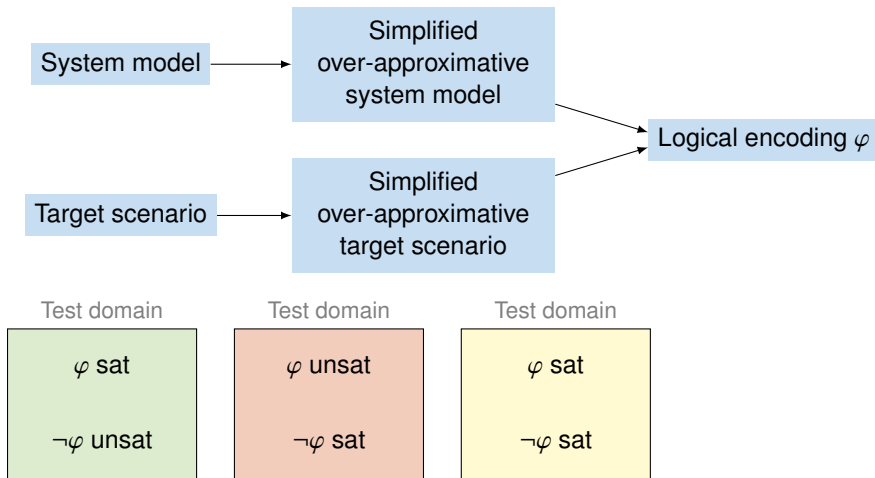
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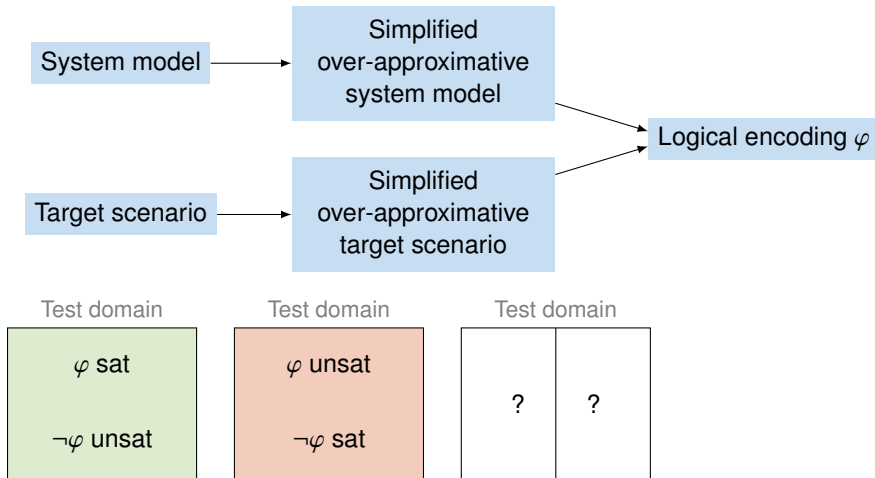
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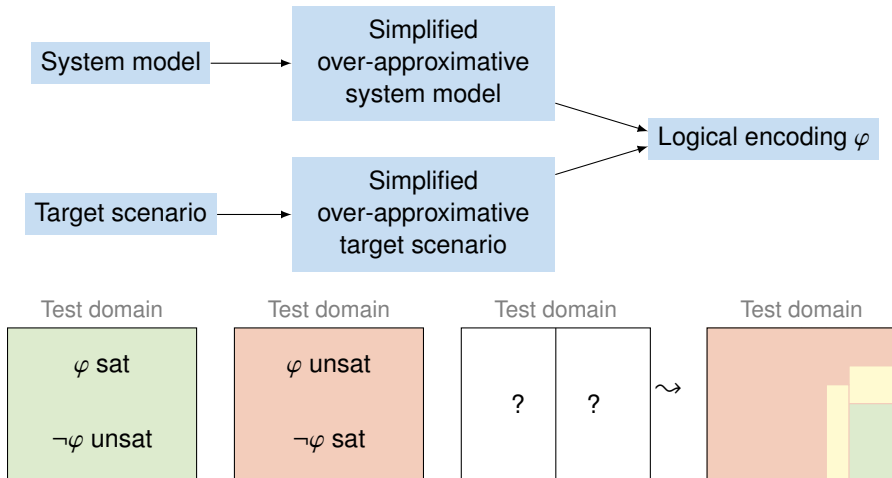
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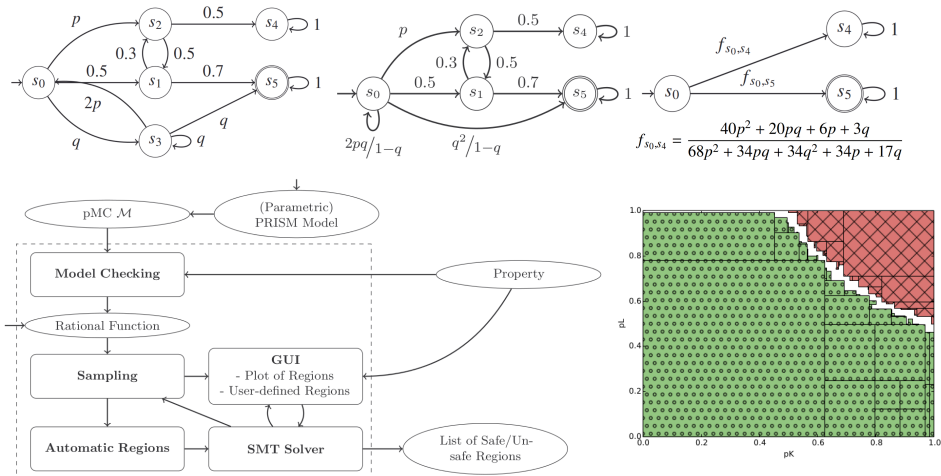
4. Relevant domains for testing (Siemens)



4. Relevant domains for testing (Siemens)



5. Parameter synthesis for probabilistic systems



Source: C. Dehnert, S. Junges, N. Jansen, F. Corzilius, M. Volk, H. Bruintjes, J.-P. Katoen, E. Ábrahám.

PROPhESY: A probabilistic parameter synthesis tool.

In Proc. of CAV'15.

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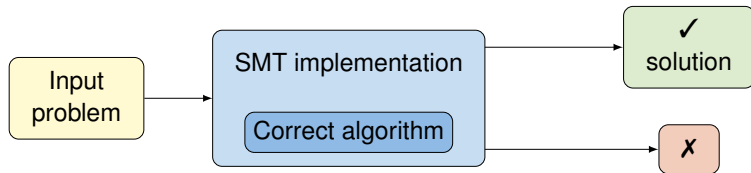
- Standard input language, benchmarks
- Online usage, command-line, programming interfaces
- Black-box usage possible, but specific knowledge is advantageous
 - for efficient usage and
 - selection of the best fitting tool (e.g. fast vs complete).

- Theoretical basics: algorithms with correctness proofs.

Correct algorithm

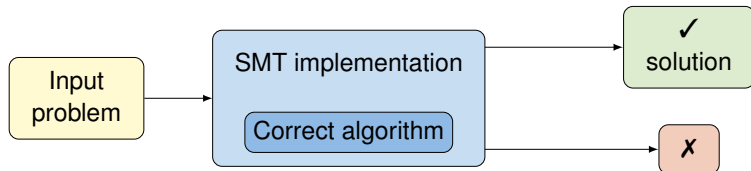
Proof generation

- Theoretical basics: algorithms with correctness proofs.
- Reliable tools: in QF_NRA for SMT-COMP'21, no bugs discovered on large benchmark sets.



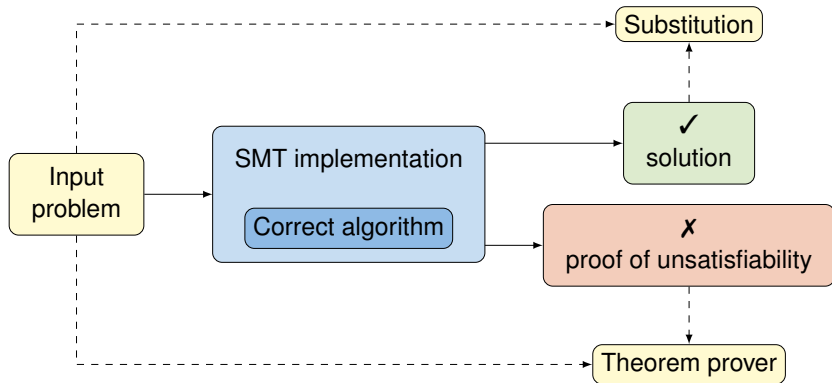
Proof generation

- Theoretical basics: algorithms with correctness proofs.
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- But still: bugs can remain undetected for a long time.



Proof generation

- Theoretical basics: algorithms with correctness proofs.
- Reliable tools: in QF_NRA for SMT-COMP'21, no bugs discovered on large benchmark sets.
- But still: bugs can remain undetected for a long time.
- Solution: **automatically checkable proof certificates**.



Further functionalities

- Model generation
- Explanations of unsatisfiability (unsat cores, interpolants)
- Optimization

- Satisfiability for quantified formulas
- Quantifier elimination (get all solutions symbolically)

- Scalability
 - Preprocessing
 - Heuristics, especially variable ordering
 - Machine learning
 - Closer integration of decision procedures
 - Parallelization

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You need to have installed...

- Python
- Z3

<https://github.com/exercism/z3/blob/main/docs/INSTALLATION.md>

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- Suppose we can solve the satisfiability problem... how can this help us?

- Suppose we can solve the satisfiability problem... how can this help us?
- There are numerous problems in the industry that are solved via the satisfiability problem of propositional logic
 - Logistics
 - Planning
 - Electronic Design Automation industry
 - Cryptography
 - ...

Example 1: Placement of wedding guests

- Three chairs in a row: 1, 2, 3
- We need to place Aunt, Sister and Father.
- Constraints:
 - Aunt doesn't want to sit near Father
 - Aunt doesn't want to sit in the left chair
 - Sister doesn't want to sit to the right of Father

Example 1: Placement of wedding guests

- Three chairs in a row: 1, 2, 3
- We need to place Aunt, Sister and Father.
- Constraints:
 - Aunt doesn't want to sit near Father
 - Aunt doesn't want to sit in the left chair
 - Sister doesn't want to sit to the right of Father
- Q: Can we satisfy these constraints?

Example 1 (continued)

Example 1 (continued)

- Notation:

Example 1 (continued)

■ **Notation:** Aunt = 1, Sister = 2, Father = 3

Left chair = 1, Middle chair = 2, Right chair = 3

Introduce a propositional variable for each pair (person, chair):

$x_{p,c}$ = “person p is sited in chair c ” for $1 \leq p, c \leq 3$

Example 1 (continued)

- **Notation:** Aunt = 1, Sister = 2, Father = 3

Left chair = 1, Middle chair = 2, Right chair = 3

Introduce a propositional variable for each pair (person, chair):

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- **Constraints:**

Example 1 (continued)

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- **Constraints:**

Aunt doesn't want to sit near Father:

Example 1 (continued)

- **Notation:** Aunt = 1, Sister = 2, Father = 3

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Introduce a propositional variable for each pair (person, chair):

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- **Constraints:**

Aunt doesn't want to sit near Father:

$$((x_{1,1} \vee x_{1,3}) \rightarrow \neg x_{3,2}) \wedge (x_{1,2} \rightarrow (\neg x_{3,1} \wedge \neg x_{3,3}))$$

Example 1 (continued)

- **Notation:** Aunt = 1, Sister = 2, Father = 3

Left chair = 1, Middle chair = 2, Right chair = 3

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Aunt doesn't want to sit in the left chair:

Example 1 (continued)

- **Notation:** Aunt = 1, Sister = 2, Father = 3

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- **Constraints:**

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$$((x_{1,1} \vee x_{1,3}) \rightarrow \neg x_{3,2}) \wedge (x_{1,2} \rightarrow (\neg x_{3,1} \wedge \neg x_{3,3}))$$

Aunt doesn't want to sit in the left chair:

$$\neg x_{1,1}$$

Example 1 (continued)

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- **Constraints:**

Aunt doesn't want to sit near Father:

$$((x_{1,1} \vee x_{1,3}) \rightarrow \neg x_{3,2}) \wedge (x_{1,2} \rightarrow (\neg x_{3,1} \wedge \neg x_{3,3}))$$

Aunt doesn't want to sit in the left chair:

$$\neg x_{1,1}$$

Sister doesn't want to sit to the right of Father:

Example 1 (continued)

- **Notation:** Aunt = 1, Sister = 2, Father = 3

Left chair = 1, Middle chair = 2, Right chair = 3

Introduce a propositional variable for each pair (person, chair):

$x_{p,c}$ = “person p is sited in chair c ” for $1 \leq p, c \leq 3$

- **Constraints:**

Aunt doesn't want to sit near Father:

$$((x_{1,1} \vee x_{1,3}) \rightarrow \neg x_{3,2}) \wedge (x_{1,2} \rightarrow (\neg x_{3,1} \wedge \neg x_{3,3}))$$

Aunt doesn't want to sit in the left chair:

$$\neg x_{1,1}$$

Sister doesn't want to sit to the right of Father:

$$(x_{3,1} \rightarrow \neg x_{2,2}) \wedge (x_{3,2} \rightarrow \neg x_{2,3})$$

Example 1 (continued)

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Each person is placed:

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$$(x_{1,1} \vee x_{1,2} \vee x_{1,3}) \wedge (x_{2,1} \vee x_{2,2} \vee x_{2,3}) \wedge (x_{3,1} \vee x_{3,2} \vee x_{3,3})$$

$$\bigwedge_{p=1}^3 \bigvee_{c=1}^3 x_{p,c}$$

Example 1 (continued)

Each person is placed:

$$(x_{1,1} \vee x_{1,2} \vee x_{1,3}) \wedge (x_{2,1} \vee x_{2,2} \vee x_{2,3}) \wedge (x_{3,1} \vee x_{3,2} \vee x_{3,3})$$

$$\bigwedge_{p=1}^3 \bigvee_{c=1}^3 x_{p,c}$$

At most one person per chair:

Example 1 (continued)

Each person is placed:

$$(x_{1,1} \vee x_{1,2} \vee x_{1,3}) \wedge (x_{2,1} \vee x_{2,2} \vee x_{2,3}) \wedge (x_{3,1} \vee x_{3,2} \vee x_{3,3})$$

$$\bigwedge_{p=1}^3 \bigvee_{c=1}^3 x_{p,c}$$

At most one person per chair:

$$\bigwedge_{p1=1}^3 \bigwedge_{p2=p1+1}^3 \bigwedge_{c=1}^3 (\neg x_{p1,c} \vee \neg x_{p2,c})$$

Example 2: Assignment of frequencies

- n radio stations
- For each station assign one of k transmission frequencies, $k < n$.
- E – set of pairs of stations, that are too close to have the same frequency.

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- n radio stations
- For each station assign one of k transmission frequencies, $k < n$.
- E – set of pairs of stations, that are too close to have the same frequency.
- **Q:** Can we assign to each station a frequency, such that no station pairs from E have the same frequency?

Example 2 (continued)

- Notation:

Example 2 (continued)

■ Notation:

$x_{s,f}$ = “station s is assigned frequency f ” for $1 \leq s \leq n$, $1 \leq f \leq k$

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$x_{s,f}$ = “station s is assigned frequency f ” for $1 \leq s \leq n$, $1 \leq f \leq k$

- **Constraints:**

Every station is assigned at least one frequency:

Example 2 (continued)

- **Notation:**

$x_{s,f}$ = “station s is assigned frequency f ” for $1 \leq s \leq n$, $1 \leq f \leq k$

- **Constraints:**

Every station is assigned at least one frequency:

$$\bigwedge_{s=1}^n \left(\bigvee_{f=1}^k x_{s,f} \right)$$

Example 2 (continued)

■ Notation:

$x_{s,f}$ = “station s is assigned frequency f ” for $1 \leq s \leq n$, $1 \leq f \leq k$

■ Constraints:

Every station is assigned at least one frequency:

$$\bigwedge_{s=1}^n \left(\bigvee_{f=1}^k x_{s,f} \right)$$

Every station is assigned at most one frequency:

Example 2 (continued)

■ Notation:

$x_{s,f}$ = “station s is assigned frequency f ” for $1 \leq s \leq n$, $1 \leq f \leq k$

■ Constraints:

Every station is assigned at least one frequency:

$$\bigwedge_{s=1}^n \left(\bigvee_{f=1}^k x_{s,f} \right)$$

Every station is assigned at most one frequency:

$$\bigwedge_{s=1}^n \bigwedge_{f1=1}^{k-1} \bigwedge_{f2=f1+1}^k (\neg x_{s,f1} \vee \neg x_{s,f2})$$

Example 2 (continued)

■ Notation:

$x_{s,f}$ = “station s is assigned frequency f ” for $1 \leq s \leq n$, $1 \leq f \leq k$

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Every station is assigned at least one frequency:

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Close stations are not assigned the same frequency:

Example 2 (continued)

■ Notation:

$x_{s,f}$ = “station s is assigned frequency f ” for $1 \leq s \leq n$, $1 \leq f \leq k$

■ Constraints:

Every station is assigned at least one frequency:

$$\bigwedge_{s=1}^n \left(\bigvee_{f=1}^k x_{s,f} \right)$$

Every station is assigned at most one frequency:

$$\bigwedge_{s=1}^n \bigwedge_{f1=1}^{k-1} \bigwedge_{f2=f1+1}^k (\neg x_{s,f1} \vee \neg x_{s,f2})$$

Close stations are not assigned the same frequency:

For each $(s1, s2) \in E$,

$$\bigwedge_{f=1}^k (\neg x_{s1,f} \vee \neg x_{s2,f})$$

Example 3: Seminar topic assignment

- n participants
- n topics
- Set of preferences $E \subseteq \{1, \dots, n\} \times \{1, \dots, n\}$
 $(p, t) \in E$ means: participant p would take topic t

Example 3: Seminar topic assignment

- n participants
- n topics
- Set of preferences $E \subseteq \{1, \dots, n\} \times \{1, \dots, n\}$
 $(p, t) \in E$ means: participant p would take topic t
- **Q:** Can we assign to each participant a topic which he/she is willing to take?

Example 3 (continued)

- Notation:

Example 3 (continued)

- **Notation:** $x_{p,t}$ = “participant p is assigned topic t ”

Example 3 (continued)

- **Notation:** $x_{p,t}$ = “participant p is assigned topic t ”
- **Constraints:**

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- **Constraints:**
 - Each participant is assigned at least one topic:

Example 3 (continued)

- **Notation:** $x_{p,t}$ = “participant p is assigned topic t ”
- **Constraints:**
Each participant is assigned at least one topic:

$$\bigwedge_{p=1}^n \left(\bigvee_{t=1}^n x_{p,t} \right)$$

Example 3 (continued)

■ **Notation:** $x_{p,t}$ = “participant p is assigned topic t ”

■ **Constraints:**

Each participant is assigned at least one topic:

$$\bigwedge_{p=1}^n \left(\bigvee_{t=1}^n x_{p,t} \right)$$

Each participant is assigned at most one topic:

Example 3 (continued)

- **Notation:** $x_{p,t}$ = “participant p is assigned topic t ”
- **Constraints:**

Each participant is assigned at least one topic:

$$\bigwedge_{p=1}^n \left(\bigvee_{t=1}^n x_{p,t} \right)$$

Each participant is assigned at most one topic:

$$\bigwedge_{p=1}^n \bigwedge_{t1=1}^{n-1} \bigwedge_{t2=t1+1}^n \left(\neg x_{p,t1} \vee \neg x_{p,t2} \right)$$

Example 3 (continued)

■ **Notation:** $x_{p,t}$ = “participant p is assigned topic t ”

■ **Constraints:**

Each participant is assigned at least one topic:

$$\bigwedge_{p=1}^n \left(\bigvee_{t=1}^n x_{p,t} \right)$$

Each participant is assigned at most one topic:

$$\bigwedge_{p=1}^n \bigwedge_{t1=1}^{n-1} \bigwedge_{t2=t1+1}^n \left(\neg x_{p,t1} \vee \neg x_{p,t2} \right)$$

Each participant is willing to take his/her assigned topic:

Example 3 (continued)

■ **Notation:** $x_{p,t}$ = “participant p is assigned topic t ”

■ **Constraints:**

Each participant is assigned at least one topic:

$$\bigwedge_{p=1}^n \left(\bigvee_{t=1}^n x_{p,t} \right)$$

Each participant is assigned at most one topic:

$$\bigwedge_{p=1}^n \bigwedge_{t1=1}^{n-1} \bigwedge_{t2=t1+1}^n \left(\neg x_{p,t1} \vee \neg x_{p,t2} \right)$$

Each participant is willing to take his/her assigned topic:

$$\bigwedge_{p=1}^n \bigwedge_{(p,t) \notin E} \neg x_{p,t}$$

Example 3 (continued)

Example 3 (continued)

Each topic is assigned to at most one participant:

Example 3 (continued)

Each topic is assigned to at most one participant:

$$\bigwedge_{t=1}^n \bigwedge_{p1=1}^n \bigwedge_{p2=p1+1}^n (\neg x_{p1,t} \vee \neg x_{p2,t})$$

DIMACS input syntax for SAT solvers

The DIMACS format for SAT solvers has three types of lines:

- **header:** “ p cnf n m ” in which
 - n denotes the highest variable index and
 - m the number of clauses.
- **clauses:** a sequence of integers ending with “0”
- **comments:** any line starting with “c “

Example:

		<i>c example</i>
		<i>p cnf 2 4</i>
$(a \vee b)$	\wedge	1 2 0
$(\neg a \vee b)$	\wedge	-1 2 0
$(a \vee \neg b)$	\wedge	1 -2 0
$(\neg a \vee \neg b)$	\wedge	-1 -2 0

Solving propositional logic with SMT solvers

- SMT-LIB format:
<https://microsoft.github.io/z3guide/docs/logic/propositional-logic>
- Python interface:
<https://ericpony.github.io/z3py-tutorial/guide-examples.htm>
- Both:
<https://cvc5.github.io/tutorials/beginners/>

Contents

- SAT solving
 - Exploration (also called enumeration)
 - Boolean constraint propagation (BCP)
 - Conflict resolution and backtracking
 - Exploration revisited
- SMT solving
 - Approaches
 - SMT-RAT
 - SMT-LIB
 - SMT solvers as integrated engines
 - Future challenges
- Hands-on material
 - SAT solving
 - SMT solving

Syntax of core theory

```
:sorts ((Bool 0))
:fun (
  (true Bool)
  (false Bool)
  (not Bool Bool)
  (and Bool Bool Bool :left-assoc)
  ...
  (par (A) (= A A Bool :chainable))
  (par (A) (ite Bool A A A))
  ...
)
```


Syntax of real theory

```
:sorts ((Real 0))
:fun (
  ...
  (+ Real Real Real :left-assoc)
  (* Real Real Real :left-assoc)
  ...
  (< Real Real Bool :chainable)
  ...
)
```

SMT-LIB commands

- Lisp-like script language
- Supported by essentially all SMT solvers
- Easy to parse and extend

Boolean example

```
(set-logic QF_UF)
(declare-const p Bool)
(assert (and p (not p)))
(check-sat)
```

SMT-LIB commands

- Lisp-like script language
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Linear integer example

```
(set-logic QF_LIA)
(declare-const x Int)
(declare-const y Int)
(assert (= (- x y) (+ x (- y) 1)))
(check-sat)
```

SMT-LIB commands

- Lisp-like script language
- Supported by essentially all SMT solvers
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Unsatisfiable cores

```
(set-logic QF_UF)
(set-option :produce-unsat-cores true)
(declare-const p Bool)
(declare-const q Bool)
(declare-const r Bool)
(assert (! (=> p q) :named a))
(assert (! (=> q r) :named b))
(assert (! (not (=> p r)) :named c))
(assert ...)
(check-sat)
(get-unsat-core)
```

SMT-LIB commands

- Lisp-like script language
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Optimization

```
(set-logic QF_LIA)
(declare-const x Int)
(declare-const y Int)
(assert (and (< y 5) (< x 2)))
(assert (< (- y x) 1))
(maximize (+ x y))
(check-sat)
(get-objectives)
```

Solving theory formulas with SMT solvers

- <https://cvc5.github.io/tutorials/beginners>
- SMT-LIB input:
<https://microsoft.github.io/z3guide/docs/logic/intro/>
<https://smt-lib.org/examples.shtml>
- Z3/cvc5 Python interface:
<https://ericpony.github.io/z3py-tutorial/guide-examples.htm>