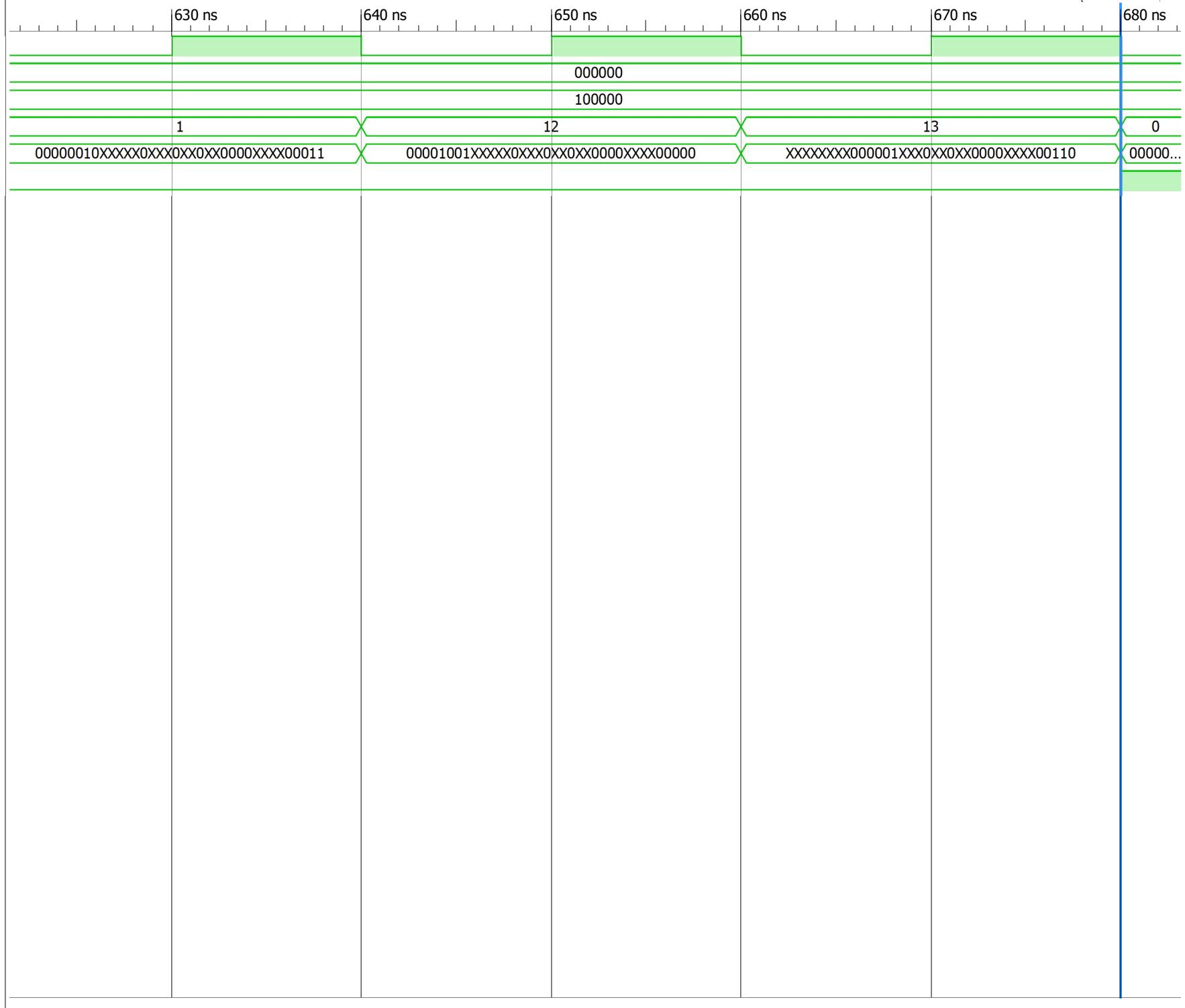
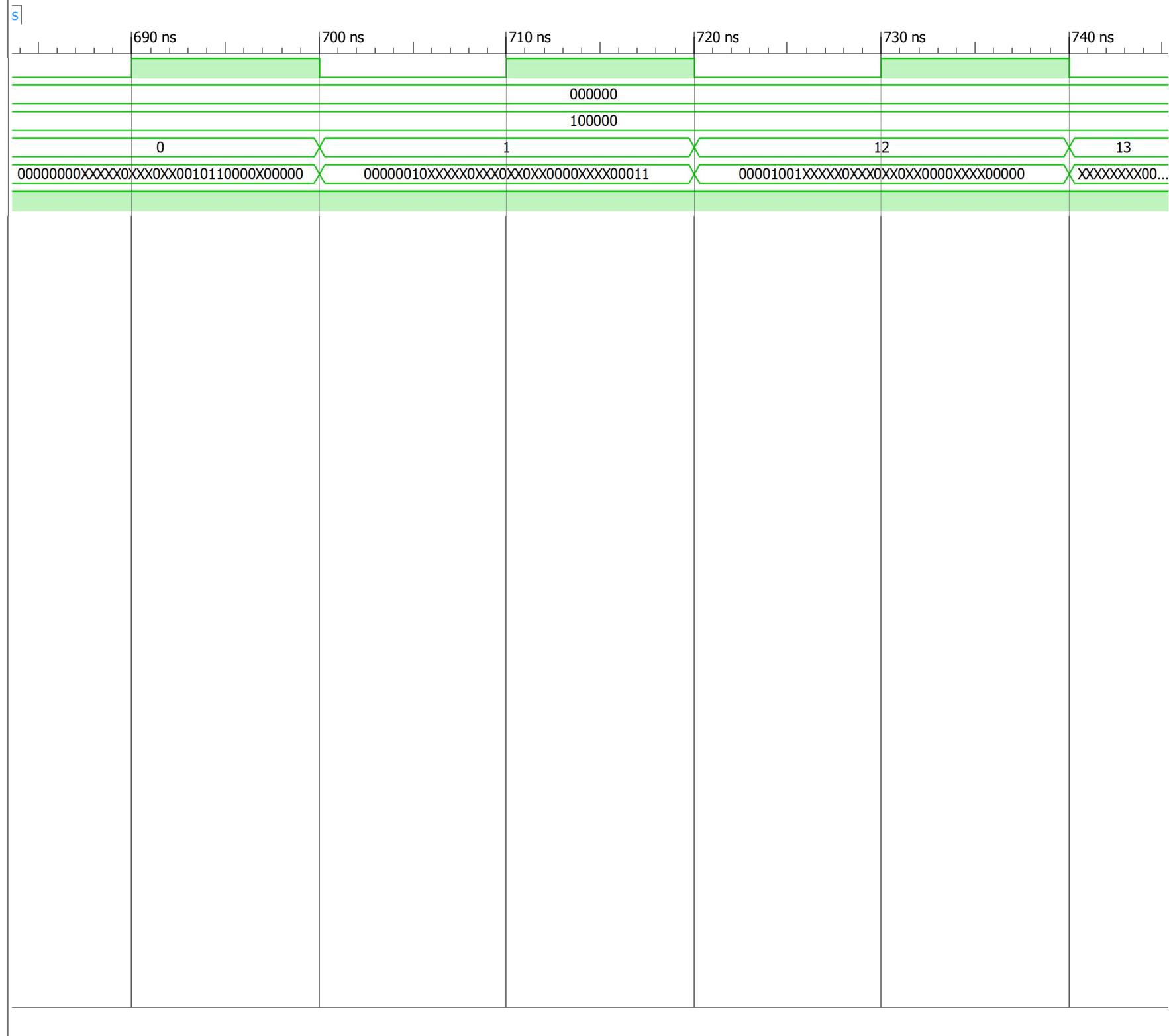


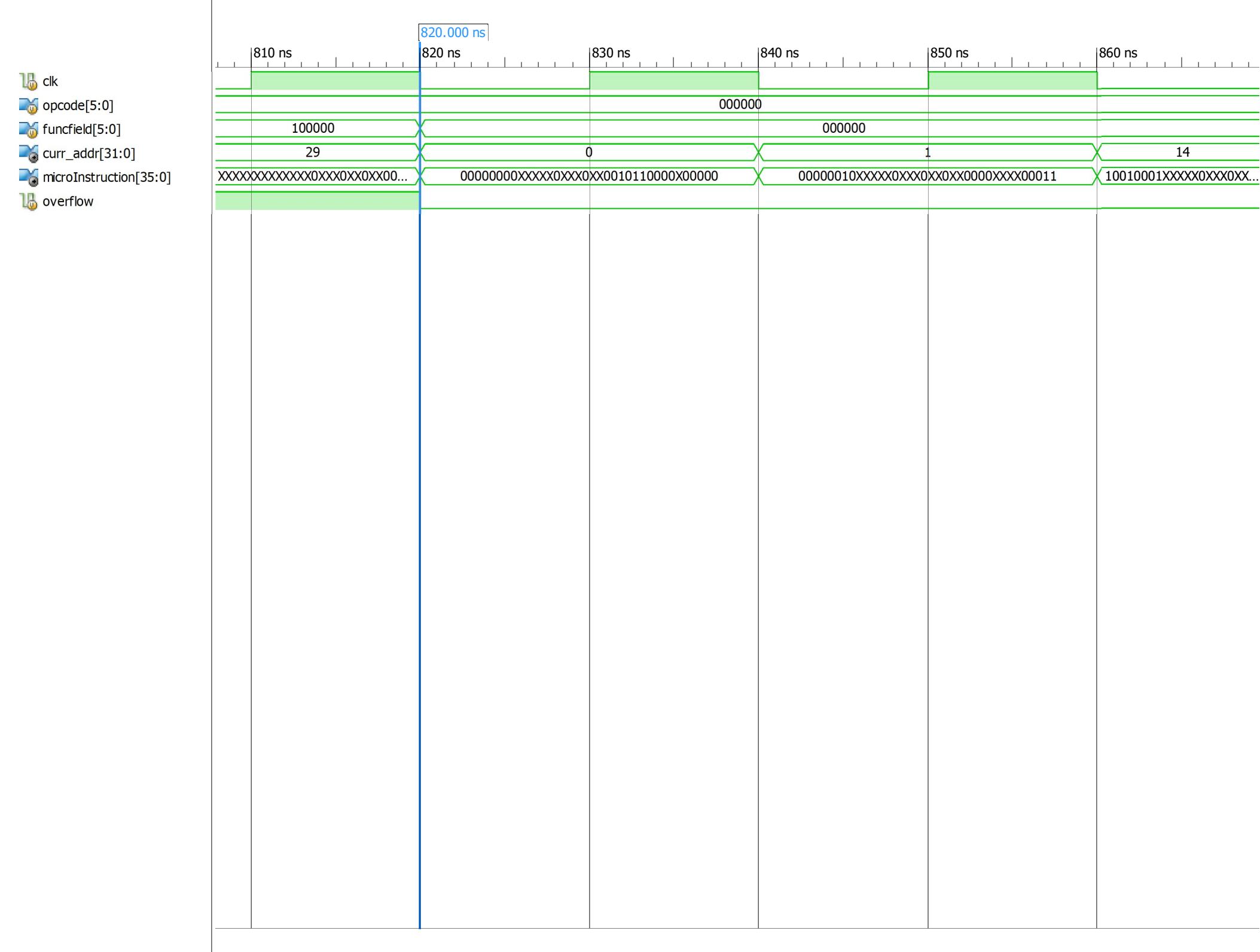
680.000 ns

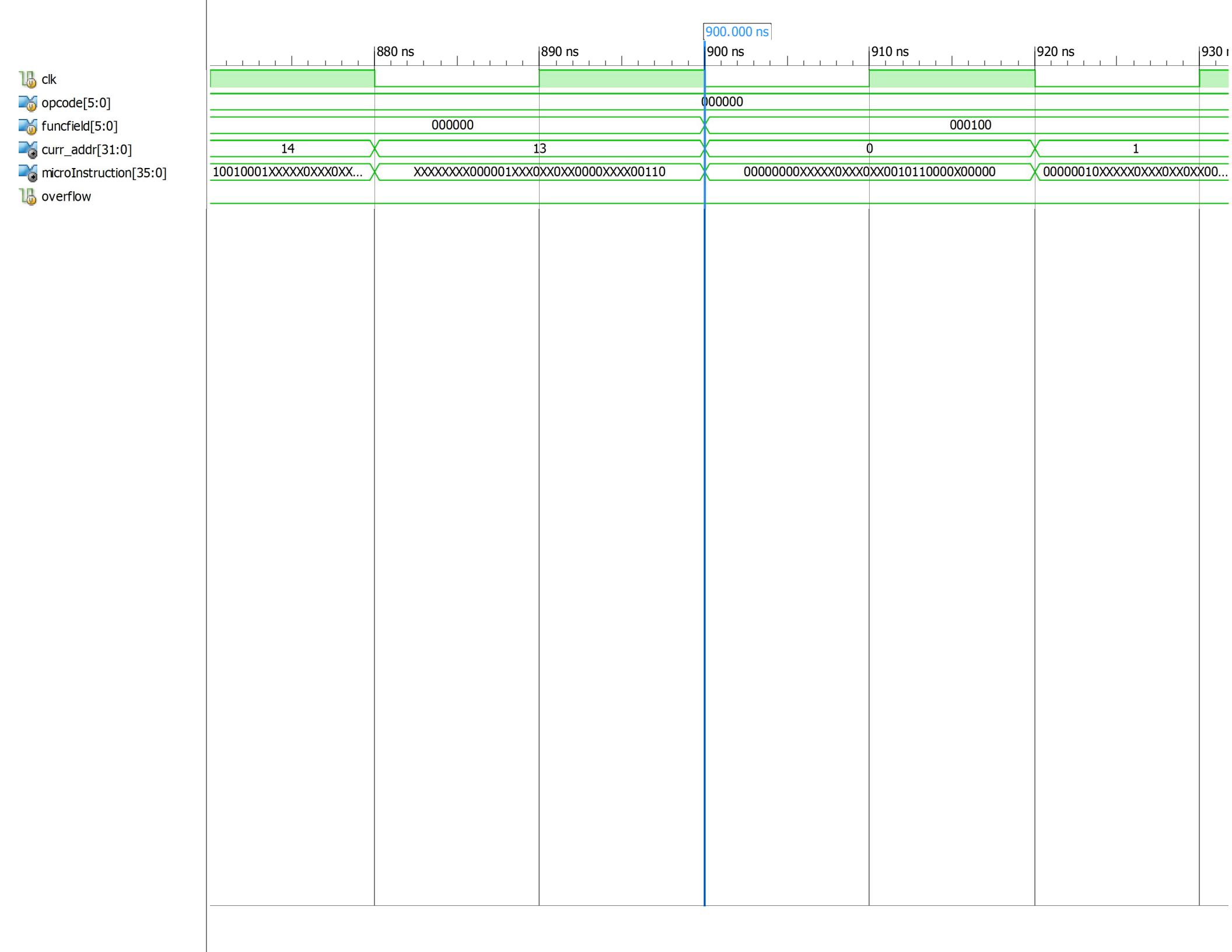
clk  
opcode[5:0]  
funcfield[5:0]  
curr\_addr[31:0]  
microInstruction[35:0]  
overflow

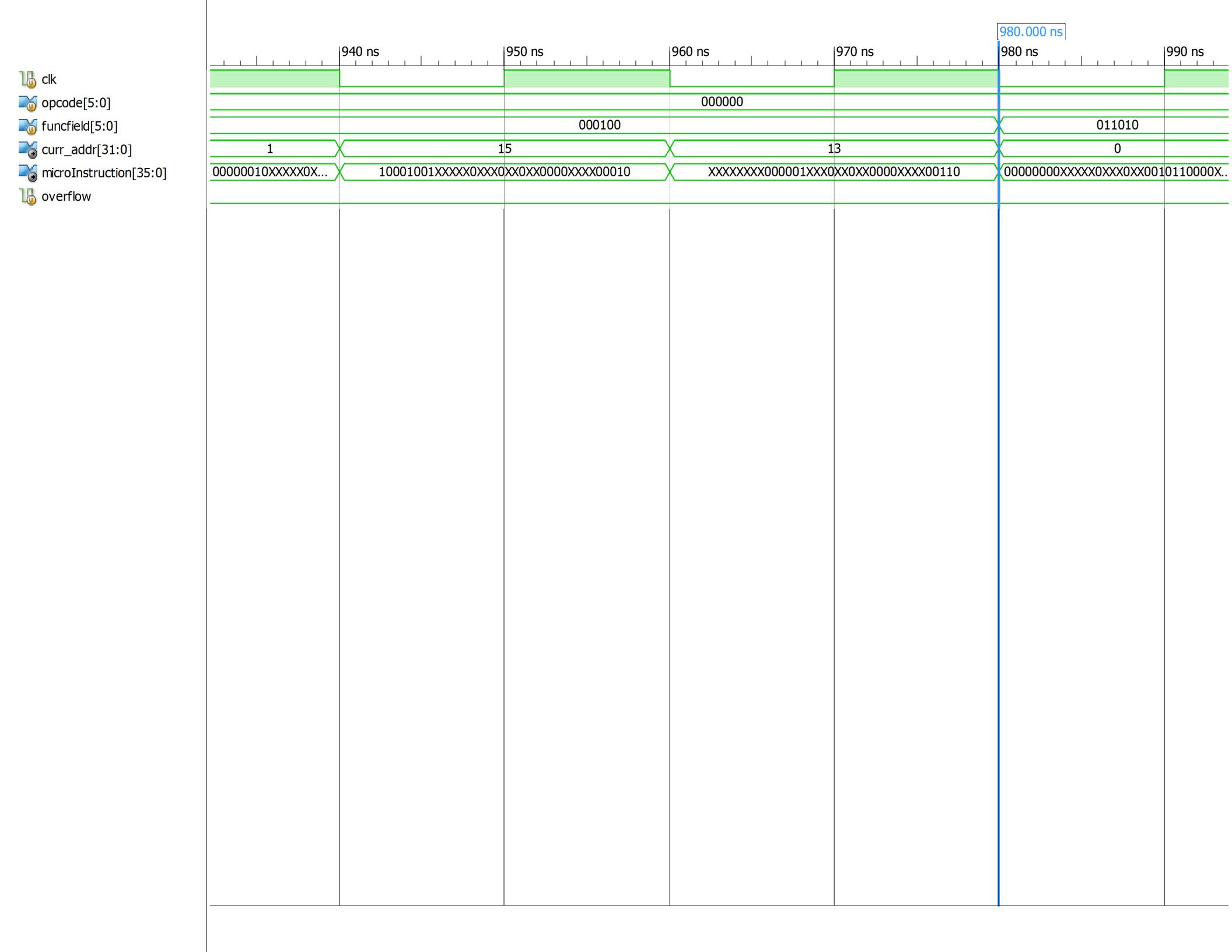




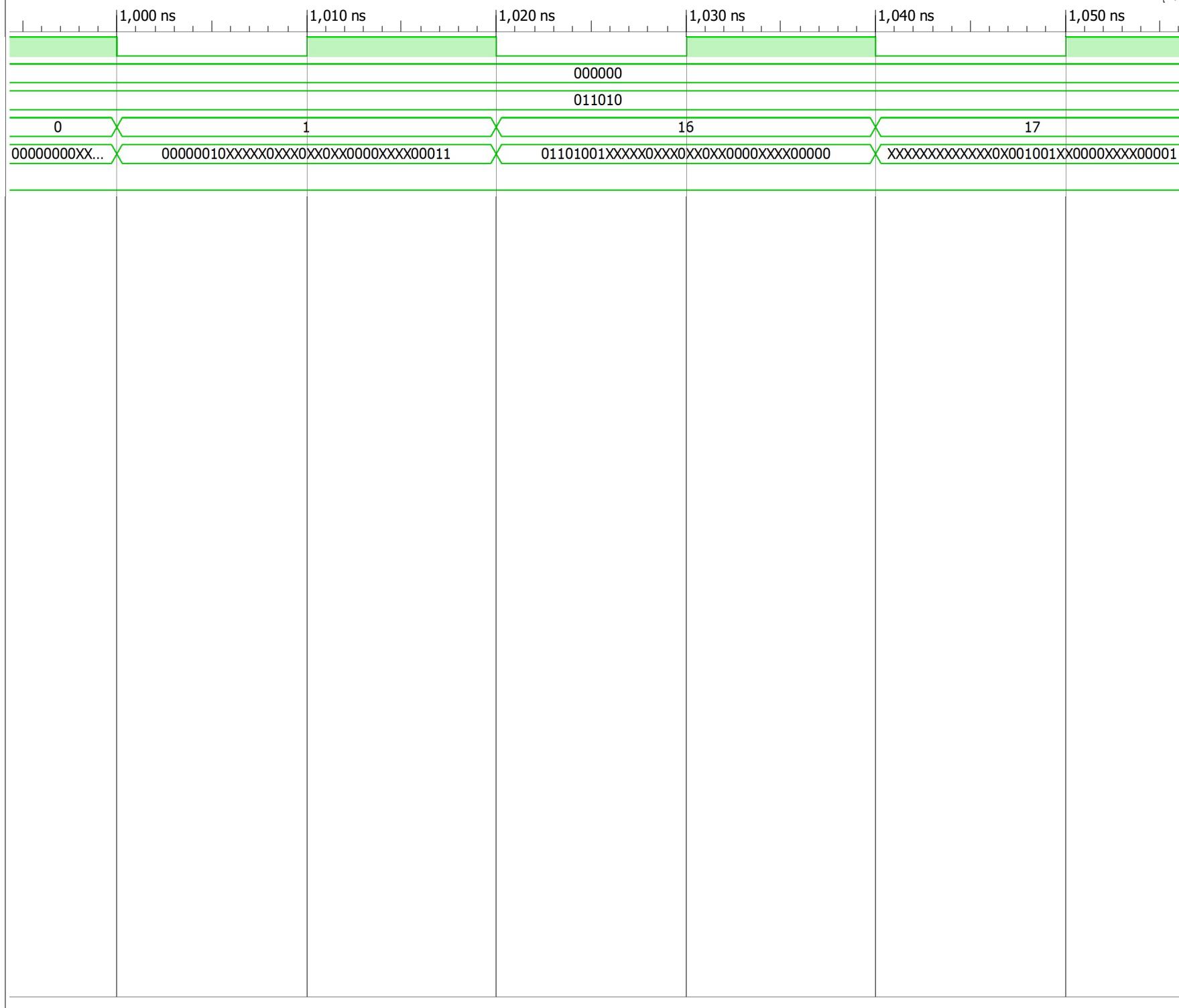




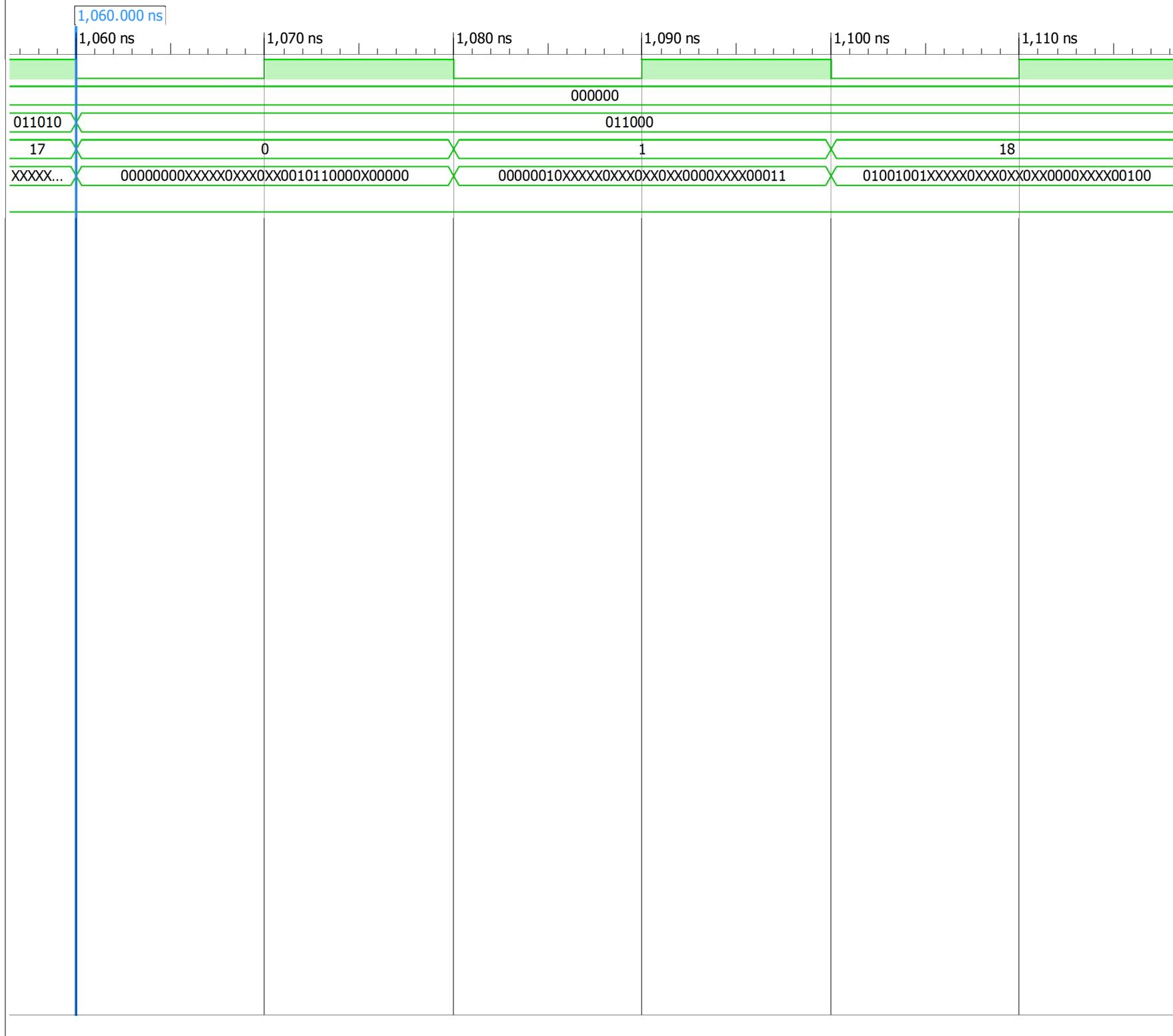


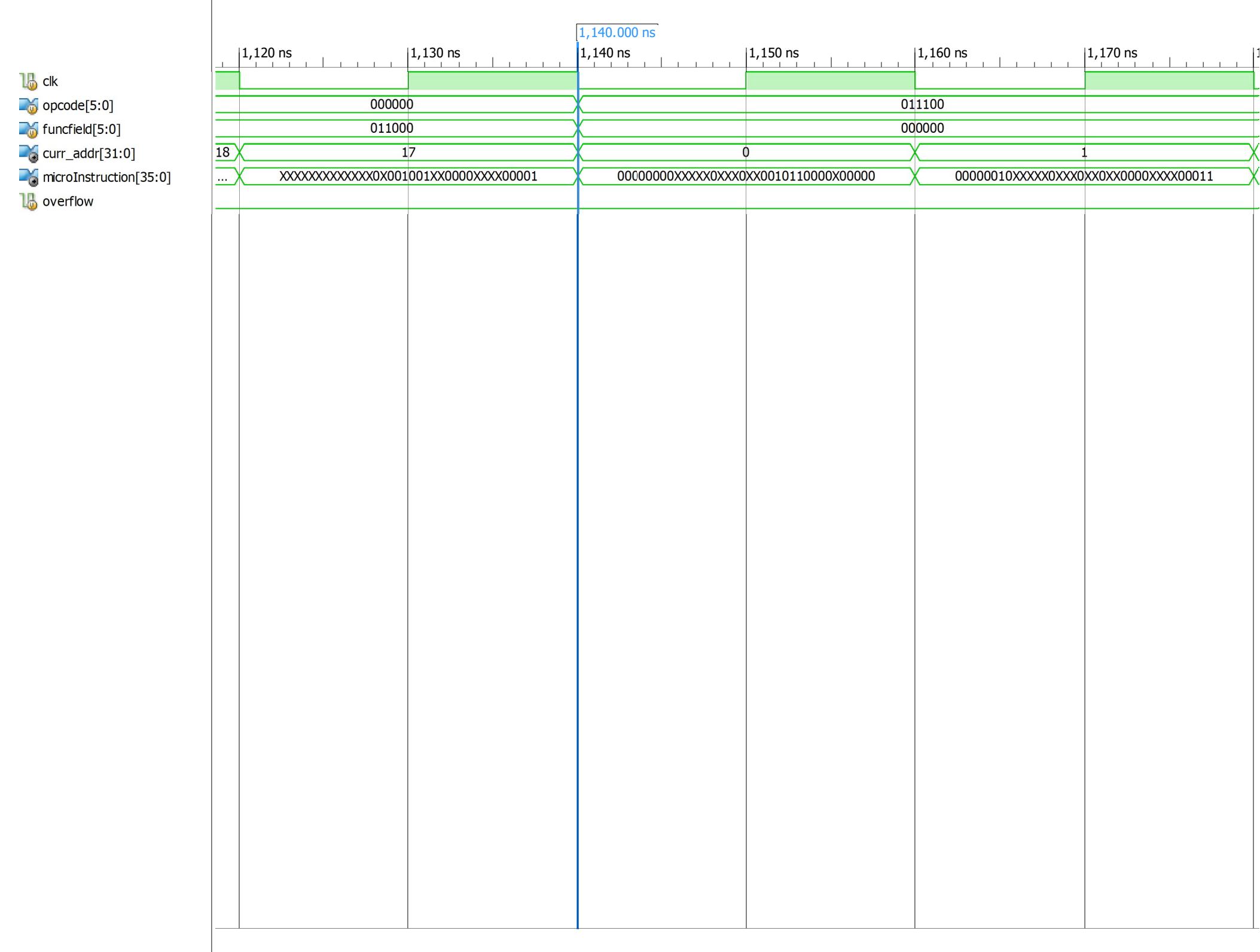


- clk
- opcode[5:0]
- funcfield[5:0]
- curr\_addr[31:0]
- microInstruction[35:0]
- overflow

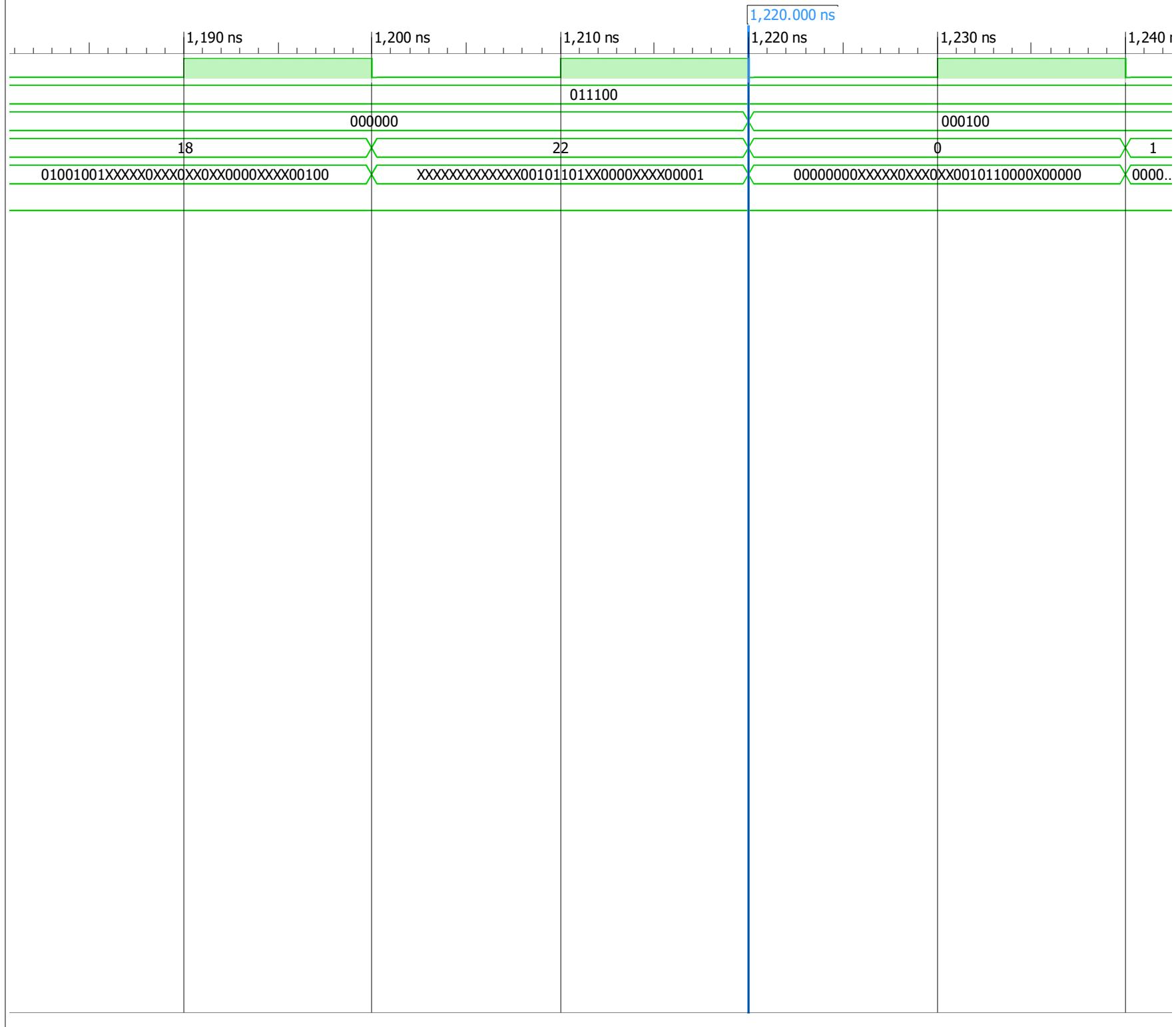


clk  
opcode[5:0]  
funcfield[5:0]  
curr\_addr[31:0]  
microInstruction[35:0]  
overflow



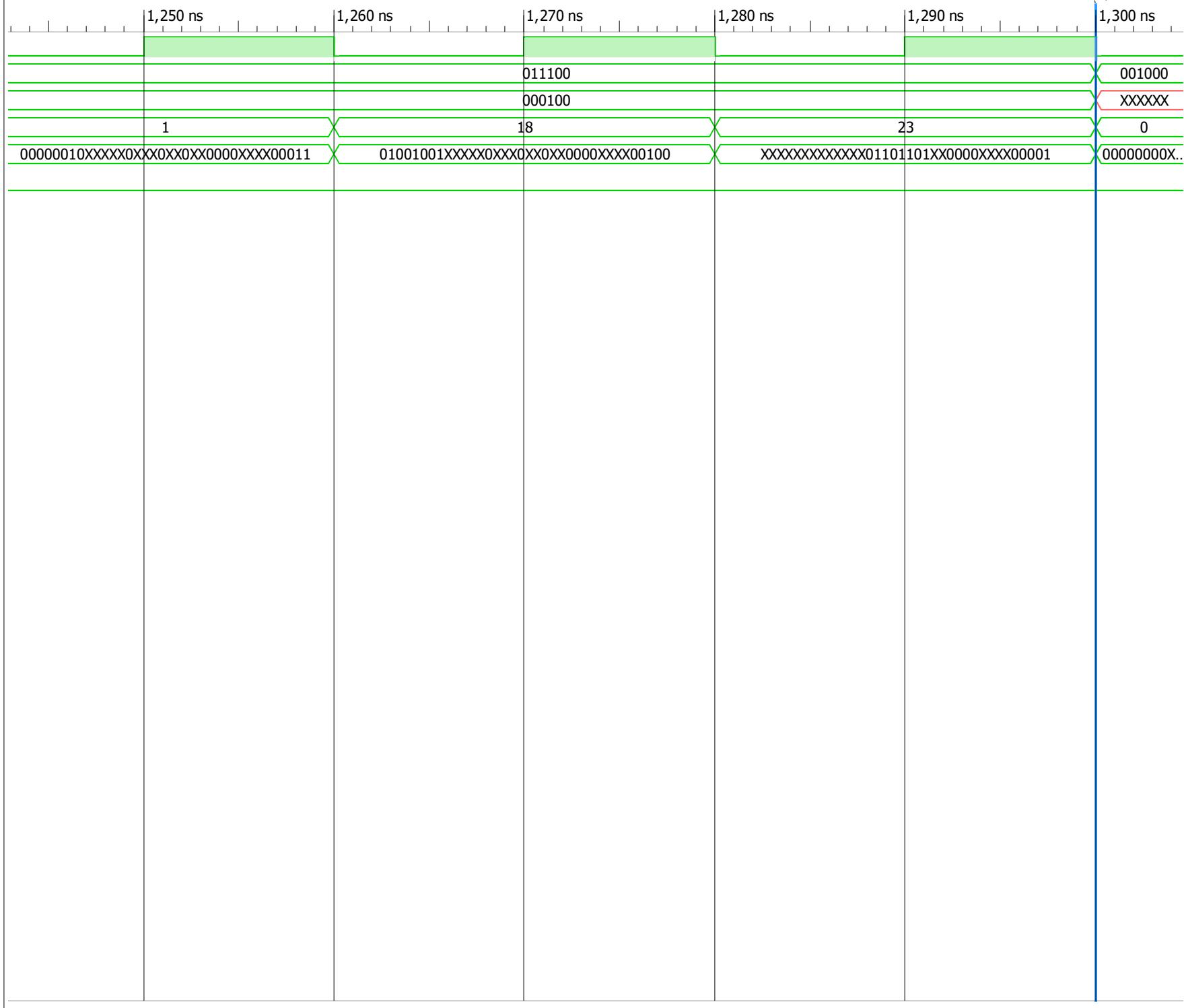


- clk
- opcode[5:0]
- funcfield[5:0]
- curr\_addr[31:0]
- microInstruction[35:0]
- overflow

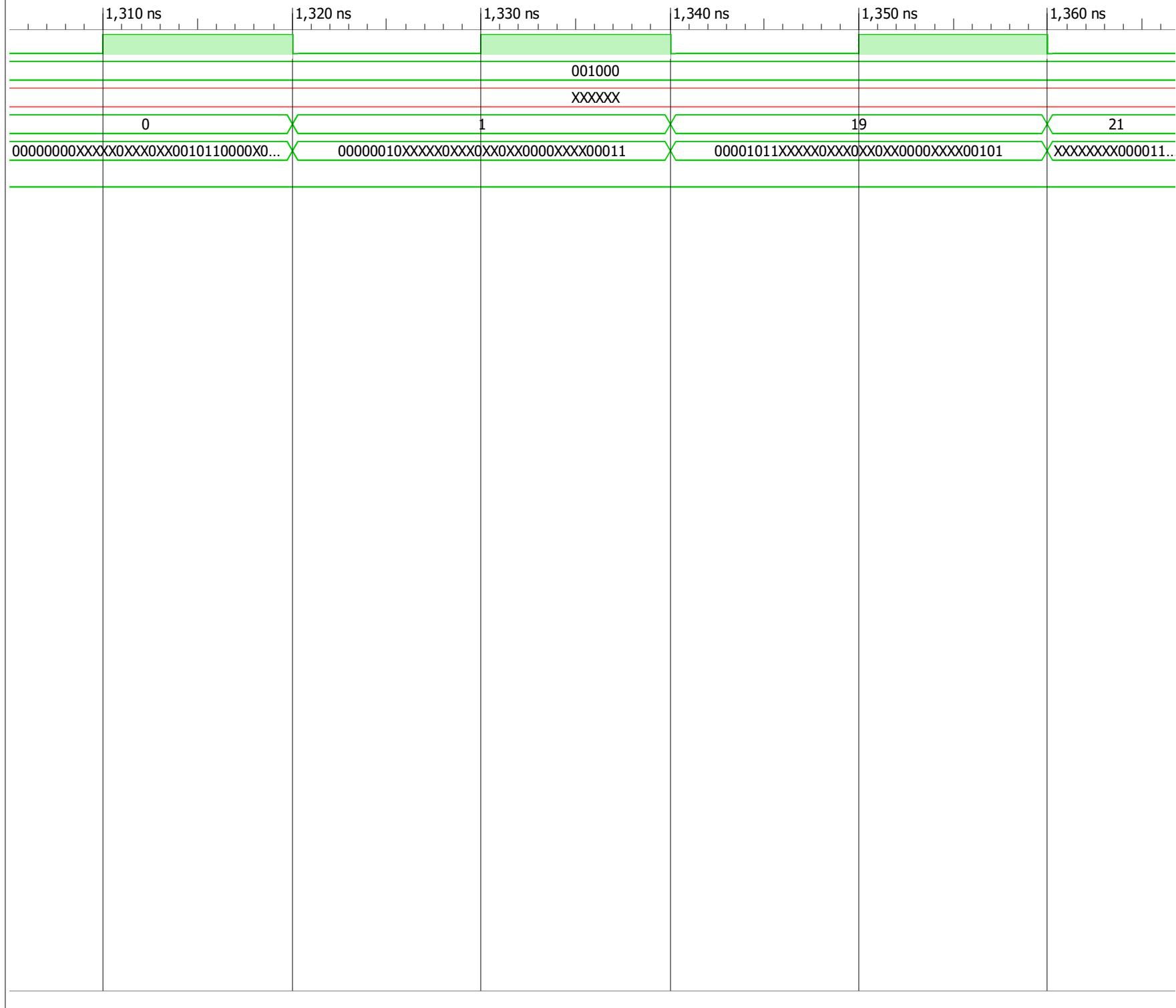


1,300.000 ns

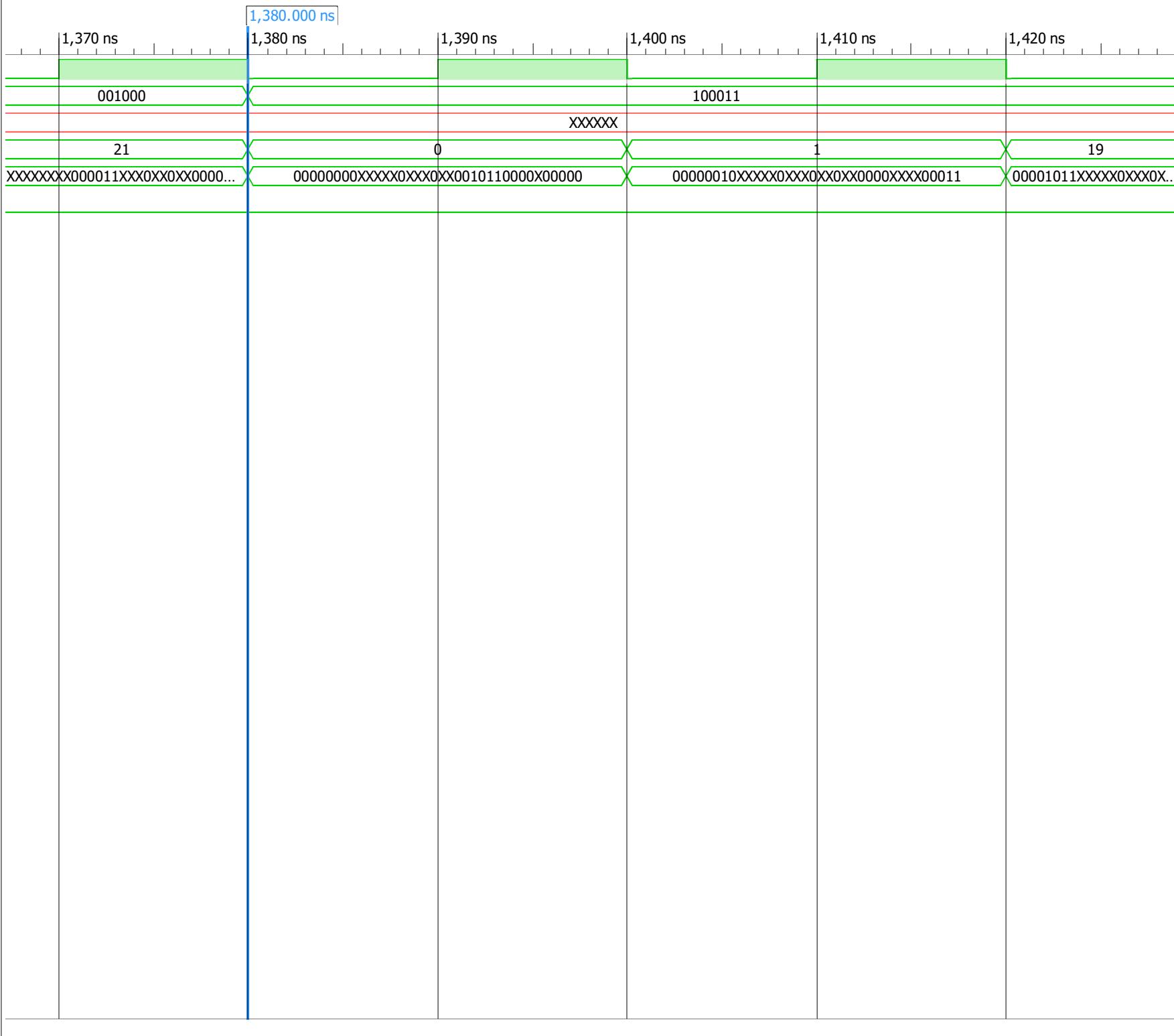
-  clk
-  opcode[5:0]
-  funcfield[5:0]
-  curr\_addr[31:0]
-  microInstruction[35:0]
-  overflow

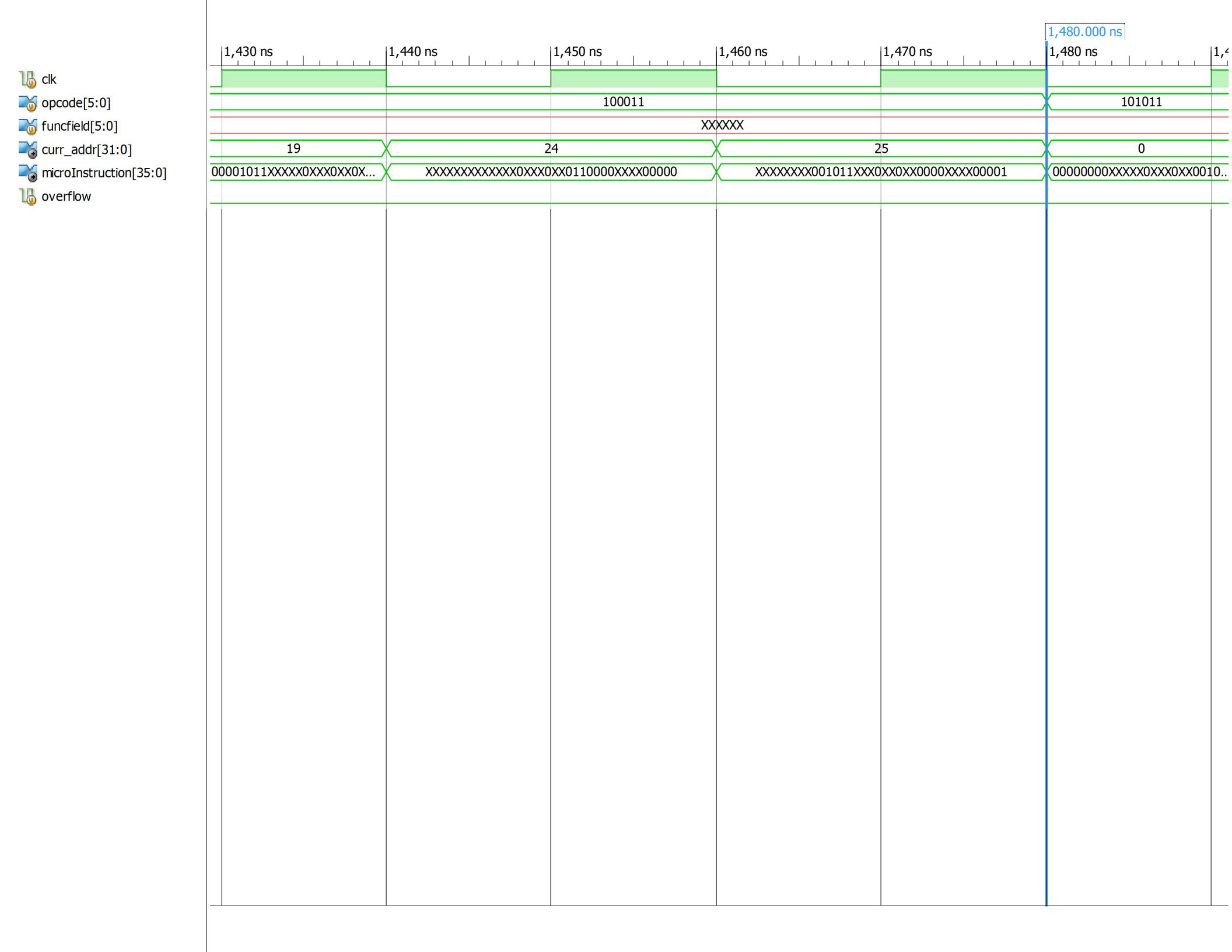


- clk
- opcode[5:0]
- funcfield[5:0]
- curr\_addr[31:0]
- microInstruction[35:0]
- overflow

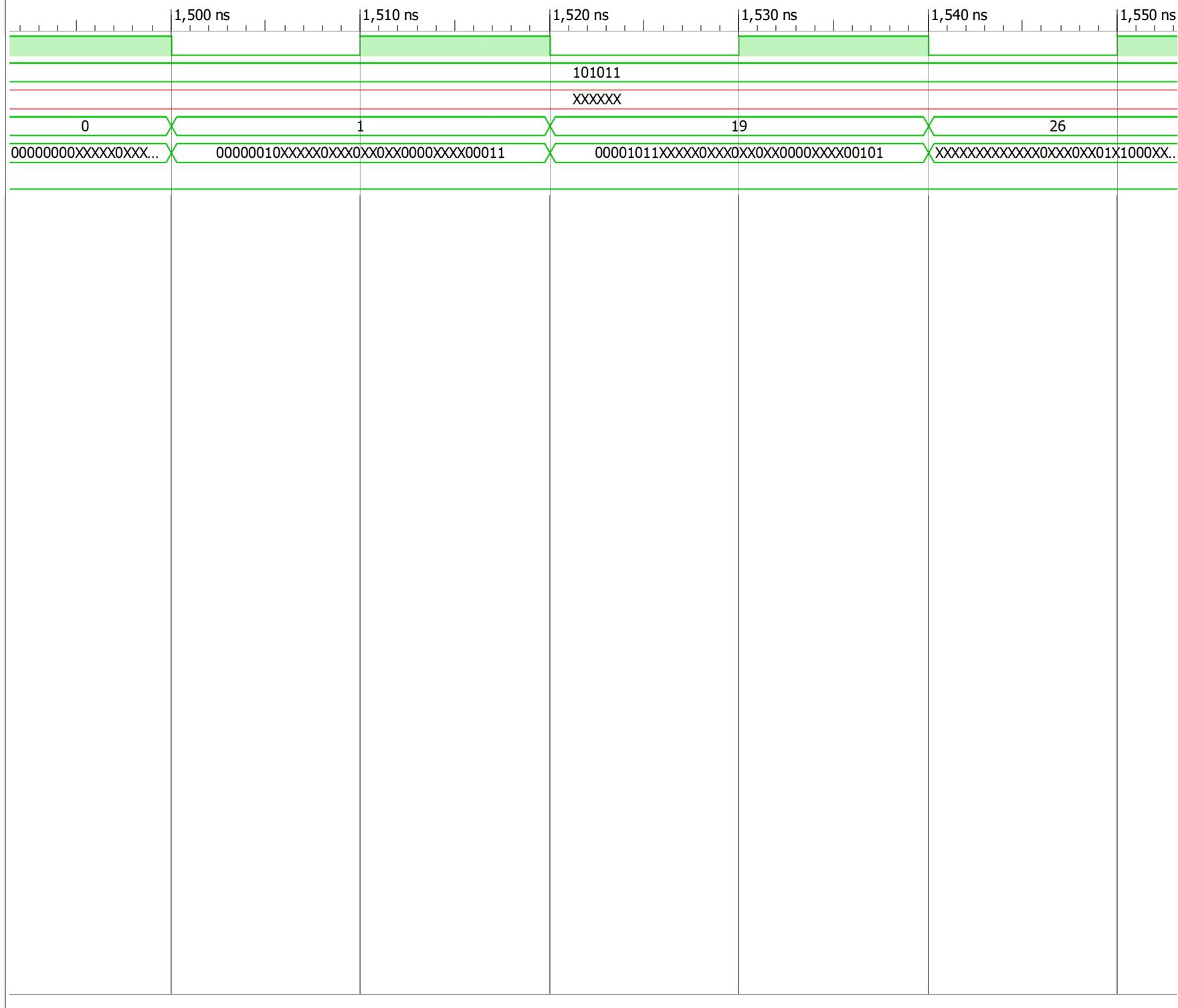


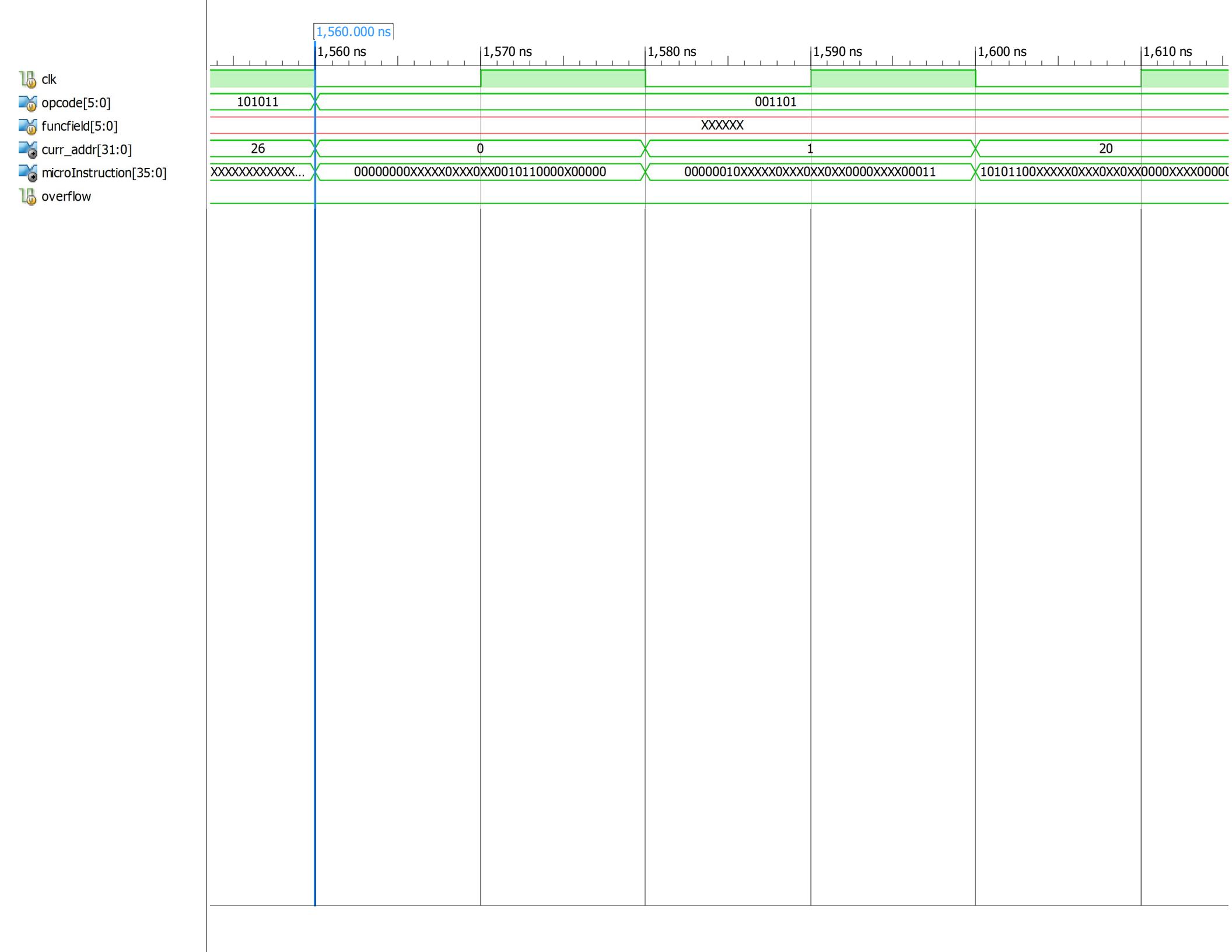
- clk
- opcode[5:0]
- funcfield[5:0]
- curr\_addr[31:0]
- microInstruction[35:0]
- overflow

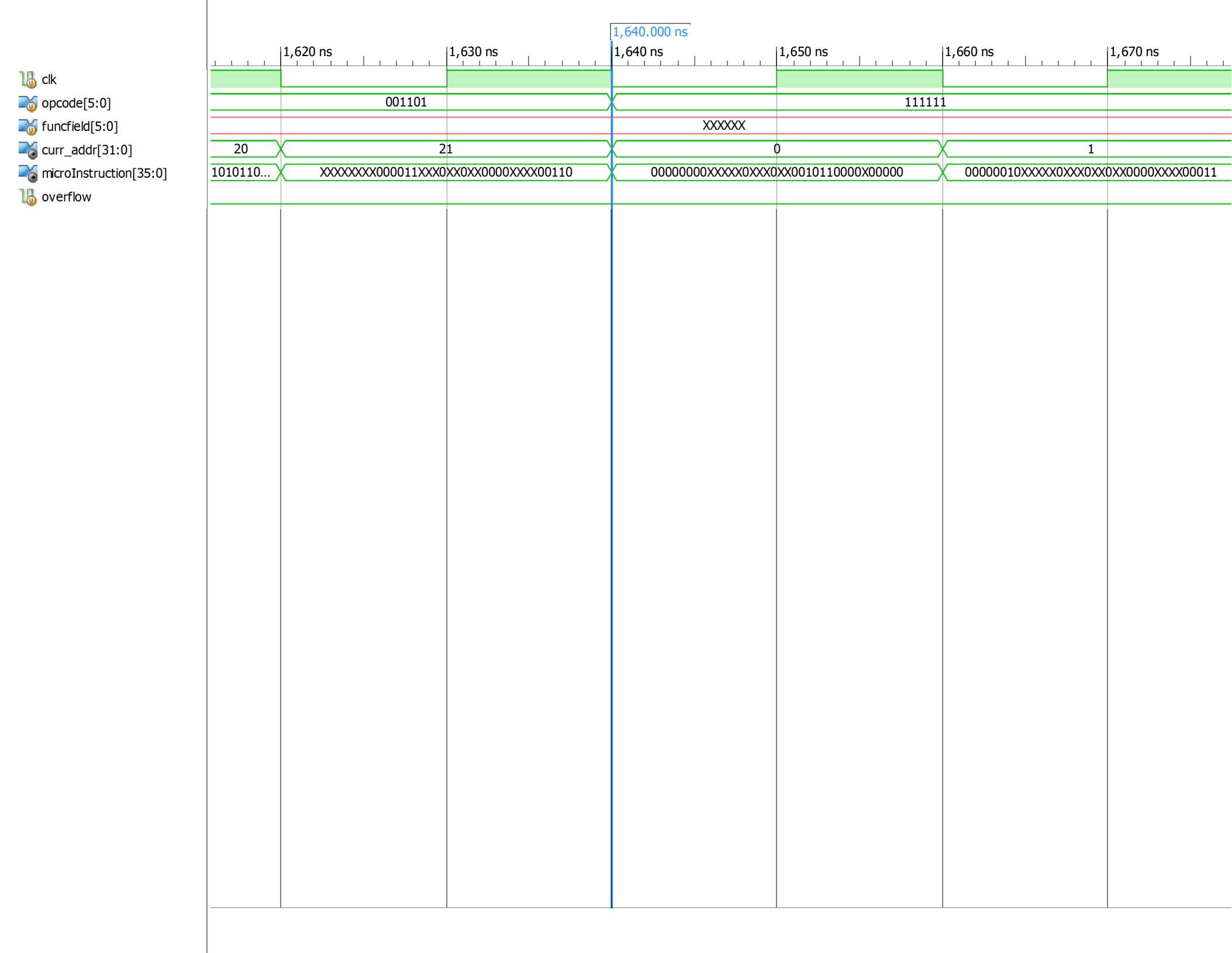




clk  
opcode[5:0]  
funcfield[5:0]  
curr\_addr[31:0]  
microInstruction[35:0]  
overflow







1,740.000 ns

- clk
- opcode[5:0]
- funcfield[5:0]
- curr\_addr[31:0]
- microInstruction[35:0]
- overflow

