Research on TSV Thermal-mechanical Reliability Based on Finite Element Analysis

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Abstract—Three-dimensional integrated packaging technology is recognized as the fourth generation packaging technology with the hope of breaking Moore's law. And through silicon via(TSV) technology is the key of three-dimensional packaging technology. In order to study the thermal-mechanical reliability of TSV structure, the finite element method was used to simulate the equivalent stress and deformation of TSV with different TSV size, aspect ratio, pitch and structure. The distribution of equivalent stress and deformation was obtained. The simulation results showed that the increase of TSV size would lead to the increase of equivalent stress and deformation, the aspect ratio of TSV would only affect deformation, and the increase of TSV pitch would lead to the decrease of equivalent stress and the increase of deformation. In addition, TSV filled with parvlene was analyzed in this paper. The stress could be effectively released by increasing the size of parylene.

Keywords—thermal-mechanical reliability; TSV; finite element simulation; equivalent stress; deformation

I. INTRODUCTION

Through silicon via (TSV) technology refers to the fabrication of through micro via on silicon wafers in micronano processing, so that the distance between the chip and the chip in the vertical direction is the shortest. TSV is the key technology of three-dimensional packaging technology [1-2]. TSV has significant advantages over other three-dimensional package interconnections such as wire bonding, and can be utilized to manufacture devices that are small, high-density, versatile, wide-bandwidth, and highly reliable [3-4]. TSV is a disruptive technology and is considered to be an effective way to achieve the "Super Molar Law", which is widely used in design and production of mainstream devices in the future [5].

However, in the TSV structure, the coefficient of thermal expansion of silicon is about 2.5x10⁻⁶ /°C, and the coefficient of thermal expansion of copper is about 17.5x10⁻⁶/°C [6]. Thermal-mechanical reliability refers to the ability of microelectromechanical devices to perform specified functions under specified thermal conditions and within specified time. Due to the large difference in thermal expansion coefficients between silicon and copper, large thermal stresses are generated when process temperatures change [7-9]. The residual stress accumulated in the process and the thermal stress in the structure are superimposed, resulting in swelling of the silicon material and delamination of copper and silicon

dioxide [10-11]. The thermal-mechanical reliability problem of TSV structure seriously affects the performance of the chip and limits the marketization process of TSV products. Therefore, many scholars at home and abroad have studied the reliability of TSV structure.

At present, academic research on the thermal-mechanical reliability of TSV structure mainly adopts the method of finite element simulation, and some people have carried out related experiments. J.H.Lau et al. analyzed the effects of different TSV pitch and aspect ratio on the stress and strain of silicon and copper by finite element simulation[12-13]. Choa et al. conducted an experimental study on thermal-mechanical reliability of an 8-layer multilayer TSV. It was found that the diameter of the TSV and the thermal expansion coefficient of the filler material have a significant effect on the thermomechanical stress [14]. Yu et al. established a wet-heat stress model to study the Hydrogen-based semi-siloxane based TSV wet stress and thermal stress distribution, and coupled the wetheat stress to the traditional silicon-based TSV [15]. An et al. established a two-dimensional model of the TSV adapter plate, and gave the stress analysis solution for the case where the through hole is completely filled with copper and partially filled with copper. It can be used to discuss the analysis of the TSV stress [16].

However, at present, the study of TSV size parameters does not fully analyze the stress and deformation on key sections, and does not fully consider the factors of size parameters and filler materials. In addition, there is not any suggestion that can effectively reduce the stress and strain of the TSV structure. Based on the finite element simulation, the effects of TSV size, aspect ratio and pitch on the equivalent stress and deformation were discussed. The relationship between the equivalent stress and deformation of the parvlene filled TSV size was further analyzed. The effective thermalmechanical reliability recommendations had certain guiding significance for TSV reliability design. The full text was divided into 5 sections, and the first section was the introduction. Subsequent chapters were arranged as follows: Section 2 established a finite element model based on reasonable assumptions and set simulation parameters. Section 3 discussed the equivalent stress, deformation distribution and critical path distribution of TSV simulation of different size, aspect ratio, and pitch. Section 4 analyzed the different structures and compares the effects of filling parylene and

changing the size of the filled parylene on stress and deformation. Section 5 summarized the full text, proposed thermal-mechanical reliability recommendations, and looked ahead to future research.

II. FINITE ELEMENT MODELING AND PARAMETER SETTING

In the actual simulation process, finite element analysis is sometimes difficult due to the complex structure of TSV, especially the obvious difference in scale. In order to facilitate the simulation, we propose the following assumptions: (1) Because the barrier layer and redistribution layer are very thin, the influence on the analysis structure can also be neglected, which is not considered in the simulation; (2) Because silicon and silicon dioxide do not reach the plastic condition temporarily under the thermal stress in the simulation process. only elastic deformation occurs, so it is assumed that only elastic deformation occurs in silicon and silicon dioxide,(3) In order to facilitate simulation, it is assumed that all materials are isotropic in the simulation process, which has little influence on the simulation results. Considering the symmetry of the TSV structure, in order to improve the simulation efficiency, a 1/8 axisymmetric model is used in the simulation process. The simplified model of TSV is shown in Figure 1, and the meshing is shown in Figure 2. Path A is the line from the center of the TSV to the edge of the upper bottom surface, and path B is the line from the center to the edge of the middle section face.

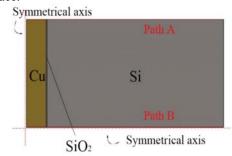
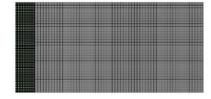


Figure 1. TSV simplified model.



(a) Key area meshing



(b) Overall meshing Figure 2. TSV meshing.

In order to study the influence of TSV size and aspect ratio on thermal-mechanical reliability, the TSV pitch is 200um, the TSV size is changed from 10um to 40um, and the aspect ratio is 3, 5, 7, and 10 respectively. In order to study the effect of TSV pitch on thermal-mechanical reliability, when the TSV size is 20µm and the aspect ratio is 5, the TSV pitch varies from 100 µm to 400 µm for simulation analysis. To investigate the effect of TSV-filled parylene on thermalmechanical reliability, we considered the simulation of hollow copper columns and parylene filling at a TSV size of 20 µm, an aspect ratio of 5, and a pitch of 200 µm. The size of the parylene in the parylene-filled TSV model is 5 µm, and the size of the hollow in the hollow copper-filled TSV is also 5 um. In order to study the influence of the size of parylene on the thermal-mechanical reliability, the TSV size is 20µm, the aspect ratio is 5, the pitch is 200 µm, and the parylene size is changed from 5µm to 20µm, respectively. The thickness of the silicon dioxide insulating layer between copper and silicon is $0.5 \mu m$. The temperature is ramped from -25 ° C to 125 ° C in the load. The TSV structural material parameters are shown in Table 1.

TABLE I. TSV STRUCTURAL MATERIAL PARAMETERS

Material	Density/(kg·m ⁻³)	Elastic Modulus /GPa	Poisson Ratio
Cu	8900	18.5	0.343
Parylene	1110	6.9	0.35
Si	2334	2.813@25□	0.28
		3.107@150□	
		3.61@117℃	
SiO2	2400	0.5	0.16

III. ANALYSIS OF SIMULATION RESULTS OF DIFFERENT SIZE PARAMETERS

A. Influence of different TSV size and aspect ratio on the equivalent stress distribution

When TSV size is $20\mu m$, the aspect ratio is 5, and the pitch is $200\mu m$, equivalent stress distribution contour is shown in Figure 3. The equivalent stress is mainly concentrated at the interface of copper, silicon dioxide and silicon, and is small on both copper and silicon substrates. Different TSV size and aspect ratio satisfy this law for the equivalent stress distribution contour.

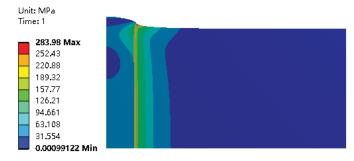
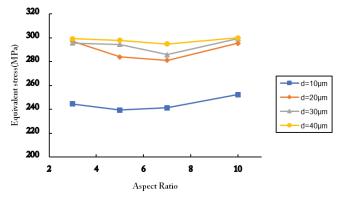
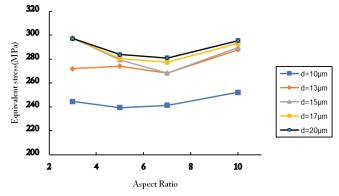


Figure 3. TSV equivalent stress distribution contour (deformation magnified 150 times).

Total equivalent stress with different TSV size and aspect ratio is shown in Figure 4. It can be seen from Figure 4 that the larger the TSV size, the larger the total equivalent stress, but the increase of the total equivalent stress gradually decreases with the increase of the size. When the TSV size is constant, the aspect ratio of the TSV is changed, and the total equivalent stress variation is small. Therefore, adjusting the TSV size is the main way to change the total equivalent stress of the TSV structure.



(a) Total Equivalent Stress of 10 µm -40µm TSV Size and Aspect Ratio.



(b) Total Equivalent Stress of $10\mu m$ - $20\mu m$ TSV Size and Aspect Ratio.. Figure 4. Total equivalent stress with different TSV size and aspect ratio.

When the TSV aspect ratio is 5, the equivalent stress with different TSV size on path A is shown in Figure 5. It can be seen from Figure 5 that on the upper bottom surface, the equivalent stress in the copper-filled TSV structure tends to decrease slightly as the distance from the center increases. The equivalent stress at the interface of the copper, silicon dioxide, and silicon substrate jumps to the highest, and thereafter the equivalent stress on the silicon substrate gradually decreases as the distance from the center increases. The larger the TSV size is, the greater the equivalent stress in each part of the upper bottom surface is.

When the TSV aspect ratio is 5, the equivalent stress with different TSV size on path B is as shown in Figure 6. It can be seen from Figure 6 that in the middle section face, the

equivalent stress in the copper-filled TSV structure hardly changes, and the variation of the center distance and the TSV size does not affect the equivalent stress, and the rest of the equivalent stress distribution similar to the upper base. The larger the TSV size, the greater the equivalent stress at the interface between copper and silicon, and the greater the equivalent stress on the silicon substrate.

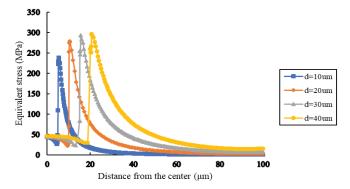


Figure 5. Equivalent stress with different TSV size on path A.

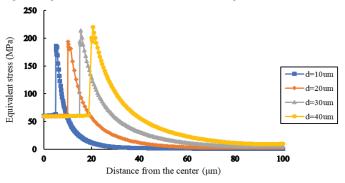


Figure 6. Equivalent stress with different TSV size on path B.

When the TSV size is $20\mu m$, the equivalent stress with different aspect ratio on path A is shown in Figure 7. It can be seen from Figure 7 that on the upper bottom surface, the images of different aspect ratio almost coincide, and the equivalent stress is not affected by the aspect ratio.

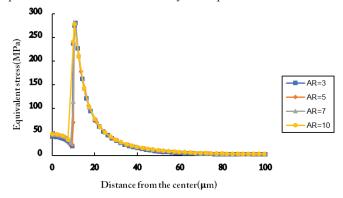


Figure 7. Equivalent stress with different aspect ratio on path A.

When the TSV size is $20\mu m$, the equivalent stress with different aspect ratio on path B is shown in Figure 8. It can be seen from Figure 8 that in the middle section face, when the TSVs have the same size and the aspect ratio of the TSV is not

less than 5, the equivalent stress on the path A are hardly affected by the aspect ratio. When the aspect ratio of the TSV is less than 5, the equivalent stress is low in the copper-filled TSV.

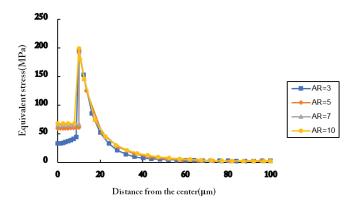


Figure 8. Equivalent stress with different aspect ratio on path B.

B. Influence of different TSV size and aspect ratio on deformation distribution

When TSV size is $20\mu m$, the aspect ratio is 5, and the pitch is $200\mu m$, TSV deformation distribution contour is shown in Figure 9. The deformation is larger on the copper-filled TSV, especially at the copper at the top of the TSV, and the smaller the deformation is toward the inside of the TSV. On the silicon substrate, except for the deformation of the contact with the silica, the deformation of the other portion increases as the center distance increases. Different TSV size and aspect ratio satisfy this law for deformation distribution.



Figure 9. TSV deformation distribution contour (deformation magnified 150 times).

The total deformation with different TSV size and aspect ratio is shown in Figure 10. It can be seen from Figure 10 that the larger the TSV size, the larger the total deformation; the larger the aspect ratio of the TSV, the larger the total deformation. Under the same TSV size, the aspect ratio of TSV has a certain linear relationship with the total deformation. The larger the TSV size, the greater the rate of change of total deformation. Adjusting the TSV size and aspect ratio can change the overall deformation of the TSV structure.

When the TSV aspect ratio is 5, the deformation with different TSV size on path A is as shown in Figure 11. It can be seen from Figure 11 that in the upper bottom surface, when the TSV size is larger than 10 μ m, the maximum value of the

deformation is at the center of the upper bottom surface, and thereafter the deformation is reduced as the distance from the center increases. At the interface of copper, silicon dioxide and silicon substrates, the deformation increases slightly with the distance from the center. When the TSV size is less than $10~\mu m$, the maximum value of the deformation is not at the center of the upper bottom surface, but at the edge of the silicon substrate.

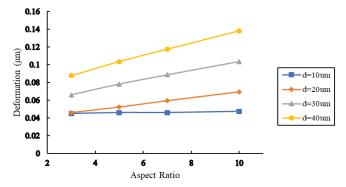


Figure 10. Total deformation with different TSV size and aspect ratio.

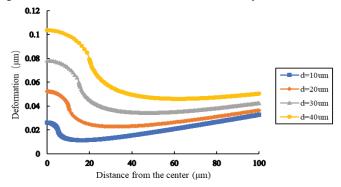


Figure 11. Deformation with different TSV size on path A.

When the TSV aspect ratio is 5, the deformation with different TSV size on path B is as shown in Figure 12. It can be seen from Figure 12 that the equivalent stress gradually increases with the increase of the center distance in the copper-filled TSV structure, reaching a maximum at the interface between copper and silicon, and thereafter the deformation continue to rise after a small decrease and reach a maximum at the edge of the silicon substrate. The larger the TSV size, the greater the deformation at the copper-silicon interface and the greater the deformation on the silicon substrate.

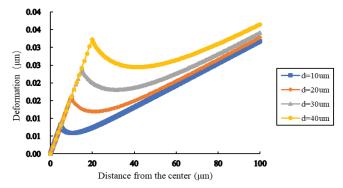


Figure 12. Deformation with different TSV size on path B.

When the TSV size is $20\mu m$, the deformation with different aspect ratio on path A is as shown in Figure 13. It can be seen from Figure 13 that the shape of the deformation curves of the different aspect ratio is similar when the TSVs are the same size on the upper bottom surface. The larger the aspect ratio, the larger the deformation.

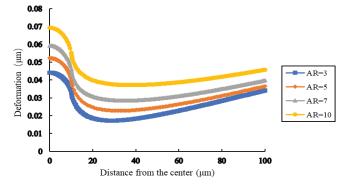


Figure 13. Deformation with different aspect ratio on path A.

When the TSV size is $20\mu m$, the deformation with different aspect ratio on path B is as shown in Figure 14. It can be seen from Figure 14 that when the TSV has the same size and the aspect ratio of the TSV is not less than 5, the deformation is hardly affected by the aspect ratio.

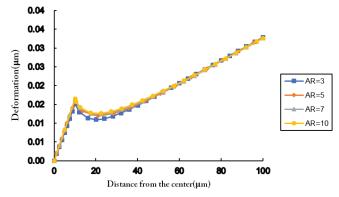
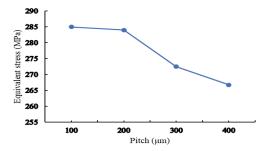


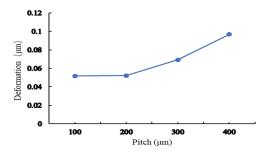
Figure 14. Deformation with different aspect ratio on path B.

C. Influence of different TSV pitches of equivalent stress and deformation

When the TSV size is $20\mu m$ and the aspect ratio is 5, the different TSV pitch of $100\mu m$ to $400\mu m$ are taken respectively, and the total equivalent stress and total deformation are shown in Figure 15. It can be seen that as the size and aspect ratio are constant, as TSV pitch increases, the total equivalent stress of the TSV structure decreases and the total deformation increases. When the TSV pitch is greater than $200\mu m$, the influence of TSV pitch on total equivalent stress and total deformation is obvious.



(a) Total equivalent stress with different TSV pitch.



(b) Total deformation with different TSV pitch.

Figure 15. Total equivalent stress and total deformation with different TSV pitch.

IV. ANALYSIS OF TSV SIMULATION RESULTS OF DIFFERENT STRUCTURES

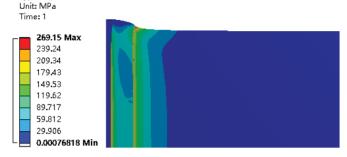
A. Analysis of different filled TSV simulation results

Total equivalent stress and total strain of different filled TSVs are shown in Table 2. It can be seen that the total equivalent stress of the TSV filled with parylene is smaller than the total equivalent stress of the copper-filled TSV structure, and the parylene-filled structure has certain advantages in thermal-mechanical reliability. The total deformation of the parylene and hollow copper filled TSV is slightly reduced relative to the copper filled TSV.

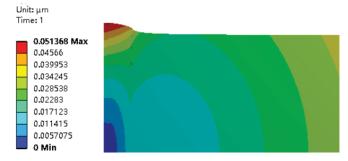
TABLE II TOTAL EQUIVALENT STRESS AND TOTAL STRAIN OF TSVS WITH DIFFERENT STRUCTURES

TSV material	Total Equivalent Stress/MPa	Total Deformation/μm
Copper filled TSV	283.98	0.52252
Parylene Filled TSV	269.15	0.51898
Hollow copper filled TSV	310.89	0.51368

The parylene-filled TSV equivalent stress distribution contour and deformation contour are shown in Figure 16. The equivalent stress increases significantly at the interface between parylene and copper at the interface with copper, silicon dioxide and silicon substrates. The deformation is similar to the copper-filled control group. There is a maximum at the center of the bottom surface of the TSV. On the silicon substrate, except for the deformation at the contact with the silicon dioxide, the deformation of the other part increases as the center distance increases.

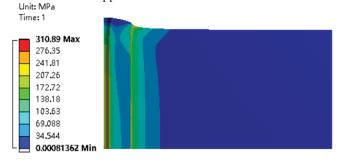


(a)Parylene equivalent TSV-filled stress distribution contour.

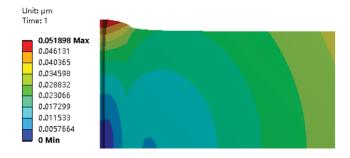


(b)Parylene equivalent TSV-filled deformation distribution contour. Figure 16. Parylene equivalent TSV-filled stress distribution contour deformation distribution contour (deformation magnified 150 times).

The hollow copper pillar filled TSV equivalent stress distribution contour and deformation distribution contour are shown in Figure 17. The equivalent stress is the highest on the inner side of the hollow copper column, and the junction of copper, silicon dioxide and silicon substrate is obviously higher, and the equivalent stress is the lowest at the silicon substrate away from the center. The deformation of the TSV filled with hollow copper pillars is similar to that of parylene-filled TSV and copper-filled TSV.



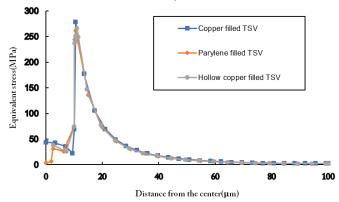
(a) Hollow copper filled TSV equivalent stress distribution contour.



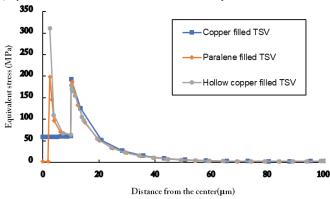
(b)Hollow copper filled TSV equivalent deformation distribution contour.

Figure 17. Hollow copper filled TSV equivalent stress distribution contour, deformation distribution contour (deformation magnified 150 times).

The equivalent stress with different materials on TSV path A and B is shown in Figure 18. It can be seen that on the upper bottom surface, the equivalent stress peak of TSV is at the junction of copper, silicon dioxide and silicon substrate, and the maximum equivalent stress of copper-filled TSV is the largest, and the hollow copper is filled with TSV, and parylene is the second. The fill TSV is minimal. This is because the coefficient of thermal expansion of parylene is less than the coefficient of thermal expansion of copper and acts as a stress release. On path B, the equivalent stress is smaller than the upper surface, and the equivalent stress of the hollow copper-filled TSV on the innermost side of the ring is as high as 310.89 MPa, which is noteworthy.



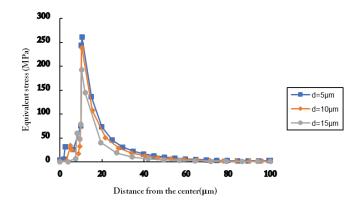
(a) Equivalent stress with different materials on TSV path A.



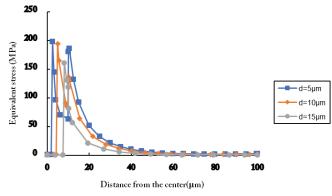
(b)Equivalent stress with different materials on TSV path B. Figure 18 Equivalent stress with different materials on TSV path A and B.

B. Analysis of simulation results of different size of parylene TSV

The equivalent stress with the different size of parylene TSV path A and B is shown in Figure 19. It can be seen that on the upper bottom surface, the equivalent stress peak of TSV is at the junction of copper, silicon dioxide and silicon substrate, and the larger the parylene size, the smaller the equivalent stress of TSV. When TSV size, aspect ratio, and pitch are determined, larger size parylene fill can be considered to improve TSV thermal-mechanical reliability.



(a) Equivalent stress with different size of parylene TSV on path A.



(b) Equivalent stress with different size of parylene TSV on path B. Figure 19. Equivalent stress with different size of parylene TSV on path A and B.

V. CONCLUSIONS AND PROSPECTS

In this paper, the effects of TSV size, aspect ratio, pitch and structure on TSV equivalent stress and deformation were discussed by using finite element simulation. The conclusions are as follows:

- (1) When the TSV aspect ratio is constant, the larger the TSV size, the larger the total equivalent stress and the larger the total deformation. When the TSV size is constant, the TSV aspect ratio is larger, the total deformation is larger, and the total equivalent stress is No significant changes.
- (2) The TSV size has a significant effect on the equivalent stress and deformation on the path A, but when the aspect ratio of the TSV exceeds 5, the stress and deformation are hardly affected by the aspect ratio.
- (3) After the size and aspect ratio are given, the pitch of the TSV is increased, the total equivalent stress of the TSV structure is reduced, and the total deformation is increased.
- (4) Parylene filled TSV can effectively reduce the equivalent stress, and the larger the size of parylene, the more obvious the effect, and the larger size parylene filling can be considered to improve the thermal-mechanical reliability of TSV.

The conclusions obtained in this paper have certain guiding significance for improving the thermal-mechanical reliability of TSV. However, this study still has shortcomings. On the one hand, only the equivalent stress was analyzed, and the radial stress, axial stress and shear stress which constitute

the equivalent stress were not separately studied; on the other hand, the research on parylene was not deep enough, only the size is considered. For the case of $20\mu m$, aspect ratio of 5, and pitch of $200\mu m$ TSV, there was a lack of comprehensive analysis of the equivalent stress and deformation of parylene size parameters for a variety of different TSV size parameters. The next step is to separately analyze the radial stress, axial stress and shear stress, and discuss the thermal-mechanical reliability of parylene filling under different TSV size parameters.

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REFERENCES

- J. H. Lau, Through silicon Vias for 3D integration. Beijing, Chemical Industry Press, 2014.
- [2] J. H. Lau, "Overview and outlook of through- silicon via (TSV) and 3D integrations, Mic'roelectronics International", vol. 28, 2011, pp. 8-22.
- [3] J. H. Liu, L. G. Liu, B. K. Ma, L. H. Deng and L. Han, "Dynamics Features of Cu-Wire Bonding During Overhang Bonding Process", IEEE Electron Device Letters, vol. 32, pp. 1731-1733, December 2011.
- [4] W. W. Shen and K. N. Chen, "Three-Dimensional Integrated Circuit (3D IC) Key Technology: Through-Silicon Via (TSV)", Nanoscale Res Lett, 2017, pp.56.
- [5] W. Zhao and Z. Y. Tong, "3D-TSV Technology An Effective Way to Continue Moore's Law" in Chinese, Special Equipment for the Electronics Industry, vol.3, 2011, pp.10-17.
- [6] Y. Dai, M. Zhang, F. Qin, P. Chen and T. An, "Effect of silicon anisotropy on interfacial fracture for three dimensional through-siliconvia (TSV) under thermal loading", Engineering Fracture Mechanics, vol. 209, 2019, pp.274-300.
- [7] D. H. Jung, Y. Kim, J. J. Kim, H. Kim, S. Choi, Y. H. Song et al., "Through Silicon Via (TSV) Defect Modeling, Measurement, and Analysis", IEEE Transactions on Components, Packaging and Manufacturing Technology, vol. 7(1), pp.138-152, 2017.
- [8] F. Qin, W. Wang, L. J. Wan, D. Q. Yu, L. Q. Cao and W. H. Zhu, "Review of thermomechanical reliability of TSV structures" in Chinese, Semiconductor Technology", vol.37, 2012, pp.825-831.
- [9] Y. Pan, F. Li, H. He, J. Li and W. Zhu, "Effects of dimension parameters and defect on TSV thermal behavior for 3D IC packaging", Microelectronics Reliability, vol.70, 2017, pp. 97-102.
- [10] C. S. Selvanayagam, J. H. Lau, X. W. Zhang, S. K. W. Seah, K. Vaidyanathan and T. C. Chai, "Nonlinear Thermal Stress/Strain Analyses of Copper Filled TSV (Through Silicon Via) and their Flip-Chip Microbumps", Electronic Components and Technology Conference, pp.1073-1081, 2008.
- [11] N. Khan, V. S. Rao, S. Lim, W. Ho Soon, V. Z. Lee, J. H. Lau et al., "Development of 3-D Silicon Module With TSV for System in Packaging", IEEE Transactions on Components and Packaging Technologies, vol. 33(1), pp.3-9, March 2010.
- [12] X. Liu, Q. Chen, P. Dixit, R. Chatterjee, R. R. Tummala and S. K. Sitaraman, "Failure mechanisms and optimum design for electroplated copper through-silicon vias", Electronic Components and Technology Conference, Atlanta, pp.624-630, 2009.
- [13] X. Liu, Q. Chen, V. Sundaram, M. Simmons-Matthews, K. P. Wachtler, R. R.Tummala and S. K. Sitaraman, "Thermo-mechanical behavior of

- through silicon vias in a 3D integrated package with inter-chip microbumps", Electronic Components and Technology Conference, pp.1090-1095, 2011.
- [14] S. Choa, J. Y. Choi, C. G. Song and H. S. Lee, "Study of thermomechanical reliability of TSV for 8-layer stacked multichip ", International Conference and Exhibition package, Scottsdale and Fountain Hills AZ, United States, pp.1549-1581, 2010.
- [15] J. F. Yu and W. G. Jiang. "Wet-thermal stress finite element analysis of through-silicon via interconnects" in Chinese, Reliability, vol37, 2012, pp.889-894.
- [16] Z. An, F. Qin, W. Wu, D. Q. Yu, L. J. Wan and W. Wang, "Thermal Stress Analysis of Through Holes in TSV Adapter Plates" in Chinese, Engineering Mechanics, vol.30, 2013, pp.262-269.